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DOI

[10.3390/aerospace12100858](https://doi.org/10.3390/aerospace12100858)

Publication date

2025

Document Version

Final published version

Published in

Aerospace

Citation (APA)

Uludag, M. S., & Aslan, A. R. (2025). Power Contingency/Margin Methodology and Operational Envelope Analysis for PlanarSats. *Aerospace*, 12(10), Article 858. <https://doi.org/10.3390/aerospace12100858>

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Article

Power Contingency/Margin Methodology and Operational Envelope Analysis for PlanarSats

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Abstract

This paper presents a power-centric systems-engineering approach for PlanarSats and for atto-, and femto-class spacecraft where surface-limited power dominates design. We review agency practices (The National Aeronautics and Space Administration (NASA), European Space Agency (ESA), Japan Aerospace Exploration Agency (JAXA)) and the American Institute of Aeronautics and Astronautics (AIAA) framework, then extend them with refined low-power subcategories and a log-linear method for selecting phase- and class-appropriate power contingencies. The method is applied to historical and conceptual PlanarSats to show how contingencies translate into required array area, allowable incidence angles, and duty cycle, linking power sizing to geometry and operations. We define the operational power envelope as the range of satellite orientations and conditions under which generated power meets or exceeds mission requirements. Consistent with agency guidance, sizing is performed to the maximum expected value (MEV) (CBE plus contingency); when bounding or stress analyses are needed, we report the maximum possible value (MPV) (Maximum Possible Value) by applying justified system-level margins to the MEV. Results indicate that disciplined, phase-aware contingency selection materially reduces power-related risk and supports reliable, scalable PlanarSat missions under severe physical constraints.

Keywords: PlanarSat; Attosat; Femtosat; small satellite; ChipSat; systems engineering

Academic Editor: Hyun-Ung Oh

Received: 21 August 2025

Revised: 11 September 2025

Accepted: 14 September 2025

Published: 24 September 2025

Citation: Uludağ, M.Ş.; Aslan, A.R. Power Contingency/Margin Methodology and Operational Envelope Analysis for PlanarSats. *Aerospace* **2025**, *12*, 858. <https://doi.org/10.3390/aerospace12100858>

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1. Introduction

Satellite design is fundamentally governed by four constraints: mass, volume, power, and data. Mass is dictated by launch cost, volume by available fairing size, power by the satellite's ability to generate and store energy, and data by mission objectives. Standardized form factors such as CubeSats and PocketQubes have enabled cost-effective access to space by imposing mass and volume limits; for example, 2 kg per 1U CubeSat and 0.25 kg per 1P PocketQube [1,2].

As the miniaturization of satellites continues, new architectures such as PlanarSats have emerged. PlanarSats are flat satellites with a surface area far larger than their thickness, where the challenge of generating sufficient power on a limited surface becomes dominant. Despite their unique geometry, PlanarSats must provide all core satellite functionalities, though subsystem scaling and layout differ from conventional satellites to fit the planar form factor [3].

A clear, structured approach for PlanarSat system development is not yet available in the literature [3]. As reviewed in [3], the PlanarSat concept is introduced alongside a

survey of atto- and femto-satellites, including their dimensions, missions, power, and mass. Kanavouras et al. propose an agile systems engineering methodology for sub-CubeSat spacecraft such as femto- and atto-satellites, enabling rapid development cycles with reduced cost and increased flexibility compared to traditional approaches [4,5]. Ekpo and George introduce a deterministic, multifunctional architecture for highly adaptive small satellites, facilitating reconfigurable designs that yield significant mass and power savings [6,7]. Although these works do not focus specifically on PlanarSats, they provide general, parametric approaches to small satellite design, incorporating “mass” as a key variable based on textbook references [8,9].

The critical challenge for PlanarSats, as for other highly miniaturized platforms, is that as satellites shrink, the available surface area for solar cells decreases rapidly, yet all essential functionalities (processing power, data storage, communication, power systems, payload) must still be included. Literature consistently shows that power budgeting errors are a leading cause of mission failures in small satellites [10–12], underscoring the need for a design methodology that prioritizes power from the outset [13,14]. Recent peer-reviewed studies provide detailed examples of power system analysis in current CubeSat missions: Acero et al. (2023) describe a scenario-based approach to electrical power system (EPS) validation, integrating subsystem modeling, operational planning, and margin allocation using real mission data [12]; Kerrouche et al. (2022) document the design and verification of a low-cost CubeSat EPS, including margin practices and lessons from hardware-in-the-loop testing [15] and Tadanki and Lightsey (2019) present practical subsystem-level power budgets and engineering trade-offs from university-built CubeSats [16]. These studies show how explicit, scenario-driven EPS sizing and contingency selection are applied in practice; an approach we extend to PlanarSats.

One key advantage of PlanarSats is their simple, two-dimensional geometry, which allows direct, empirical analysis of power generation and consumption. Leveraging established guidelines from organizations such as AIAA, NASA, and ESA, it is possible to build a more systematic and robust design process, including the integration of recommended contingencies and margins at every stage of design to ensure mission success.

PlanarSats, by definition, are essentially two-dimensional satellites. Especially at the atto- and femto-satellite levels, they tend to be a single substrate; most often a single printed circuit board (PCB) [3]. Power generation depends on solar cells that directly occupy some of the available surface. Depending on solar-cell technology and efficiency, the required area varies and can limit the surface available for other functions. Conversely, to provide sufficient power, the satellite may need to be sized up to accommodate more solar cells, which, in turn, increases the area available for other functionalities.

Extreme miniaturization consolidates all essential subsystems, including power, communication, data handling, attitude control, and payload, onto a single substrate (typically a single PCB), with careful allocation of both surface area and power. Figure 1 shows that as satellites shrink, required functionalities remain while the available power-generation area diminishes, forcing trade-offs between size, capability, and operational robustness.

Figure 2 illustrates three foundational PlanarSat configurations based on the allocation of solar cells and electronics: (a) separated; solar cells on one side, electronics on the other; (b) half-mixed; solar cells on one side and a mix of solar cells and electronics on the other and (c) mixed; both sides carry a combination of solar cells and components. These layouts directly influence power generation, orientation sensitivity, and subsystem integration.

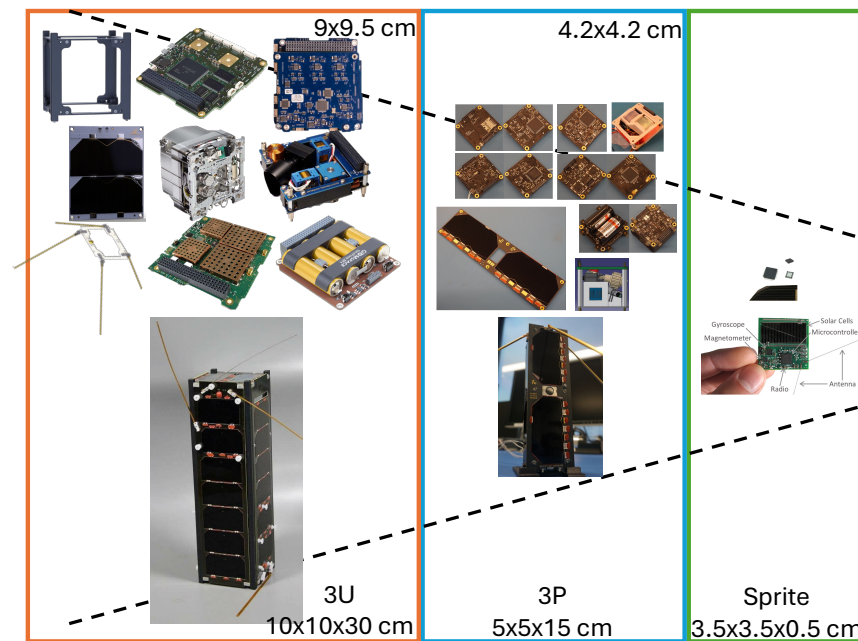


Figure 1. Subsystems and functions for CubeSats (orange), PocketQubes (blue), and PlanarSats (green). Systems are getting smaller, but required functionalities remain the same. Dimensions of the example satellites are given in the bottom right of the respective boxes; in the top right is the approximate footprint of a subsystem for each satellite [17–25].

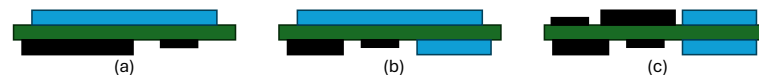


Figure 2. Basic architecture of PlanarSats. Colors represent: PCB, solar cells, and integrated circuits. (a) separated; solar cells on one side, electronics on the other; (b) half-mixed; solar cells on one side and a combination of solar cells and electronics on the other; and (c) mixed; both sides carry a combination of solar cells and components.

This study develops a structured, power-driven methodology for atto- and femto-class PlanarSats that adapts NASA/ESA/AIAA contingency/margin philosophy to the surface-constrained regime. The framework supplements—rather than replaces—agency guidance, and is demonstrated on both real and conceptual missions. Key elements include a formal operational power envelope (OPE) to make the reliability implications of power sizing explicit, refined low-power categories with contingency scaling as a transparent extension of existing standards, and a mission-adjusted value that scales the MEV by mission-specific factors (e.g., tumbling incidence, conversion efficiency) to reflect operational uncertainty.

2. Power-Based Satellite Design

The PlanarSat form factor constrains available surface area for both power generation and electronics. A power-centric design process, therefore, enables a direct, iterative development flow that ties geometry to capability.

In this work, sizing proceeds bottom-up: the process begins from power requirements and surface-area constraints, together with known subsystem needs, and builds the overall design accordingly. For PlanarSats and other ultra-small satellites, limited heritage reduces the reliability of traditional top-down methods common for larger spacecraft. Consequently, this paper derives power subcategories and contingency guidance tailored to these emerging classes.

Contingency factors are considered at each design phase, with iterative updates as maturity increases. To ground the methodology, definitions and calculation steps are provided

in Tables 1 and 2, while agency practices from ESA, NASA, and JAXA are summarized in Table 3. These practices vary with design maturity, product evolution, and risk, and are tracked at key reviews (Preliminary design review (PDR), Critical design review (CDR), Post-shipment review (PSR)). In parallel, AIAA provides phase- and category-dependent guidance for power contingencies (Table 4).

Terminology note: agencies differ in nomenclature. For cross-agency comparison in this paper, values in Table 3 are interpreted using the definitions in Table 1: percentages applied to current best estimate (CBE) are treated as contingency (contributing to MEV), while any additional system-level reserves above MEV are treated as margin (defining MPV), when explicitly stated by the source.

Table 1. Summary of contingency and margin features for system analysis.

Feature	Contingency	Margin
Applied to	CBE (estimated demand/cost)	Capability above MEV (demand + contingency)
Covers	Known unknowns, estimating error, expected growth	Unknown unknowns, robustness, worst-case bounds
Management	Subsystem/team level	Project/program level
Trend	Burns down with maturity	Remains positive as design buffer

Table 2. Summary of CBE, MEV, and MPV for power system analysis.

Term	What It Means	How to Calculate	Typical Use
CBE	Best estimate (today's design)	Engineer's calculated value	Baseline, preliminary analysis
MEV	Maximum expected value (with contingency)	$CBE \times (1 + \text{contingency } \%)$	Power system sizing
MPV	Maximum possible value (with margin)	$MEV \times (1 + \text{margin } \%)$	Stress/bounding, risk assessment

2.1. Margin and Contingency: Agency and Textbook Definitions

The terms margin and contingency are central to spacecraft power budgeting but differ in meaning and application across agencies and references. In this paper, the following working definitions are used, consistent with agency usage: Contingency is a percentage uplift applied to the CBE to cover known unknowns, estimating uncertainty, and expected growth; Margin is capability retained above MEV to provide robustness and worst-case bounds. Table 1 distills the role of contingency versus margin as used in this paper.

NASA distinguishes contingency (growth allowance) from margin. Contingency is applied to the CBE; CBE+contingency is the MEV. Recommended contingency decreases with maturity (e.g., 30–40% early, 10–20% at PDR, 5–10% at CDR), Table 3. For power systems, NASA recommends sizing to MEV and avoiding additional margin to prevent “pile-up” (Figure 3) [26,27].

ESA often uses margin broadly, encompassing uncertainty allowances and reserves. Typical system-level values start near 20% in early phases and are reduced at CDR (Table 3). ESA guidance explicitly warns against double counting across subsystem and system levels [28].

AIAA specifies both contingency and margin for power systems [29,30]. Contingency is a multiplicative uplift on CBE (e.g., 1.1–1.3 initially, tapering with maturity), applied at the subsystem level and burned down as knowledge grows. A separate system margin (typically 5–20%) is reserved for bounding or stress tests and is not stacked on top of contingency for routine sizing. In most cases, sizing is performed to the MEV; MPV (which includes margin) is used for worst-case bounds.

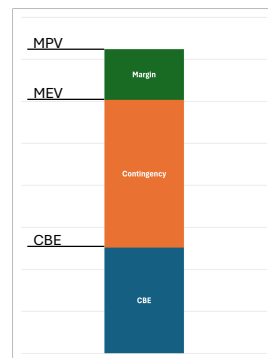


Figure 3. Contingency and margin terminology and how they combine. Adapted from [26].

Space Mission Analysis and Design (SMAD) defines margin as excess capability relative to requirement and contingency as a percentage added to early estimates. Both are emphasized early and reduced with maturity [31]. **Elements of Spacecraft Design (ESD)** follows a similar process but may report a fraction-of-capability “margin,” which can appear smaller if not clearly defined [9].

Table 2 summarizes the computation pathway (CBE → MEV → MPV) adopted in this work. For sizing in this paper, contingency is applied to each subsystem’s CBE, results are summed to obtain the MEV, and the power system is sized to MEV. Additional system-level margin (MPV) is used only for bounding or explicit mission requirements, avoiding double counting.

Table 3. Power margins by agency and project phase.

Agency	Project Phase	Power Margin	Report Timing	Reference(s)
ESA	Equipment Level (Off-The-Shelf A/B)	$\geq 5\%$	During design and development	[32–34]
ESA	Equipment Level (Off-The-Shelf C)	$\geq 10\%$	During design and development	[32–34]
ESA	Equipment Level (New Design/Major Modification D)	$\geq 20\%$	During design and development	[32–34]
ESA	System Level (General)	$\geq 20\%$ of nominal power	Throughout project lifecycle	[32,34]
ESA	System Level (IOD CubeSat at PDR)	20%	At Preliminary Design Review (PDR)	[32,34]
ESA	Pre-PDR	20%	Before Preliminary Design Review (PDR)	[32,34]
ESA	Pre-CDR	10%	Before Critical Design Review (CDR)	[32,34]
ESA	At PDR	5–15%	Preliminary Design Review (PDR)	[32,34,35]
ESA	At CDR	5–15%	Critical Design Review (CDR)	[32,34,35]
NASA	Phase B (Formulation)	$\sim 30\%$	During Preliminary Design Phase	[36–38]
NASA	Initial Design (SMAD Recommendation)	25%	Initial design phase	[36,38]
NASA	Historical Average (Aerospace Corporation Study)	$\sim 40\%$	Throughout design phases	[36–39]
NASA	At PDR	20%	Preliminary Design Review (PDR)	[36–38]
NASA	At CDR	10%	Critical Design Review (CDR)	[36,38]

Table 3. Cont.

Agency	Project Phase	Power Margin	Report Timing	Reference(s)
JAXA	PDR (New bus & payload)	15%	At Preliminary Design Review (PDR)	[38,40,41]
JAXA	CDR (New bus & payload)	10%	At Critical Design Review (CDR)	[38,40,41]
JAXA	PSR (New bus & payload)	6%	At Post-Shipment Review (PSR)	[38,40,41]
JAXA	PDR (Large changes to existing bus/payload)	10%	At Preliminary Design Review (PDR)	[38,40,41]
JAXA	CDR (Large changes to existing bus/payload)	8%	At Critical Design Review (CDR)	[38,40,41]
JAXA	PSR (Large changes to existing bus/payload)	6%	At Post-Shipment Review (PSR)	[38,40,41]
JAXA	PDR (Minor changes to existing bus/payload)	5%	At Preliminary Design Review (PDR)	[38,40,41]
JAXA	CDR (Minor changes to existing bus/payload)	8%	At Critical Design Review (CDR)	[38,40,41]
JAXA	PSR (Minor changes to existing bus/payload)	4%	At Post-Shipment Review (PSR)	[38,40,41]
JAXA	PDR (Existing bus & payload)	5%	At Preliminary Design Review (PDR)	[38,40,41]
JAXA	CDR (Existing bus & payload)	5%	At Critical Design Review (CDR)	[38,40,41]
JAXA	PSR (Existing bus & payload)	3%	At Post-Shipment Review (PSR)	[38,40,41]

The AIAA power categories provide a structured basis for scaling contingencies with mission class and power level (Table 4). This scaling is particularly relevant for small satellites, where limited generation area, conversion losses, and reduced redundancy increase risk. In contrast, broader agency values (e.g., a uniform 20% system-level margin) may not capture low-power sensitivities. For this reason, the AIAA framework is used as a starting point, then extended to the very low power regime.

However, the lowest existing bin (0–500 W) is too coarse for nano/pico/femto/atto systems. Refined subcategories and associated contingencies are, therefore, defined as follows.

Table 4. AIAA recommended minimum standard power contingencies in %. Category descriptions follow [9,30,42].

	Proposal Stage						Design Development Stage								
	Bid Class			CoDR Class			PDR Class			CDR Class			PRR Class		
Description/ Categories	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3
Category AP 0–500 W	90	40	13	75	25	12	45	20	9	20	15	7	5	5	5
Category BP 500–1500 W	80	35	13	65	22	12	40	15	9	15	10	7	5	5	5
Category CP 1500–5000 W	70	30	13	60	20	12	30	15	9	15	10	7	5	5	5
Category DP 5000 W and up	40	25	13	35	20	11	20	15	9	10	7	7	5	5	5

2.2. Definition of Power Subcategories

The lowest established AIAA bin is 0–500 W (Table 4) [9,30]. PlanarSats operate far below this level, motivating a more granular breakdown. As summarized in Table 5, notional upper bounds for standardized classes (CubeSats, PocketQubes) are estimated

using a representative commercial solar cell (3G30A, AZUR SPACE Solar Power GmbH, Heilbronn, Germany; 4 cm × 8 cm, ~30% efficiency, 1.2 W at normal incidence) [43]. Cells are mounted on each face, and for comparability across classes, a three-face, 45° illumination convention is assumed. This is a normalization scenario for category derivation; actual duty cycles and attitudes are handled in mission analysis. The three-face, 45° convention is not a physically realizable solar geometry and is used solely to derive power bins on a consistent basis across form factors; mission- and attitude-dependent power is treated later through the standard budget and contingency process.

$$P(\theta) = \eta_{\text{conv}} P_{\text{norm}} \cos \theta, \quad (1)$$

where P_{norm} is the BoL normal-incidence array power at AM0 and η_{conv} captures conversion/wiring efficiencies. For free-tumbling PlanarSats, a mission-averaged $\langle \cos \theta \rangle$ consistent with expected attitude dynamics is used in later analyses.

Table 5. Standardized small satellites equipped with the maximum number of standard solar cells [43] on X, Y, and Z faces. Power is computed for a 45° incidence on three faces.

	Number of Cells on Surfaces			Maximum Power at 45 Degrees
	X	Y	Z	Total [W]
16U	20	20	8	40.73
12U	14	14	8	30.55
8U	20	10	4	28.85
6U	14	7	4	21.21
4U	10	10	2	18.67
3U	7	7	2	13.58
2U	4	4	2	8.49
1U	2	2	2	5.09
3P	2	2	0.5	3.82
2P	1.5	1.5	0.5	2.97
1.5P	1	1	0.5	2.12
1P	0.5	0.5	0.5	1.27
0.5P	0.25	0.25	0.5	0.85

Analysis of the resulting levels shows that conventional bins are too coarse. A four-level breakdown (50–20 W, 20–5 W, 5–1.2 W, 1.2–0 W) aligns with achievable power trends and supports downstream estimation. Combining original and extended bins yields: 5000+ W, 5000–1500 W, 1500–500 W, 500–100 W, 100–50 W, 50–20 W, 20–5 W, 5–1.2 W, and 1.2–0 W. The 100–50 W bin bridges the gap between legacy AIAA categories and the new low-power bins. Old and new power trends are shown in Figure 4.

For context, the upper plotting limit (set at 50 kW) covers large systems (e.g., International Space Station (ISS) up to 248 kW BoL; Tiangong ~55 kW) [44,45]; the focus here is the low-power end relevant to PlanarSats.

2.3. Investigation of Contingencies for Lower Power Categories

Because historical data are sparse at very low power, contingency values below 500 W are extrapolated from higher-power anchors in Table 4. Alternatives were assessed (linear, spline, piecewise). Linear methods can mislead over wide dynamic ranges [46]; higher-order fits can oscillate with sparse data [47]. Given the multi-decade span and near-exponential behavior, log-linear interpolation is adopted as a conservative, consistent choice.

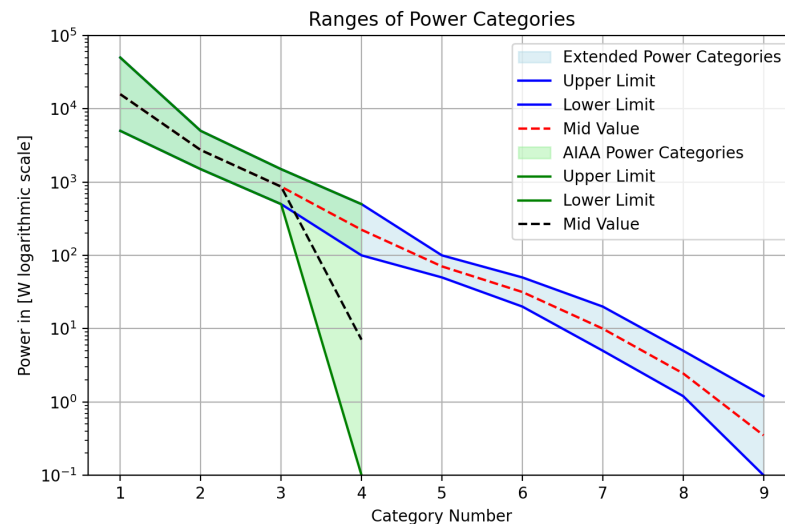


Figure 4. Power level categories: original AIAA (green) [30], extended bins (blue), and geometric means (black/red dashed) used for extrapolation. Lower bound is 0.1 W (log scale).

Each bin is represented by the geometric mean of its endpoints. For the lowest bin (0–1.2 W), a 0.1 W lower bound avoids $\log(0)$. The extrapolated contingency C_x at P_x is computed as

$$C_x = C_1 + \frac{C_2 - C_1}{\log_{10} P_2 - \log_{10} P_1} (\log_{10} P_x - \log_{10} P_1), \quad (2)$$

where (P_1, C_1) and (P_2, C_2) are adjacent known anchors. Extrapolated values are calculated in log space, then rounded to the nearest 5%; values exactly halfway between multiples of 5 are rounded to the higher multiple. Anchor values are retained as published.

Applying this to AIAA anchors yields the extended recommendations in Table 6. The main text reports rounded values for engineering clarity; unrounded values appear in Table A1 (Appendix A). This provides a transparent, repeatable extension into regimes with limited heritage.

Table 6. Extended recommended power contingencies based on AIAA minimum standard reserves [30]. Original anchors are retained; italicized entries are extrapolated for the refined low-power bins. “Bid” = Proposal phase; CoDR = Concept Design Review; PDR = Preliminary Design Review; CDR = Critical Design Review; PRR/FRR = Production/Flight Readiness Review. Class definitions follow [9]. The full set of unrounded values appears in Table A1 (Appendix A).

Description/Categories	Proposal Stage						Design Development Stage						
	Bid Class			CoDR Class			PDR Class			CDR Class			PRR/FRR Class
	I	II	III	I	II	III	I	II	III	I	II	III	I-II-III
0–1.2 W	120	65	13	105	50	12	70	45	9	45	40	7	5
1.2–5 W	115	60	13	100	45	12	65	40	9	40	35	7	5
5–20 W	110	55	13	95	40	12	60	35	9	35	30	7	5
20–50 W	105	50	13	90	35	12	55	30	9	30	25	7	5
50–100 W	100	45	13	85	30	12	50	25	9	25	20	7	5
100–500 W	90	40	13	75	25	12	45	20	9	20	15	7	5
500–1500 W	80	35	13	65	22	12	40	15	9	15	10	7	5
1500–5000 W	70	30	13	60	20	12	30	15	9	15	10	7	5
5000 W +	40	25	13	35	20	11	20	15	9	10	7	7	5

At the lowest powers and earliest phases, some Class I values exceed 100% (e.g., Bid at 0–1.2 W). This is consistent with agency practice permitting high early-phase contingencies

for first-of-kind or tightly constrained designs [28,48,49]. Such values reduce with maturity (PDR/CDR), as reflected in Table 6.

2.4. Sensitivity Analysis for Extrapolated Contingency Values

All sub-500 W recommendations are produced by stepwise, log-linear extrapolation from higher-power anchors (Table 4), evaluated at each bin's geometric center via Equation (2). Extrapolated values are then rounded to the nearest 5 percentage points, and the rounded value is propagated as the next anchor; values exactly halfway between multiples of 5 are rounded to the higher multiple. Published anchors from AIAA are retained as given. For the lowest bin (0–1.2 W), a lower bound of 0.1 W is used to avoid $\log(0)$.

Sensitivity is assessed with two one-at-a-time sweeps for PDR, Class II and results are shown in Table 7:

- **Sweep A (left block):** Vary the 500–1500 W anchor (5, 10, 15, 20%) with 100–500 W fixed at 20%.
- **Sweep B (right block):** Vary the 100–500 W anchor (15, 20, 25, 30, 35%) with 500–1500 W fixed at 15%.

Table 7. Sensitivity of stepwise log-linear extrapolation (PDR, Class II). Each entry shows the unrounded value followed by the rounded value in parentheses. Extrapolated values are rounded to the nearest 5 percentage points at each step; the rounded value is used as the next anchor. Underlined values in the header row show the baseline/actual values.

Power Bin (W)	Sweep A: 500–1500 W Anchor (%), 100–500 W = 20%				Sweep B: 100–500 W Anchor (%), 500–1500 W = 15%				
	5	10	<u>15</u>	20	15	<u>20</u>	25	30	35
50–100	32.75 (35)	28.50 (30)	24.25 (25)	20.00 (20)	15.00 (15)	24.25 (25)	33.50 (35)	42.75 (45)	52.01 (50)
20–50	45.48 (45)	36.99 (35)	28.49 (30)	20.00 (20)	15.00 (15)	28.49 (30)	41.99 (40)	55.48 (55)	60.48 (60)
5–20	59.31 (60)	42.15 (40)	37.15 (35)	20.00 (20)	15.00 (15)	37.15 (35)	47.15 (45)	69.31 (70)	74.31 (75)
1.2–5	78.33 (80)	46.11 (45)	41.11 (40)	20.00 (20)	15.00 (15)	41.11 (40)	51.11 (50)	88.33 (90)	93.33 (95)
0–1.2	107.81 (110)	51.95 (50)	46.95 (45)	20.00 (20)	15.00 (15)	46.95 (45)	56.95 (55)	117.81 (120)	122.81 (125)

Rounding policy: per-step versus end-of-cascade.

This work adopts per-step rounding because it reflects baselined values carried between design reviews (e.g., PDR, CDR), avoids false precision, and provides appropriate conservatism when heritage is sparse. End-of-cascade values are shown for diagnostic comparison only; per-step rounding is used in this work for all subsequent sizing. The table below compares per-step and end-of-cascade rounding for two cases, reporting unrounded (rounded) values so the computed quantities are visible:

- **Baseline:** 500–1500 W = 15%, 100–500 W = 20%.
- **High-gradient:** 500–1500 W = 15%, 100–500 W = 30%.

Lower-bound check for the 0–1.2 W bin.

Using a 0.2 W lower bound (instead of 0.1 W) yields 45.72% (unrounded) versus 46.95% (unrounded) in the baseline; both round to 45%. Thus, the reported rounded value is insensitive to the exact lower-bound choice in this range. Results and effects of this are given in Table 8.

In summary, anchor spacing (the difference between consecutive higher-power anchors) is the dominant driver of low-power results. With the adopted per-step rounding, the cascade is stable, conservative, and aligned with baselining practice at PDR/CDR; deferring rounding to the end reduces some intermediate bins by approximately 5 pp and can materially lower extreme early-phase values (e.g., 120% → 100%) in high-gradient cases.

Table 8. Comparison of rounding policies (PDR, Class II), showing unrounded (rounded).

Power Bin (W)	Baseline: Per Step	Baseline: End of Cascade	High-Gradient: Per Step	High-Gradient: End of Cascade
50–100	24.25 (25)	24.25 (25)	42.75 (45)	42.75 (45)
20–50	28.49 (30)	27.22 (25)	55.48 (55)	51.67 (50)
5–20	37.15 (35)	31.47 (30)	69.31 (70)	64.42 (65)
1.2–5	41.11 (40)	36.67 (35)	88.33 (90)	80.01 (80)
0–1.2	46.95 (45)	43.89 (45)	117.81 (120)	101.68 (100)

3. Case Study: Application to Real/Conceptual Satellites

To demonstrate the value and practical application of the proposed contingency and power-margin methodology, several representative PlanarSat missions are analyzed in the femto- and atto-satellite classes. The missions—Sprite V1, Sprite V2, PCBSat, SpaceChip, and LuxAtto—span a range of technical choices, design phases, and power budgets, providing a comprehensive view of the methodology’s applicability and its impact on design trade-offs. Figure 5 presents the mission set; Figure 6 later provides a project-level illustration using sequential Sprite variants.

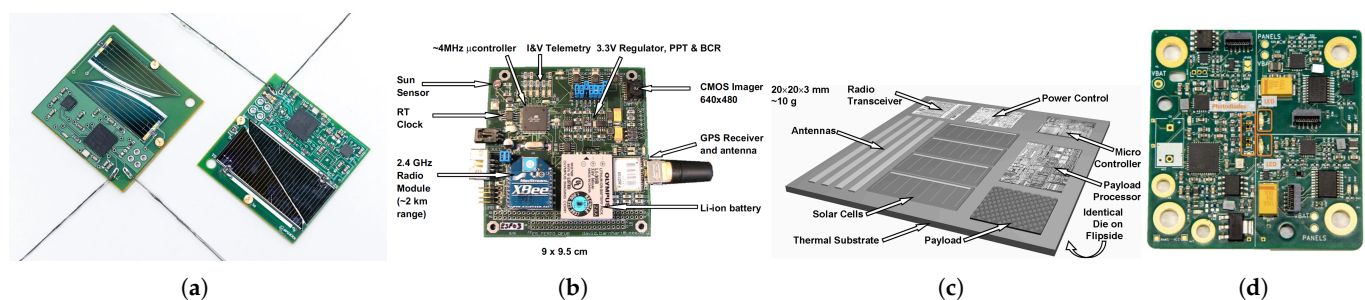


Figure 5. Analyzed missions: (a) Sprite V1 and V2 [50], (b) PCBSat [51], (c) SpaceChip [52], (d) Lux-Atto [53].

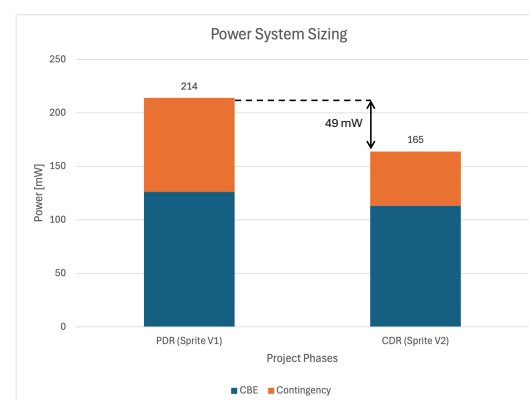


Figure 6. Project-level illustration using sequential Sprite variants: MEV reduction from PDR (Sprite V1) to CDR (Sprite V2). Retaining earlier sizing yields a heritage-derived buffer contributing to MPV.

Mission set and phase/class assignment.

Five missions are considered and summarized in Table 9:

- **PCBSat and SpaceChip:** Conceptual designs by Barnhart et al.; PCBSat was partially realized. CBE: 746 mW (PCBSat) and 1.14 mW (SpaceChip). Both were treated as Class I and analyzed at different phases [51,52].

- **LuxAtto (Primary and A&B):** Early concept line; LuxAtto P is treated as Class II at PDR, LuxAtto A&B as Class I at PDR. Variants are analyzed individually [53–55].
- **Sprite V1 and V2:** Flown in KickSat-1/2; power values are calculated from bill of materials (BOM) and datasheets. Sprite V1 is first-of-kind (Class I); Sprite V2 benefits from heritage (Class II) [23,56–58].

Table 9. Solar cell characteristics and power sizing details for analyzed missions [51–53,56,59]. For Sprites, “Max Power Generation” refers to per-side normal-incidence values. PCBSat and SpaceChip also report values sized at 45°; those appear in the “Solar Sized for Power” row.

Specification / Satellite	Sprite V1	Sprite V2	PCBSat	SpaceChip	Lux-P	Lux-A&B
Power and Sizing						
CBE [mW]	126	114	746	1.14	462	70
Total Solar Cell Area [cm ²]	4.55	2.6	56	2.28	7.36	3.68
Max Power Generation [mW]	123	68	1131	1.87	105.2	52.6
Solar Sized for Power [mW]	N/A	N/A	821 (45°)	1.34 (45°)	N/A	N/A
Satellite Dimensions [cm × cm]	3.5 × 3.5	3.5 × 3.5	9 × 9.5	2 × 2	5 × 2.5	2.5 × 2.5
Solar Cell Characteristics						
Cell Efficiency [%]	27	28	15	1	25	25
Power Density [mW/cm ²]	27	26.15	15.88	N/A	14.3	14.3
Cell Technology	Triple-J GaAs	Triple-J GaAs	Silicon	N/A	Silicon	Silicon

3.1. Case-by-Case Insights and Methodological Lessons

Unless otherwise noted, instantaneous power is single-sided, meaning only the Sun-facing face produces power at a given moment. Dual-sided coverage improves orbit-average yield under tumbling, but it does not imply simultaneous two-sided illumination. Summary of key power metrics analyzed for the satellite are shown in Table 10.

Table 10. Summary of key power metrics for analyzed satellites. Assumed project phases and classes are shown. As-built Solar Area is as flown or as documented; Req. Solar Area (MEV) is the normal-incidence area needed to meet MEV; PCB-side Area Inc. is the increase in single-side PCB planform adopted in the case studies to achieve MEV.

Satellite	Class	Phase	Contingency (%)	MEV (mW)	As-Built Solar Area (cm ²)	Req. Solar Area (MEV) (cm ²)	PCB Area Inc. (%)	Notes
SpaceChip	I	Bid	120	2.51	2.28	3.06	19.5	Concept, 1% cell
PCBSat	I	CoDR	105	1529.3	56.00	96.30	47.13	Concept/Prototype
LuxAtto A&B	I	PDR	70	119.0	3.68	8.32	54.72	New form factor
LuxAtto P	II	PDR	45	669.9	7.36	46.85	316.00	Predecessor: Sprite
Sprite V1	I	CDR	45	182.7	4.55	6.77	18.12	First of kind
Sprite V2	II	CDR	40	159.6	2.60	6.10	28.57	Heritage from V1

Method: Req. Solar Area (MEV) is computed as $A_{\text{req}} = \text{MEV} / \rho_p$ using the power densities in Table 9. For SpaceChip, ρ_p is estimated from the reported normal-incidence capability (1.87 mW) and solar area (2.28 cm²), giving $\rho_p \approx 0.82 \text{ mW cm}^{-2}$. If sizing at a worst-case incidence θ (e.g., 45°), multiply A_{req} by $1 / \cos \theta$.

PCBSat and SpaceChip. PCBSat’s as-built generation (up to 1131 mW at normal incidence) exceeded its CBE but was below the recommended MEV of 1529.3 mW for Class I at conceptual design review (CoDR). The original design used a 45° sizing angle and included conversion losses [51]. At that level, CBE is met only when incidence is roughly 45° or better; larger angles or longer eclipses reduce margin. Sizing to MEV would increase tolerance to degraded pointing and tumbling, with a required PCB-side area increase of

47.13% (equivalently, a required solar area of $\sim 96.30 \text{ cm}^2$ at normal incidence). For a planar architecture, that growth maps directly to PCB planform, which may influence board mass, stiffness, and rideshare accommodation.

SpaceChip used very low efficiency cells (about 1%) and no attitude determination and control system (ADCS). The reported normal-incidence capability is 1.87 mW, with a 45° -sized value of 1.34 mW. The MEV of 2.51 mW at Bid (Class I) implies a 19.5% PCB area increase (Req. Solar Area (MEV) $\sim 3.06 \text{ cm}^2$ at normal incidence). Although small in absolute terms, even modest platform changes at this scale compete with component footprints and interconnect routing. Sizing to MEV expands the usable attitude envelope without major integration penalties.

LuxAtto (Primary and A&B). For LuxAtto P, the available generation of 105.2 mW at normal incidence is below the CBE of 462 mW, so operations rely on battery energy [53]. Sizing to the MEV of 669.9 mW (Class II at PDR) raises incidence tolerance at a given duty cycle and enables faster recharge. This requires a 316.00% increase in PCB side area, from 12.5 cm^2 to 52 cm^2 in the documented envelope (equivalently, a required solar area of $\sim 46.85 \text{ cm}^2$ at normal incidence). Because solar area scales the PCB planform for PlanarSats, this growth has programmatic implications: larger panel outlines tend to increase manufacturing cost and handling complexity, reduce panel yield per panelization sheet, lengthen harness runs, and raise assembly risk; dispensers or rideshare slots sized for a given footprint may accommodate fewer units, or a larger envelope may need to be purchased; higher exposed area can alter thermal radiation balance, increase solar-radiation-pressure and aerodynamic torques, and modestly increase drag, which can shorten lifetime in low orbit unless compensated elsewhere.

For LuxAtto A&B, the CBE is 70 mW with 52.6 mW generation at normal incidence. Sizing to the MEV of 119 mW (Class I at PDR) reduces time in power deficit. The required PCB side area increase is 54.72%, from 6.25 cm^2 to 9.67 cm^2 , which is significant for multi-unit deployments. Practical effects include tighter packing in dispensers or trays, reduced unit count for a fixed envelope, higher per-unit board cost due to lower panelization efficiency, and potential updates to structure and thermal interfaces.

Sprite V1 and V2. Sprite V1's CBE of 126 mW slightly exceeds its per-side normal-incidence maximum of 123 mW. Consequently, continuous full-CBE operation under single-sided illumination is not achievable without intermittent duty cycling or storage; near normal incidence, however, reduced-duty operation remains feasible with low losses [56]. With limited storage, performance falls with larger incidence angles, for example, when the radio draws 60 mW. Sizing to the MEV of 182.7 mW (Class I at CDR) increases operational robustness under tumbling. The minimum board-area growth cited for this case is 18.12%, from 12.25 cm^2 to 14.47 cm^2 . Even small absolute increases at this scale matter because they reduce edge clearance for antennas, affect stiffness and modal behavior, and can change radiator view factors.

Sprite V2 benefits from heritage (Class II at CDR) and has a lower MEV of 159.6 mW. Per-side normal-incidence generation is 68 mW; dual-sided coverage improves orbit-average yield during tumbling but does not change the single-sided instantaneous limit. Meeting the MEV requires a 28.57% increase in board area, from 12.25 cm^2 to 15.74 cm^2 . Similar to V1, platform growth at this scale has practical impacts on component placement, connector reach, and structural margins.

3.2. From MEV to MPV: Adding Operational and Heritage Margins

The case studies above use MEV as the sizing target, which avoids contingency pile-up. MPV remains useful for bounding analyses or where program rules require additional reserves. Two effects are particularly relevant at ultra-low power:

- **Operational effect:** Free tumbling or limited ADCS reduces average effective incidence; designs sometimes specify a worst-case incidence for sizing rather than relying on normal-incidence capability.
- **Heritage effect:** When a later-phase design has a lower MEV than an earlier phase but retains the earlier, larger array, the difference functions as a de facto margin at the later phase.

Example: project-level illustration with the Sprite family.

At PDR, Sprite V1 has CBE = 126 mW with 70% contingency, giving MEV = 214 mW. At CDR for Sprite V2, CBE = 114 mW with 45% contingency, giving MEV = 165 mW. Retaining the earlier sizing yields a heritage-derived buffer of $214 - 165 = 49$ mW, so

$$MPV_{CDR} = 165 \text{ mW} + 49 \text{ mW} = 214 \text{ mW}.$$

Design feasibility check.

Physical limits such as maximum available solar area, PCB outline, dispenser geometry, and budget place a ceiling on achievable MPV. If a computed MPV exceeds feasible capability, MPV should be reset to the realizable bound so that risk and expectations remain aligned with deliverable hardware.

Overall, the consolidated results reinforce three points. First, class- and phase-appropriate contingencies provide a consistent, review-ready basis for PlanarSat power sizing. Second, instantaneous generation is single-sided, so dual-sided coverage helps only the orbit-average under tumbling. Third, making orientation assumptions explicit and recognizing that solar area directly drives PCB planform helps avoid optimistic claims and ties power sizing to real integration, cost, and rideshare constraints.

4. PlanarSat System Development Approach

While the example in this section uses ideal solar cell performance values (for example, 0° Sun angle, AM0 irradiance, and no degradation), this is for demonstration clarity. In real missions, power generation capability should be scaled using mission-specific factors such as incidence-angle statistics and conversion efficiency, as discussed earlier. Applying these factors to the MEV yields an MPV that reflects a more realistic power design target under operational uncertainties.

Most PlanarSats in the femto and atto ranges do not include a battery. The reasons include direct temperature exposure, cost (financial and protection-circuit development time), and especially limited volume. Given the single-plane structure and the possibility of using both surfaces, three initial configurations are commonly considered. Depending on resources and mission criticality, designers may select among the following:

- **Separated:** solar cells on one face, electronics on the other (Figure 2a).
- **Half-mixed:** a full solar face on one side, a partial solar area with electronics on the other (Figure 2b).
- **Mixed:** solar cells and electronics on both faces (Figure 2c).

As introduced in Figure 2 (see Section 1), these options establish the foundational trade-offs for PlanarSat system design.

In the absence of active attitude control, PlanarSats are typically modeled as free tumblers. Actual attitude statistics depend on geometry, mass distribution, orbit, and environment, and may show some weak preferential orientations. For the comparative analyses here, we assume single-sided instantaneous power; only the Sun-facing side generates at a given moment. Two-sided coverage improves orbit-average energy under tumbling, but it does not imply simultaneous two-sided illumination. Performance can be improved with

ADCS (for example, magnetorquers for Sun pointing), although the ADCS power must be included in the budget.

4.1. A PlanarSat Design: Operational Power Envelopes

The design of current femto and atto PlanarSats is rooted in practical experience and available components [3], with detailed examples in [51,52] based on SMAD [31]. Limited flight heritage at these scales leads to higher design uncertainty. This subsection shows how phase- and class-appropriate contingency and margin can inform sizing and operational analysis using a realistic example.

We consider a single-sided (separated) concept to illustrate the path from CBE to MEV to MPV, and the reverse (constraint-driven) viewpoint. Representative components, shown in Table 11, are: MCU (STM32L496RGT6, STMicroelectronics N.V., Geneva, Switzerland; 1.96 cm^2 , 30 mW at 16 MHz) [60], transceiver (SX1278IMLTRT, Semtech Corporation, Camarillo, CA, USA; 0.49 cm^2 , 95.7 mW during transmit at 13 dBm, 40 mW receive) [61], and a generic payload (2.00 cm^2 , 50 mW including peripherals). The satellite operates in two primary modes: transmission (MCU + transmitter) and payload with receiver active; receive is assumed active except during transmission. The CBE for transmission is 125.7 mW, which places the design in the 0–1.2 W category of Table 6. As a first-of-kind mission (Class I) at Bid, a 120% contingency is applied. For illustration only, a 20% system margin is then added to form MPV.

Table 11. Representative component areas and references (payload entry is illustrative).

Component	Area [cm^2]	Reference
STM32L496RGT6 MCU	1.96	[60]
SX1278IMLTRT Transceiver	0.49	[61]
Example payload (illustrative)	2.00	

We use the planar cosine relation defined earlier (Equation (1)) to interpret operational envelopes versus incidence angle. As shown in Figure 7, the maximum allowable θ for full operation is set by the ratio of available array power to required power (MEV or MPV). Lower θ provides surplus power that can be used for additional functions or robustness.

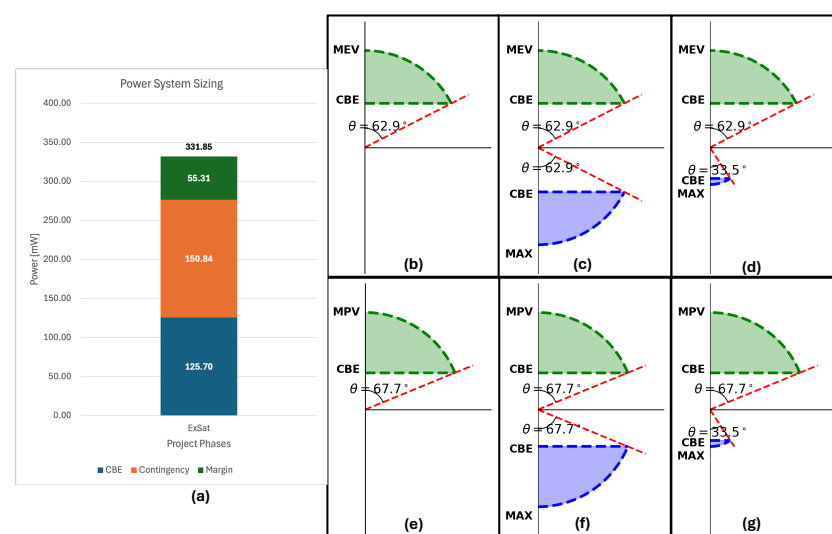


Figure 7. Solar panel sizing and operational power envelopes for PlanarSat architectures. (a) Cumulative power sizing from CBE = 125.7 mW to MEV = 276.54 mW to MPV = 331.85 mW. (b–d) Operational envelopes for separated, mixed, and half-mixed designs sized to MEV; (e–g) same designs sized to MPV. The maximum incidence angle θ for full operation is indicated in each case.

Figure 7b–d shows the operational envelopes for the three architectures (as defined in Figure 2) when arrays are sized to MEV. In the separated case (b), generation occurs only when the solar face is Sun-facing. In the mixed case (c), placing cells on both faces allows operation in either orientation at the expense of added cell count and cost. The half-mixed design (d) places a full array on one face and a smaller area on the reverse, resulting in different allowable incidence angles depending on which face is Sun-facing. Figure 7e–g repeats the analysis for MPV sizing, showing the expected expansion of operational envelope. The figure assumes single-axis variation in incidence for clarity; mission-specific attitude statistics should be used when available.

Over-allocating margin can increase cost, complexity, and mass without proportional risk reduction. In line with NASA’s concurrent engineering resource management guidance [26], margin and contingency should be applied judiciously and refined as the design matures.

Sizing Example: Requirement-Driven vs. Constraint-Driven for a Separated PlanarSat

We present a worked example using typical component footprints and AM0 solar-cell performance. Unless noted, we adopt the AZUR Space 3G30A beginning-of-life normal-incidence power density of 39.76 mW cm^{-2} at AM0 [43]. The total electronics footprint is

$$A_{\text{elec}} = 1.96 + 0.49 + 2.00 = 4.45 \text{ cm}^2.$$

These values are beginning-of-life and exclude incidence, temperature, wiring, and conversion derates, which are captured in the mission-specific MPV factor.

(1) Requirement-driven sizing (power first).

With $\text{CBE} = 125.7 \text{ mW}$ and 120% contingency, $\text{MEV} = 276.54 \text{ mW}$. Adding 20% system margin gives $\text{MPV} = 331.85 \text{ mW}$. Required solar areas at normal incidence are

$$A_{\text{solar,MEV}} = \frac{276.54}{39.76} = 6.96 \text{ cm}^2, \quad A_{\text{solar,MPV}} = \frac{331.85}{39.76} = 8.35 \text{ cm}^2.$$

For a separated layout, the board side length is set by the larger of the solar face and the electronics face:

$$\text{side}_{\text{MEV}} = \sqrt{\max(6.96, 4.45)} = 2.64 \text{ cm}, \quad \text{side}_{\text{MPV}} = \sqrt{\max(8.35, 4.45)} = 2.89 \text{ cm}.$$

(2) Constraint-driven sizing (fixed outline first).

If the outline is fixed by manufacturing or rideshare constraints, the available solar area on the solar face equals the full face area for a separated architecture. For a compact $2.5 \times 2.5 \text{ cm}$ board,

$$A_{\text{solar,avail}} = 6.25 \text{ cm}^2, \quad P_{\text{max,solar}} = 6.25 \times 39.76 = 248.5 \text{ mW}.$$

This falls short of the MEV target of 276.54 mW even at normal incidence, so geometry alone forces a power deficit that will grow with off-nominal incidence or conversion losses. For a larger $5 \times 5 \text{ cm}$ outline,

$$A_{\text{solar,avail}} = 25.00 \text{ cm}^2, \quad P_{\text{max,solar}} = 25.00 \times 39.76 = 994 \text{ mW}.$$

The quantitative outcomes of both approaches are summarized in Table 12, which highlights the differences between requirement-driven and constraint-driven sizing. The table makes clear how surface area and side length directly translate into either achievable power

(constraint-driven) or required power targets (requirement-driven), emphasizing the trade-off between geometric feasibility and mission power needs.

Table 12. Separated configuration sizing: requirement-driven and constraint-driven cases at normal incidence.

Approach	Surface Area (cm ²)	Side Length (cm)	Power Target or Max (mW)
Constraint-driven (fixed 2.5 × 2.5 cm)	6.25	2.50	248.5 (max)
Requirement-driven (MEV)	6.96	2.64	276.54
Requirement-driven (MPV)	8.35	2.89	331.85
Constraint-driven (fixed 5 × 5 cm)	25.00	5.00	994 (max)

This example shows the direct, quantitative link between surface allocation, power-margin methodology, and hardware feasibility. Actual operational power will be reduced by incidence, temperature, wiring, and conversion losses, which should be captured in mission-specific MPV factors and verified with attitude statistics.

This approach provides a basis for subsequent steps such as explicit surface allocation among subsystems and optimization of power–data trades under strict geometric constraints. While the examples here demonstrate the core architectural effects, a full comparison among designs with detailed allocation and phase-based contingency is left for future work.

Finally, the present work does not include validation through flight data or end-to-end simulations. The use of MEV and MPV follows established power-aware sizing practice, but the framework should be validated against mission-specific dynamics such as tumbling, degradation, and off-nominal incidence. Agency standards remain a solid baseline; however, at extreme miniaturization levels, a tailored, geometry- and context-specific application is required. Future efforts will focus on validating and refining the framework through higher-fidelity analyses and mission demonstrations.

4.2. Applicability and Limitations at the Atto Scale

Here, “atto-class” refers to spacecraft with a wet mass below 10 g. The contingency–margin framework (CBE→MEV→MPV), the rounding policy, and the operational power envelope construction with single-sided instantaneous generation and separated/half-mixed/mixed layouts are scale-neutral and apply at this mass range.

Quantitative inputs, however, must be calibrated for <10 g designs. In practice, this means measuring, for the chosen implementation: (i) photovoltaic performance and interconnect losses on very small panels, (ii) conversion and distribution efficiency, and start-up thresholds at μW – mW loads, and (iii) attitude and environment effects for low-inertia bodies that inform mission-specific MPV derates. With these inputs measured, the framework applies unchanged. Until such data are available for a given implementation, atto-class numbers should be treated as preliminary and updated as characterization results become available.

5. Conclusions and Future Work

This work introduced a structured, power-based systems engineering methodology for PlanarSats and, more broadly, atto-, femto-, and pico-class satellites. By extending contingency and margin philosophies from NASA, ESA, JAXA, and AIAA to the ultra-low-power regime, we provided a scalable framework for sizing and verifying power systems where available surface area is the dominant constraint.

Detailed low-power subcategories and a log-linear extrapolation of AIAA anchor values were developed to populate contingency guidance below 500 W (Table 6). A sen-

sitivity study (Section 2.4) showed that extrapolated values are driven primarily by the gradient between adjacent anchors, and that rounding to the nearest 5% at each step yields stable, monotonic, and transparent recommendations suitable for engineering use. The consolidated definitions of contingency and margin, aligned with agency practice, separate uncertainty allowances applied to the current best estimate (CBE) from system-level buffers, and avoid double counting by sizing to MEV for routine design while reserving MPV for bounding analyses.

We linked this margin philosophy to hardware geometry through the operational power envelope concept (Figure 7). The envelope quantifies the set of orientations and environmental states in which available power meets or exceeds demand, making the reliability implications of power sizing explicit for separated, half-mixed, and mixed PlanarSat layouts (Figures 2 and 7). A worked example demonstrated requirement-driven and constraint-driven sizing for a separated configuration, with single-sided instantaneous generation stated explicitly. This clarified that two-sided cell coverage improves orbit-average yield under tumbling but does not imply simultaneous illumination, a frequent source of over-optimistic power claims in miniaturized designs.

Application to historical and conceptual missions (Sprite V1/V2, PCBSat, SpaceChip, LuxAtto) showed how class and phase reduce contingency as design maturity and heritage increase, and how MEV-based sizing translates directly into surface area, mass, and cost implications (Section 3.1 and Table 10). For low-power PlanarSats that operate near the edge of feasibility, the methodology makes these trade-offs quantitative and reviewable, helping to reduce the risk of under-designed power systems, a recurring root cause of small-satellite shortfalls.

Future work will expand the empirical basis for atto- and femto-class systems, refine subcategory contingencies as flight data and ground testing accumulate, and integrate additional system constraints into the power and surface allocation process. Priorities include the following: (i) incorporating temperature, degradation, wiring, and conversion losses into mission-specific MPV factors tied to attitude statistics; (ii) co-optimization of solar array placement with payload, communications, and thermal design and (iii) higher-fidelity analyses or hardware-in-the-loop demonstrations to validate the operational power envelope under tumbling and partial illumination. As datasets grow, the tables and envelopes presented here can be recalibrated and extended, providing a living, scale-aware methodology for robust PlanarSat power system design.

Author Contributions: Conceptualization, M.Ş.U.; methodology, M.Ş.U.; software, M.Ş.U.; validation, M.Ş.U. and A.R.A.; formal analysis, M.Ş.U.; investigation, M.Ş.U.; resources, M.Ş.U.; data curation, M.Ş.U.; writing—original draft preparation, M.Ş.U.; writing—review and editing, M.Ş.U. and A.R.A.; visualization, M.Ş.U.; supervision, A.R.A.; project administration, M.Ş.U.; funding acquisition, M.Ş.U. and A.R.A. All authors have read and agreed to the published version of the manuscript.

Funding: This research received no external funding.

Data Availability Statement: The original contributions presented in the study are included in the article, further inquiries can be directed to the corresponding author.

Acknowledgments: The authors thank Onur Çelik, Stefano Speretta and Erdem Turan for their feedback, comments and support.

Conflicts of Interest: The authors declare no conflicts of interest.

Abbreviations

The following abbreviations are used in this manuscript:

ADCS	attitude determination and control system
AIAA	American Institute of Aeronautics and Astronautics
Bid	bidding
BoL	beginning of life
BOM	bill of materials
CBE	current best estimate
CDR	critical design review
CoDR	conceptual design review
COTS	commercial off-the-shelf
DOD	Department of Defence
ESA	European Space Agency
ESD	Elements of Spacecraft Design
EPS	electrical power system
FRR	flight readiness review
IC	integrated circuit
ISS	International Space Station
JAXA	Japan Aerospace Exploration Agency
MCU	microcontroller
MEV	maximum expected value
MPV	maximum possible value
NASA	National Aeronautics and Space Administration
OPE	operational power envelope
PCB	printed circuit board
PDR	preliminary design review
PRR	production readiness review
PSR	post-shipment review
SMAD	Space Mission Analysis and Design

Appendix A. Extended Power Contingencies

This appendix lists the unrounded contingencies that underlie the rounded recommendations in Table 6. All definitions and the interpolation rule of Equation (2) apply. Each bin is represented by the geometric mean of its endpoints (with a 0.1 W lower bound for 0–1.2 W to avoid $\log(0)$); anchor values from AIAA are retained as published; non-anchor entries are obtained by base-10 log-linear interpolation/extrapolation between adjacent anchors. Parentheses show the computed unrounded values; the adjacent integers are the recommended values after rounding to the nearest 5% (ties rounded up). The PRR/FRR column remains fixed at 5% following the standard. These unrounded figures are provided to enable sensitivity analyses or alternative rounding conventions without re-deriving the table.

Table A1. Extended Recommended Power Contingencies based on AIAA contingencies (minimum standard power reserve percentages) [30]. Original anchor values are maintained; extrapolated values are shown in italic. “Bid” corresponds to Proposal phase bid estimates; CoDR to Concept Design Review; PDR to Preliminary Design Review; CDR to Critical Design Review and PRR to Production/Flight Readiness Review. Mission Class I refers to a completely new spacecraft (unique and first-generation); Class II represents the next generation of spacecraft that builds upon a previously established design, offering increased complexity or capability within the same framework and Class III is a production-level model derived from an existing design, intended for multiple units with a significant degree of standardization. All values are calculated and interpolated in log-space, with final recommendations rounded to the nearest 5% for engineering use; parentheses indicate pre-rounded values. This approach ensures clarity and practical applicability in design calculations.

Description/Categories	Proposal Stage						Design Development Stage						
	Bid Class			CoDR Class			PDR Class			CDR Class			PRR/FRR Class
	I	II	III	I	II	III	I	II	III	I	II	III	I-II-III
0–1.2 W	120 (121.95)	65 (66.95)	13	105 (106.95)	50 (51.95)	12	70 (71.95)	45 (46.95)	9	45 (46.95)	40 (41.95)	7	5
1.2–5 W	115 (116.11)	60 (61.11)	13	100 (101.11)	45 (46.11)	12	65 (66.11)	40 (41.11)	9	40 (41.11)	35 (36.11)	7	5
5–20 W	110 (112.15)	55 (57.15)	13	95 (97.15)	40 (42.15)	12	60 (62.15)	35 (37.15)	9	35 (37.15)	30 (32.15)	7	5
20–50 W	105 (106.99)	50 (48.49)	13	90 (91.99)	35 (33.49)	12	55 (53.79)	30 (28.49)	9	30 (28.49)	25 (23.49)	7	5
50–100 W	100 (98.50)	45 (44.25)	13	85 (83.50)	30 (27.55)	12	50 (49.25)	25 (24.25)	9	25 (24.25)	20 (19.25)	7	5
100–500 W	90	40	13	75	25	12	45	20	9	20	15	7	5
500–1500 W	80	35	13	65	22	12	40	15	9	15	10	7	5
1500–5000 W	70	30	13	60	20	12	30	15	9	15	10	7	5
≥5000 W	40	25	13	35	20	11	20	15	9	10	7	7	5

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