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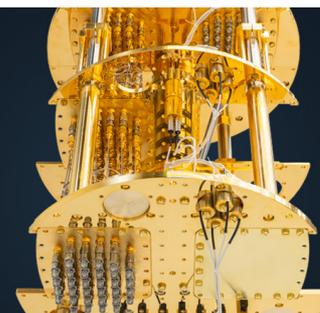
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ABSTRACT

Superconducting nanowire single-photon detectors (SNSPDs), owing to their unique performance, are currently the standard detector in most demanding single-photon experiments. One important metric for any single-photon detector is the recovery time, which defines the minimum temporal separation between consecutive detection events. In SNSPDs, the recovery time is more subtle, as the detection efficiency does not abruptly drop to zero when the temporal separation between detection events gets smaller, instead, it increases gradually as the SNSPD current recovers. SNSPD's recovery time is dominated by its kinetic inductance, the readout impedance, and the degree of saturation of internal efficiency. Decreasing the kinetic inductance or increasing the readout impedance can accelerate the recovery process. Significant reduction of the SNSPD recovery time, by, for example, adding a series resistor in the readout circuitry, is possible but can lead to detector latching, which hinders further detector operation or enforces underbiasing and hence a reduction in detection efficiency. Previous research has demonstrated passive resistive networks for the reduction of recovery time that rely on trial and error to find the appropriate resistance values. Here, we show that, using a cryogenically compatible and tunable resistor technology, one can find the optimized impedance values, delivering fast SNSPD recovery time while maintaining maximum internal detection efficiency. Here, we show an increase of around twofold in both maximum achievable detection rates and the achievable detection efficiency at high photon fluxes, demonstrating detection rates close to 114 Mcps with no loss of internal detection efficiency.

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Superconducting nanowire single-photon detectors (SNSPDs), composed of superconducting nanowires biased close to their critical currents, have become a cutting-edge technology in single-photon detection.^{1–3} SNSPDs have gained numerous research interests, attributed to their remarkable single-photon sensitivity, broad spectral range,^{4,5} high detection efficiency (>95 %^{6–8}), and low time jitter (tens of picoseconds^{9–11}). There is an increasing demand for SNSPDs in various applications such as biomedical imaging,¹² optical communication,¹³ and quantum key distribution.^{14–16}

When a photon is absorbed by the SNSPD, a resistive section is formed in the nanowire, which will initially expand due to Joule

heating. Subsequently, in an appropriately functioning SNSPD, the bias current is diverted to the readout, and this resistive section disappears due to the reduction of Joule heating and continuous cooling of the sample. These dynamics in the current redistribution between the SNSPD and the readout electronics lead to an electrical pulse at the output. The resulting output pulse usually rises steeply (sub-nanosecond) and decays slowly (nanoseconds), which is correlated with the inductive time constant L_k/R (L_k is the kinetic inductance of the SNSPD, which dominates its total inductance over the geometric inductance, and R is the resistance in the circuit). Thus, the falling edge of the output influences the nanowire current recovery time,

which is linked to its internal detection efficiency as a result. As the efficiency does not drop and recover abruptly in SNSPDs, the recovery time τ_{rec} can be defined as the time interval required to register a second photon subsequent to an earlier detection event with 50% of the normalized detection efficiency. This parameter is one of the essential performance metrics for applications that demand higher photon count rates, such as optical communication or high-throughput optical quantum computing.

SNSPDs have a shorter recovery time when their kinetic inductances are lower, straightforwardly achieved by shortening the meander. However, this also results in the reduction of the active detection area, which is disadvantageous for optical absorption and hence system detection efficiency. Other approaches to raise detection rates, such as utilizing quasi-constant-voltage (QCV) bias¹⁷ and implementing a DC-coupled readout scheme,¹⁸ were demonstrated, but these non-cryogenic solutions have limited performance enhancements as there is a considerable delay (nanoseconds) between the SNSPD and the control circuit. Another more effective way is to introduce a cryogenic serial resistor to SNSPD's readout circuit to reduce the recovery time by increasing R_s .^{19,20} The value of the serial resistance needs to be carefully optimized to minimize recovery time while avoiding the formation of a stable hot spot in the superconducting nanowire, which is referred to as the latching phenomenon.²¹ Based on the analysis of electrothermal effects in SNSPDs, the optimized value for this serial resistance can be influenced by various factors such as the heat transfer coefficient between the superconducting nanowire and the substrate, the geometry of the meandering nanowire, and the material properties of the superconducting layer (sheet resistance, critical current density, biasing current, etc.).²² Therefore,

the resistance value is often refined experimentally through a series of trials and errors, which is time-consuming and does not necessarily guarantee optimal performance.

In this work, we demonstrate a cryogenic tunable resistor to tune the serial resistance to the optimal value. The tunable resistor consists of a superconducting channel and a metal heater insulated by a SiO_2 layer. The resistance of the superconducting channel is controlled by the localized heat transfer from the heater and the self-Joule heating of the superconducting channel. This adjustable electrical component operating in cryogenic environments constitutes an efficient strategy to enhance the SNSPD performance at high photon flux rates. We show that this on-chip device is fully compatible with SNSPDs, enabling real-time optimization of the electrical recovery time while maintaining high internal detection efficiency.

Figure 1 represents a one-dimensional electrothermal simulation as described in Yang *et al.*,²¹ which illustrates the basic principle of decreasing the recovery time by connecting a small resistor in series with an SNSPD (more information about the simulation can be found in the [supplementary material](#)). The results were simulated at 2.5 K, which is the base temperature of our cryostat. An equivalent electrical diagram of an SNSPD in series with a resistor R_s is depicted in Fig. 1(a). The SNSPD is biased with a current source I_{bias} and is in parallel with the readout electronics, which is a capacitor C_b and a load resistor $Z_0 = 50 \Omega$, representing the input impedance of an RF amplifier.

Once a single photon triggers a resistive section across the nanowire, this section quickly expands due to Joule heating, and the effective bias current through the detector I_d is redirected to the load resistor, resulting in the rising edge at the output. The characteristic time of the rising edge is $\tau_1(t) \approx L_k / (Z_0 + R_s + R_L(t))$, where

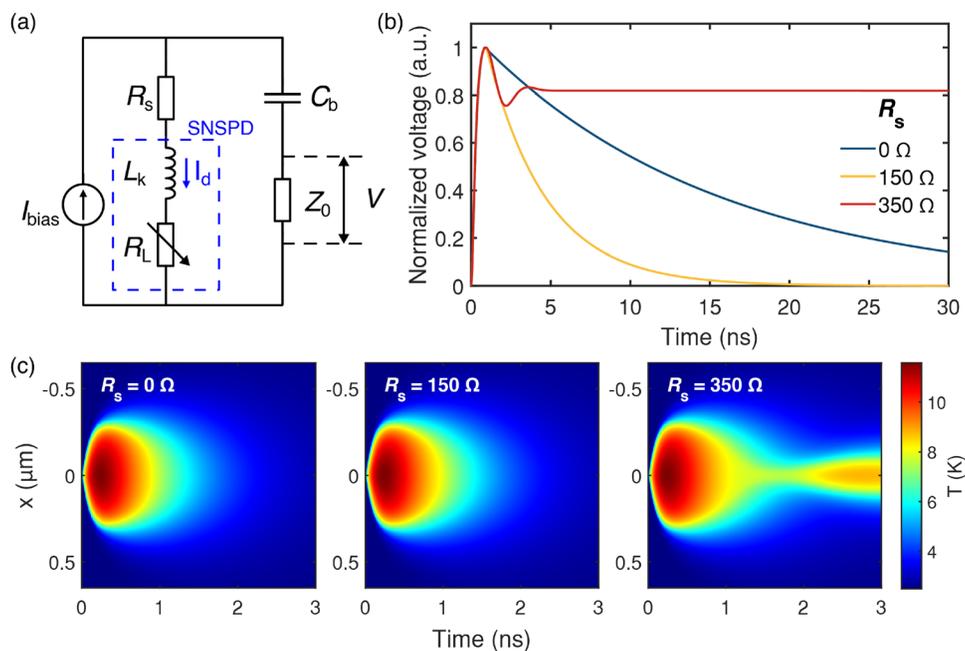


FIG. 1. (a) Schematic representation of electrical equivalent model of an SNSPD with a resistor R_s in series. (b) Simulated output voltage profile with $R_s = 0, 150,$ and 350Ω , respectively. (c) The evolution of the temperature profile of the superconducting nanowire with $R_s = 0, 150,$ and 350Ω , respectively. x is the spatial coordinate along the nanowire with the hot spot at the origin. Details of the simulation parameters can be found in the [supplementary material](#).

$L_k = 750$ nH and $R_L(t)$ represent the kinetic inductance of the detector and the real-time total hot-spot resistance in our simulation, respectively. After the resistive spot disappears via the heat dissipation to the substrate, the current flows back to the SNSPD with the time constant $\tau_2 \approx L_k / (Z_0 + R_s)$, corresponding to the falling edge of the output signal. Since $R_L(t)$ (typically a few k Ω) significantly exceeds Z_0 and R_s , the pulse width τ_{out} defined at $1/e$ of the pulse height is dominated by τ_2 .

The simulated output signals with three different R_s values are shown in Fig. 1(b). The pulse decay time (estimated via exponential fitting) reduces from 14.99 to 3.75 ns by increasing R_s from 0 to 150 Ω without changing the nanowire structure, which is consistent with the analytical expression of τ_2 . However, for a larger value of $R_s = 350$ Ω , the detector latches to a non-superconducting state, as represented by the temperature profile in Fig. 1(c) (more information can be found in the supplementary material). In the latter case, the current returns to the SNSPD so rapidly that the resistive spot is sustained by reaching an equilibrium between heat generation via Joule heating in the nanowire and heat dissipation to the substrate.

The threshold of the serial resistance to avoid latching depends on both the electrical properties of the device, such as the kinetic inductance of the SNSPD, and the thermal properties of the materials, such as the heat transfer coefficient between the nanowire and the substrate. Unfortunately, measuring these properties accurately on different platforms is not straightforward and, therefore, it is non-trivial to predict the maximum R_s , which optimizes the current recovery while avoiding the latching or loss of internal efficiency. Using fixed resistors, the experimental testing and validation process can take a considerable amount of effort and time. Hence, here, we propose and demonstrate a cryogenic tunable resistor, as depicted in Fig. 2, which makes it convenient to optimize the SNSPD performance with a serial tunable resistor.

An illustration of an SNSPD in series with a tunable resistor, as well as the schematic of the associated readout electronics, is shown in Fig. 2(a). An optical micrograph of a representative tunable resistor is shown in Fig. 2(b), which consists of a metal strip (an 80-nm-thick Ti layer with a 5-nm-thick Au layer) fabricated over a superconducting channel (NbTiN, with a thickness of 8–10 nm) insulated by a thin SiO₂ layer (40–50 nm in thickness). The metal strip acts as a micro-heater once connected to an electric power supply, producing Joule heat and raising the local temperature. When the local temperature exceeds the critical temperature, a resistive strip is formed in the channel, which introduces a tunable resistance in the circuit. The channel is narrowed down as it approaches the center of the metal heater to constrict the expansion of the resistive area and prevent the channel from latching.

The resistance of the channel is determined by both the bias current through the superconducting channel, I_{bias} , and the heater current, I_h . In Fig. 2(c), we characterize the dependence of channel resistance R on the currents I_{bias} and I_h in the device (DC analysis). The minimum width of the NbTiN channel is 1 μm , which is larger than the meander width of the SNSPD (typically < 100 nm) to ensure that stray photons cannot trigger a hot spot in the channel, and the metal heater has a width of 0.3 μm . As the bias current through the superconducting channel increases, the resistance grows faster with the heater current. This behavior is due to the generation of Joule heat in the NbTiN channel $I_{bias}^2 R$, which becomes significant at higher bias currents. To ensure reliable tunability in the desired resistance range

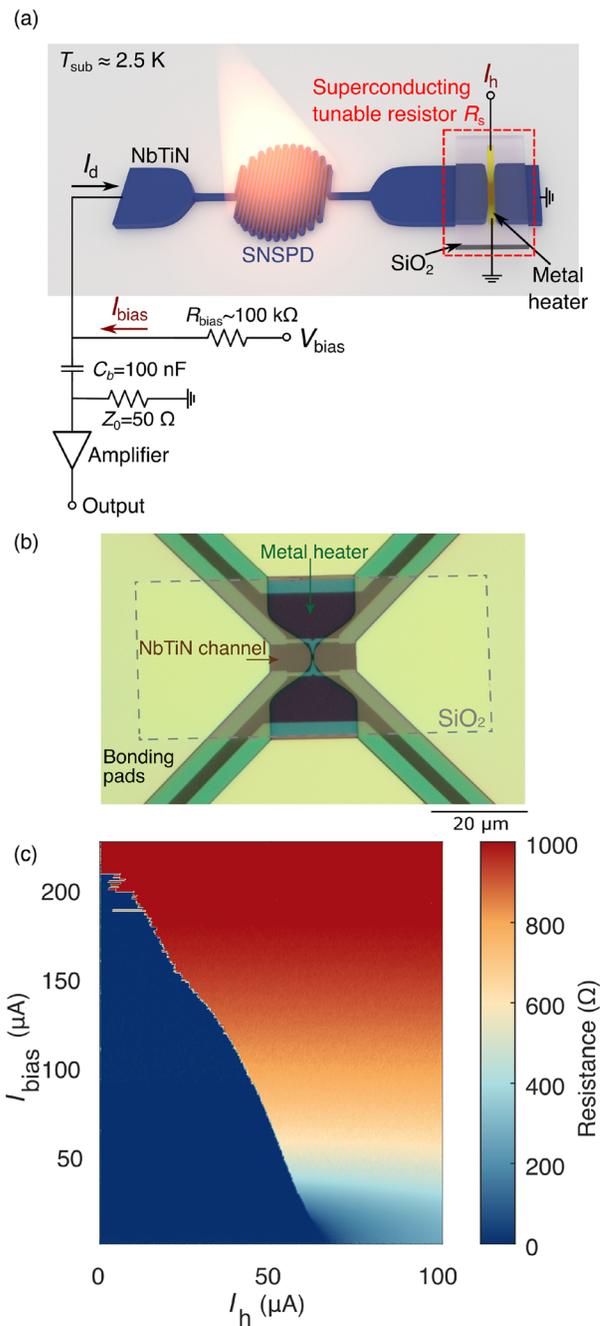


FIG. 2. (a) Illustration of a cryogenic tunable resistor R_s in series with an SNSPD and its electronic readout circuit. (b) Representative optical microscopy image of a cryogenic tunable resistor. (c) Characterization of the channel resistance in a cryogenic tunable resistor as a function of the channel bias current and the heater current. The channel has a width of 1 μm and the heater has a width of 300 nm.

(0–300 Ω), the tunable resistor is designed to operate at bias currents much lower than the switching current of the channel, typically around 5% of the switching current depending on the heater and the channel geometry as well as the resistivity of the superconducting

material. More information about the stability of the channel resistance and the influence of the device geometry can be found in the [supplementary material](#).

As depicted in [Fig. 2\(a\)](#), an SNSPD is connected in series with a cryogenic tunable resistor and biased and read out using a conventional bias tee and RF amplifiers. We characterize the sample in a flood illumination configuration with light injected from a fiber several centimeters above the sample. The output pulse is amplified and recorded using an oscilloscope. Both the theoretical analysis and the simulation results indicate that a higher I_h results in a shorter output pulse width τ_{out} , allowing two consecutive photons to, in principle, be detected with a shorter time delay. However, another crucial characteristic to consider is the detection efficiency of the second event, which itself depends on not only the current recovery speed but also on the degree

of saturation of internal detection efficiency (IDE) (i.e., the relative length of the plateau in the detection efficiency curve to the switching current). Since the detection efficiency of SNSPD increases with the effective bias current, the probability of detecting a second photon grows as the current through the SNSPD recovers. Thus, we experimentally measured the recovery of the detection efficiency as a function of the time delay after the first detection event using continuous light sources at different heater currents (i.e., different serial resistances). The time when the normalized detection efficiency recovers to 50% is estimated as the recovery time τ_{rec} .

[Figure 3](#) shows the experimental results of an SNSPD composed of a 70-nm-wide nanowire in series with a cryogenic tunable resistor with a 1- μm -wide channel and a 0.3- μm -wide heater. The SNSPD was biased at around 90% of the switching current and measured at a

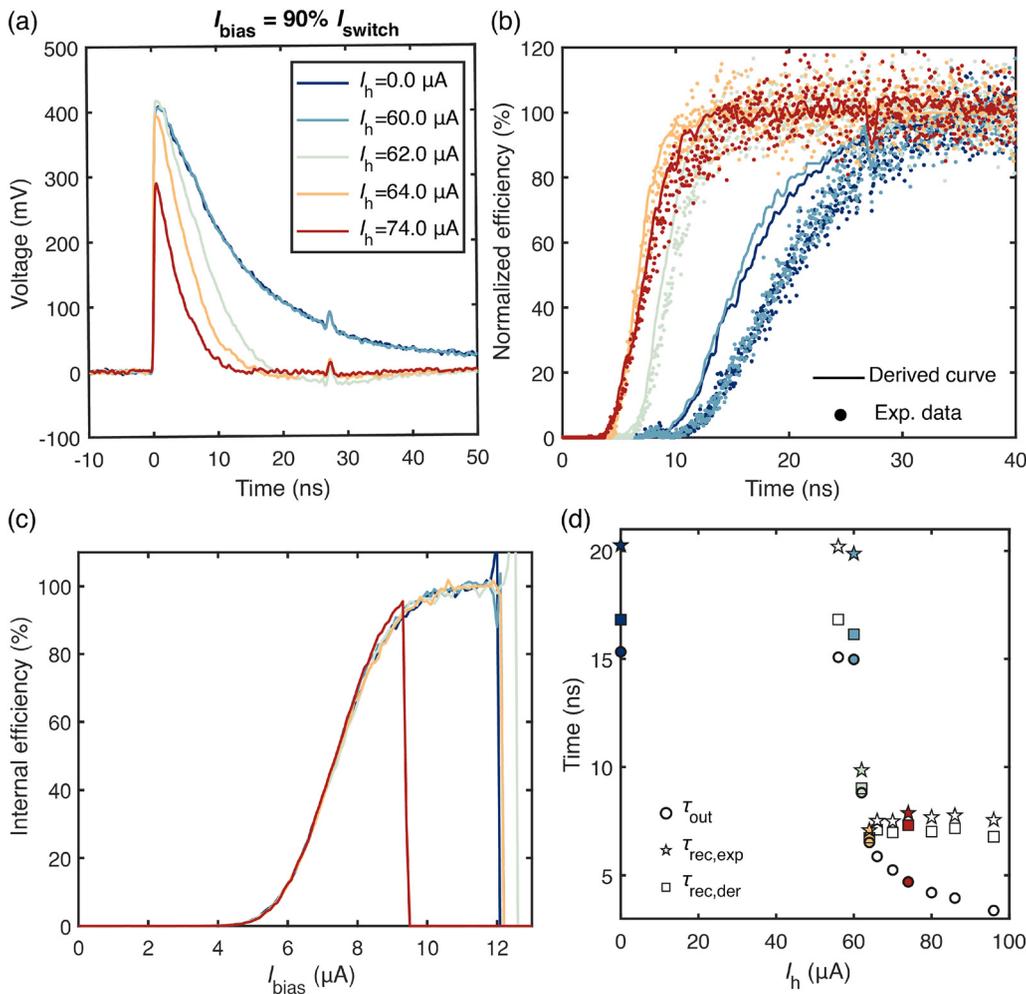


FIG. 3. (a) Amplified output waveforms of the SNSPD (curves are averaged over 100 sweeps). Comparison of the averaged and the single-sweep waveform can be found in the [supplementary material](#). (b) Histogram of the normalized detection efficiency as a function of the time delay after one detection event with various I_h for the wavelength of 1548 nm. The filled dots represent the experimental data, and the solid ones are the predicted efficiency recovery curves derived from internal detection efficiency curves and output pulses. (c) Normalized internal detection efficiency curves with various heater currents I_h , obtained for the wavelength of 1548 nm. (d) Comparison of the output pulse recovery times width τ_{out} (measured at $1/e$ of the pulse amplitude), the experimental recovery time $\tau_{rec,exp}$, and the derived recovery time $\tau_{rec,der}$. $\tau_{rec,exp}$ or $\tau_{rec,der}$ is obtained by fitting the experimental data or the derived curve with the sigmoid function $y = A(1 + \text{erf}(S(x - x_0)))$, where x_0 represents the fitted recovery time.

wavelength of 1548 nm. Figure 3(a) shows the recorded output pulses for various heater currents I_h . The experimental pulse widths match the trend in the simulation results. As the serial resistance increases by setting a higher heater current, the output pulse width declines from 15.3 ns at $I_h = 0 \mu\text{A}$ to 4.7 ns at $I_h = 74 \mu\text{A}$. The falling time constant, obtained by exponential fitting of the falling edge, reduces from 13.85 ns at $I_h = 0 \mu\text{A}$ to 4.04 ns at $I_h = 74 \mu\text{A}$, leading to a theoretical estimation of $R_s = 121.4 \Omega$ at $I_h = 74 \mu\text{A}$.

However, the efficiency recovery time does not monotonically decrease as the serial resistance increases. We experimentally measured the efficiency recovery curve (filled dots) as shown in Fig. 3(b) (see the measuring approach in the supplementary material). The experimental recovery time $\tau_{\text{rec, exp}}$ is obtained by fitting the experimental data to the sigmoid function $y = A(1 + \text{erf}(S(x - x_0)))$, where x_0 indicates the fitted recovery time. $\tau_{\text{rec, exp}}$ is suppressed from 20.3 ns at $I_h = 0 \mu\text{A}$ to 7.1 ns at $I_h = 64 \mu\text{A}$, which is consistent with the declining trend of the output pulse width. However, the recovery time increases to 7.9 ns at $I_h = 74 \mu\text{A}$ although τ_{out} becomes shorter. This results from the degradation of the internal detection efficiency of the detector. Figure 3(c) shows the internal detection efficiency (IDE) curves as a function of the bias current, which are normalized to the saturated efficiency estimated by fitting to the sigmoid function.²³ Since $I_h = 74 \mu\text{A}$ leads to an unsaturated IDE curve, a higher ratio of the effective bias current to the switching current I_d/I_{switch} , corresponding to a longer delay between two events, is required to achieve the same detection probability. This effect counteracts the influence of the shorter output pulse width, leading to a worse efficiency recovery time at $I_h = 74 \mu\text{A}$.

Since the power consumption of a cryogenic tunable resistor ($< 3 \mu\text{W}$) is much lower than the cooling power of the cryostat ($\sim 130 \text{ mW}$), the influence of local heating on the SNSPD performance can be dismissed (see the supplementary material). The change in the IDE curves is attributed to the decrease in the switching current $I_{\text{switch}} = \min\{I_c, I_{\text{latch}}\}$, where I_c is the critical current determined by the geometry of the nanowire and the critical current density of the superconducting film, and I_{latch} is the latching current at which the resistive spot in the superconducting nanowire is retained. As the output pulse width τ_{out} decreases, I_{latch} declines and eventually drops below I_c .^{18,24} This results in the reduction of I_{switch} , restricting the bias current

from approaching the critical current. Consequently, the saturation plateau of the internal detection efficiency diminishes as R_s exceeds an optimal value. The switching current increases slightly at $I_h = 62 \mu\text{A}$, which is consistently observed in multiple datasets. This phenomenon may result from voltage attenuation across the serial tunable resistor, which suppresses reflections in the SNSPD circuit.

Furthermore, we investigate the recovery of the instantaneous detection efficiency derived from the output pulses and the IDE curve in Figs. 3(a) and 3(c).²⁵ The instantaneous effective bias current can be estimated using $I_d(t) = I_{\text{bias}} - V_{\text{out}}(t)/G/Z_0$, where $V_{\text{out}}(t)$ is the output pulse and $G = 10^{2.9}$ is the measured gain of our amplifier. Combined with the IDE curve $\eta = f(I_d)$, the instantaneous detection efficiency can be derived as $\eta(t) = f(I_d(t))$, which is shown as solid lines in Fig. 3(b). Similar to the acquisition of the experimental recovery time, the derived recovery time $\tau_{\text{rec, der}}$ can be estimated by fitting the derived curve $\eta(t) = f(I_d(t))$ with the sigmoid function. Figure 3(d) shows the comparison between the pulse width τ_{out} , the experimental recovery time $\tau_{\text{rec, exp}}$, and the derived recovery time $\tau_{\text{rec, der}}$ at various heater currents. The result indicates that there is an optimal heater current I_h at $64 \mu\text{A}$, that is, an optimal serial resistance R_s , where the efficiency recovery time can be minimized by reducing the pulse width. Discrepancies between $\tau_{\text{rec, der}}$ and $\tau_{\text{rec, exp}}$ are probably caused by inaccurate estimations of effective bias currents or the triggering threshold for the second detection event (hysteresis settings of the counter's Schmitt trigger), which omits some events because of their lower amplitude or background noise (see the supplementary material). Nevertheless, $\tau_{\text{rec, der}}$ and $\tau_{\text{rec, exp}}$ show similar tendencies with respect to I_h , and exhibit good agreement under conditions of rapid efficiency recovery. Therefore, analyzing $\tau_{\text{rec, der}}$ can be considered a time-efficient approach to optimize the serial tunable resistor.

In order to further investigate the effect of recovery time reduction, we characterize the SNSPD detection efficiency at high count rates illuminated with a pulsed light source at 1548 nm at different heater currents. The detection efficiency, for each input photon flux, is calculated using the count rates at 95% of the switching current. Examples of the measurement curves (count rate vs bias current at different input photon fluxes) are presented in the supplementary material. The analyzed results are shown in Fig. 4 for the laser

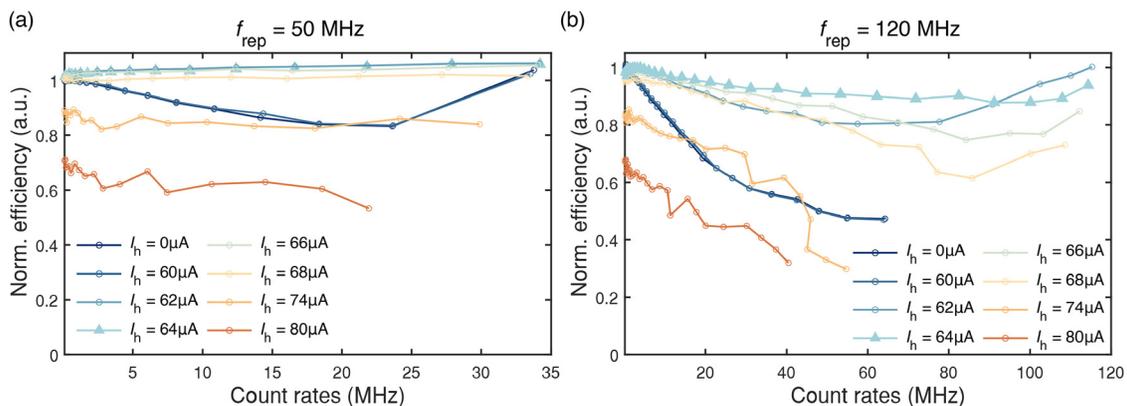


FIG. 4. Normalized detection efficiency as a function of detection count rates at various heater currents I_h with a repetition rate of (a) $f_{\text{rep}} = 50$ and (b) $f_{\text{rep}} = 120$ MHz. For each laser repetition rate, the best detector performance can be achieved by tuning the heater current. Measurements at $I_h = 64 \mu\text{A}$, which consistently delivers near-optimal or optimal performances across a broad range of count rates, are highlighted with triangles for a better display.

repetition rates of 50 and 120 MHz. Note that the data in Fig. 4 are normalized with respect to the efficiency at the lowest input photon flux (lowest count rate) when $I_h = 0 \mu\text{A}$ (more measurements can be found in the [supplementary material](#)).

According to the measurement in Fig. 3(d), the recovery time of the SNSPD is longer or comparable to $1/f_{\text{rep}}$ at lower heater currents than $60 \mu\text{A}$. Thus, the detection efficiencies for these lower heater currents decrease as the count rate increases (due to the insufficient current recovery through the SNSPD). When the count rates increase beyond half of f_{rep} , the efficiency exhibits an upward tendency, which is attributed to the increase in SNSPD current via parasitic discharging currents of the readout capacitor.²⁶ The parasitic currents flowing back through the SNSPD enhance the effective bias currents and thus the detection efficiency.

As the heater current increases, the best efficiency performance over a broad range of count rates is achieved at $I_h = 64 \mu\text{A}$, which is in accordance with the recovery time measurement. When the heater current exceeds $64 \mu\text{A}$, the efficiency declines steadily due to the unsaturated IDE (see the [supplementary material](#)). At $f_{\text{rep}} = 50 \text{ MHz}$ and $I_h = 64 \mu\text{A}$, the detection efficiency is maintained around the highest level at various count rates, since the minimal time delay between two pulses, which is 20 ns, is significantly longer than the recovery time $\tau_{\text{rec, exp}} = 7.1 \text{ ns}$, resulting in complete recovery of the effective bias current for all detection events. As the minimum separation between incident photons for the case of $f_{\text{rep}} = 120 \text{ MHz}$ approaches the recovery time (at $I_h = 64 \mu\text{A}$), and the efficiency recovery is not fully completed, a slight decline in efficiency can be observed at higher count rates. Nevertheless, the normalized detection efficiency remains at around 90% until a count rate of 114.2 MHz at $I_h = 64 \mu\text{A}$ for $f_{\text{rep}} = 120 \text{ MHz}$, while the normalized detection efficiency is decreased to 50% with a count rate of 48.0 MHz at $I_h = 0 \mu\text{A}$. Thus, there is a twofold increase in the maximum detection count rate.

In conclusion, we demonstrated a cryogenic tunable resistor controlled by the localized micro-heater. The device is intrinsically cryogenic-compatible and suitable for integration with other superconducting devices. We showed that, by controlling the tunable resistor in series with an SNSPD, the efficiency recovery time of the detector can be tuned to enhance detection efficiency at high count rates. Compared with trial-and-error-based fixed resistive networks, the cryogenic tunable resistor significantly speeds up the optimization process and can be adapted to a variety of SNSPD geometries or materials. Our solution provides a convenient method to tune SNSPD performance at higher photon fluxes. In addition, the tunable resistor presented here has the potential to be integrated with other cryogenic technologies.

See the [supplementary material](#) for details on sample fabrication, DC characteristics of the cryogenic tunable resistor, electrothermal simulation of an SNSPD with a serial resistor, the approach to measure efficiency recovery curves, more results of high-count-rate measurements, additional information about SNSPD output pulses, and measurements of the FWHM jitter.

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AUTHOR DECLARATIONS

Conflict of Interest

N. Noordzij (full time), J. W. Niels Los (full time) and I. Esmaeil Zadeh (part-time) are employed by Single Quantum B. V. and may profit financially. The authors have no conflicts to disclose.

Author Contributions

H. Wang: Conceptualization (equal); Data curation (equal); Formal analysis (equal); Investigation (lead); Methodology (equal); Resources (equal); Software (equal); Supervision (equal); Validation (equal); Visualization (equal); Writing – original draft (equal); Writing – review & editing (equal). **N. D. Orlov:** Data curation (equal); Formal analysis (equal); Investigation (equal); Writing – original draft (equal); Writing – review & editing (equal). **N. Noordzij:** Investigation (equal); Methodology (equal); Resources (equal); Writing – original draft (equal); Writing – review & editing (equal). **T. Descamps:** Investigation (equal); Resources (equal); Writing – original draft (equal); Writing – review & editing (equal). **J. W. N. Los:** Conceptualization (equal); Supervision (equal); Validation (equal); Writing – original draft (equal); Writing – review & editing (equal). **V. Zwiller:** Supervision (equal); Writing – original draft (equal); Writing – review & editing (equal). **I. Esmaeil Zadeh:** Conceptualization (equal); Funding acquisition (equal); Investigation (equal); Methodology (equal); Project administration (equal); Supervision (equal); Validation (equal); Visualization (equal); Writing – original draft (equal); Writing – review & editing (equal).

DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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