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# Compensation Network for a 7.7 kW Wireless Charging System that uses Standardized Coils

Francesca Grazian, Wenli Shi, Thiago B. Soeiro, Jianning Dong, Peter van Duijsen and Pavol Bauer

Delft University of Technology

Delft, The Netherlands

Email: (F.Grazian, W.Shi-3, T.BatistaSoeiro, J.Dong-4, P.J.vanDuijsen, P.Bauer)@tudelft.nl

Abstract—Industrial wireless charging systems use standardized coils to guarantee interoperability between different manufacturers. In combination with these coils, the compensation network can still be designed and optimized. This paper explains the step-by-step design of the compensation network for a 7.7 kW wireless charging system (power class WPT2), which is composed of standardized coils. The compensation network must satisfy the output power and voltage requirements, the soft-switching of the inverter, and the limit of voltage and current stress on the components. The S-S compensation network is found to be unfeasible for those coils, and an optimized double-sided LCC compensation network is designed. The 3-phase grid connection is selected despite the 1-phase one because it gives the lowest total conduction losses. Finally, two parallel SiC MOSFETs C3M0075120K are chosen as inverter's switch because of their low conduction losses. This solution can achieve a payback time within a year with respect to the cheapest one.

Index Terms—Compensation networks, electric vehicles (EVs), inductive power transfer, standardized coils, wireless charging.

### I. INTRODUCTION

Nowadays, standards and regulations on wireless charging for electric vehicles (EVs) are well defined, such that interoperability is guaranteed between systems from different manufacturers [1]. Especially for what concerns the main magnetic coils, reference designs are specified in SAE 2954 [2] for the power levels up to 11.1 kW (WPT3). Each of them presents both mechanical and electrical specifications. This means that the specification already defines the values of the inductance and coupling of the coils. From an industrial point of view, manufacturers commercialize coils which are designed based on the reference designs to make sure that their products are compatible with the standards.

When designing a wireless charging system using standardized coils, the compensation network can be still optimized. The aim of the compensation network is to minimize the reactive power circulating in the charging system. In this way, it is possible to transfer the required power to charge the battery and achieve high power efficiency. At the required power level, the compensation network must also ensures that the DC output voltage is in the range of the battery voltage, which is typically around 400V. At the same time, the DC input voltage must be in the range of the rectified voltage from the AC grid, which is either 340-500V DC in case of a 1-phase connection or 650-870V DC in case of a 3-phase one, for a grid with phase-to-neutral potential of 230 V rms. Moreover, the inverter's soft-switching needs to be guaranteed,

and the voltage and current stress on all components must be within reasonable values. Therefore, given the geometry and the electrical specifications of the coils, the designed compensation network needs to satisfy all these requirements.

This paper explains the design and the optimization of the compensation network for a 7.7 kW wireless charging system, which is composed of standardized coils of the power class WPT2. The coils parameters and the design constraints are summarized in Section II. After this, Section III explains the step-by-step design and optimization of the compensation network. Then, the stress on the components is computed through an accurate circuital model and time-domain simulations. The well-known series-series (S-S) compensation network is proved to be unfeasible with selected standardized coils for the required power level, and the double-sided LCC compensation network is found to be suitable. Different designs are analyzed and compared for what concerns the soft-switching of the inverter, the voltage and current stress, conduction losses of the components and cost. Finally, in Section IV, conclusions are given on the design of a 7.7 kW compensation network.

#### II. DESIGN REQUIREMENTS AND CONSTRAINTS

The compensation network has to be designed and optimized for a 7.7 kW wireless charging system with given coils. The equivalent circuit of the system is shown in Fig. 1. Standardized coils are used according to the power class WPT2 of SAE J2954 [2]. In particular:

- Transmitter: the coil is sealed in a concrete block to facilitate its on-ground mounting. Its geometrical and electrical characteristics are based on the universal ground assembly (GA) coil. This coil has been manufactured by MAGMENT [3].
- Receiver: the coil is based on the geometrical and electrical characteristics of the WTP2/Z2 class. This coil has been manufactured by PREMO [4].

Fig. 2 shows the picture of the two coils of the wireless charging system. The coils' parameters are constraints in the design of the whole wireless charging system. In particular,  $L_1$  and  $L_2$  define the transmitter (primary) and receiver (secondary) coil inductance;  $R_1$  and  $R_2$  are, respectively, their series resistances; M is the mutual inductance between the coils, and k is their coupling factor  $(k = \frac{M}{\sqrt{L_1 L_2}})$ . For what concerns the requirements, the output power  $P_{out}$ 

is defined by the receiver's coil power class. The DC output



Fig. 1. Equivalent circuit of a 7.7 kW wireless charging system, using as compensation network: a) S-S, b) double-sided LCC.



Fig. 2. Aligned transmitter and receiver coils. Dimensions are in mm.

voltage  $V_{out}$  is considered to be equal to the nominal voltage of a conventional battery used in automotive applications. The DC input voltage  $V_{in}$  must be within the grid's rectified voltage range. The nominal operating frequency  $f_0$  and its allowed range are defined in [2]. Another essential requirement is to have reasonable voltage and current stresses  $\hat{V}$ ,  $\hat{I}$  on the components, such that they do not exceed their capability and, at the same time, they guarantee proper operation and high efficiency of the power electronics. Finally, the compensation network needs to ensure the soft-switching of the inverter.

All these parameters of the constraints and requirements are summarized in Table I. The values regarding the coils are measured at the specified airgap and with perfect alignment.

#### **III. DESIGN PROCEDURE**

It is always preferable to choose a compensation network with the minimum number of components, such that the losses can be contained. From the literature, it is well-known that at least one capacitor must be connected to each coil. In this way, the reactive power that circulates in the system is minimized, and the power factor is closed to unity at both the input and output of the resonant stage. Depending on the type of connection, the most basic compensation network can be either series-series (S-S), series-parallel (S-P), parallel-series (P-S) or parallel-parallel (P-P) [5]–[10]. Among these combinations, the S-S compensation is the most used in wireless charging because the required value of both capacitors is independent of both coupling and loading condition [5], [9]–[11]. Its schematic is shown in Fig. 1a).

Compensation networks with multiple components can be also found in the literature, such as the double-sided LCC compensation network which the schematic is shown in Fig.

 TABLE I

 Design constraints and requirements.

	Pout	7.7 kW		$L_1$	63.35 µH
ıts	Vout	400 V	s	$L_2$	$43.53  \mu H$
ner	$V_{in}$	340-500 V (1φ) or	int	$R_1$	$187\mathrm{m}\Omega$
ren		650-870 V (3φ)	tra	$R_2$	$62\mathrm{m}\Omega$
qui	$f_0$	85 kHz (79-90 kHz)	ons	M	7.4 µH
Re	$\hat{V}, \hat{I}$	$< 5 \mathrm{kV}, 80 \mathrm{A}$	0	$_{k}$	0.14
	Q1Q4	Soft-switching		airgap	$150 \mathrm{mm}$

1b) [12]–[16]. According to [12], this network has several features. First, the current flowing through the primary coil does not depend neither on the coupling nor on the load condition. This property is preferable both in dynamic charging and when multiple receivers are present. After this, since the resonant voltage is shared by more components, their voltage stress is lower than in the S-S compensation. Moreover, since the components  $L_{f1}$ ,  $C_{f1}$  resonate at the nominal operating frequency, the inverter's current high-order harmonic components are highly attenuated. This ensures low distortion of  $I_1$  that is fundamental to fulfill the electromagnetic compatibility (EMC) limits for the radiated magnetic field [17].

First, it is verified the suitability of the S-S compensation network with the given coils. This is done by checking if it satisfies all the requirements in Table I. If not, the double-sided LCC compensation would be selected and designed.

#### A. S-S compensation network

The resulting circuit's parameters of the S-S compensation network are shown in Table II. By using (1) and (2), the S-S compensation network satisfies the output requirements on  $P_{out}$  and  $V_{out}$  specified in Table I. According to [5]– [10], the compensation capacitance of both the primary and secondary circuits are calculated as:  $C_1 = \frac{1}{\omega_0^2 L_1}$ ,  $C_2 = \frac{1}{\omega_0^2 L_2}$ . In both circuits, the components' voltages and currents can be computed with the Kirchhoff's voltage law in (3) and (4). These equations use the phasor convention, where all voltages and currents are assumed to be 85 kHz sinusoids, and  $V_{AB}$  is taken as reference such that  $V_{AB} = V_{AB}/0^{\circ}$ . The DC input voltage  $V_{in}$  can be found by using (5). Both (2) and (5) are based on the first harmonic approximation (FHA) [11], [18].

$$I_{out} = \frac{P_{out}}{V_{out}} \tag{1}$$

$$I_2 = \frac{\pi}{\sqrt{2} \cdot 2} I_{out} \tag{2}$$

$$\boldsymbol{V}_{AB} = (R_1 + j\omega L_1 + \frac{1}{j\omega C_1})\boldsymbol{I}_1 + j\omega M \boldsymbol{I}_2$$
(3)

$$0 = (R_2 + R_{ac} + j\omega L_2 + \frac{1}{j\omega C_2})I_2 + j\omega M I_1 \qquad (4)$$

$$V_{in} = \frac{\pi}{4} V_{AB} \tag{5}$$

To verify if the S-S compensation is suitable, the results of Table II must be compared to the requirements in Table I. It is possible to notice that the resulting DC input voltage  $V_{in}$  is not within any of the allowed ranges. There are two possible ways to overcome this issue.  $V_{in}$  could be stepped up by inserting a highly-coupled transformer between the compensation capacitor  $C_1$  and the coil  $L_1$ , with a turns ratio greater

TABLE II PARAMETERS OF THE S-S COMPENSATION NETWORK THAT SATISFY THE OUTPUT REQUIREMENTS ( $P_{out}$ ,  $V_{out}$ ) in Table I.

$V_{in}$	112.3 V
$C_1, C_2$	55.34 nF, 80.5 nF
$\hat{I}_1,  \hat{I}_2$	129.7 A, 30.2 A
$\hat{V}_{L1},  \hat{V}_{L2}$	4.4 kV, 703 V
$\hat{V}_{C1}, \hat{V}_{C2}$	4.4 kV, 703 V

than the unity. However, this solution is not ideal because the losses in the transformer would be considerable due to the high-frequency operation. Another possibility would be to add a DC/DC converter in either the primary or secondary side. However, this would increase the cost and reduce the total efficiency. Additionally, with any of these solutions, the requirements on  $\hat{I}_1$  would not be satisfied because it exceeds the maximum rated value for the primary coil.

It can be concluded that the S-S compensation network is not preferable in this application. This means that the doublesided LCC compensation network should be investigated.

#### B. Double-sided LCC compensation network

The circuit parameters' definition and detailed analysis of the double-sided LCC compensation can be found in [12]. The parameters' definition is summarized in (6). However, in [12], the value of the parameters is chosen only for a completely symmetrical system, and not for a generalized approach.

$$L_{fi}C_{fi} = \frac{1}{\omega_0^2}, \quad L_i - L_{fi} = \frac{1}{\omega_0^2 C_i} \qquad i = 1, 2$$
 (6)

The values of  $L_1$ ,  $L_2$ , and M are known, which are listed in Table I. The values of  $L_{f1}$ ,  $L_{f2}$  need to be selected according to the requirements on  $P_{out}$  and  $V_{out}$ . After this, the values of  $C_1$ ,  $C_2$ ,  $C_{f1}$  and  $C_{f2}$  can be computed as shown in (6).

The double-sided LCC compensation has a constant-current output characteristic, since  $\hat{I}_{L_{f2}}$  is load-independent [12]:

$$\hat{I}_{L_{f2}} = \frac{M \frac{4}{\pi} V_{in}}{\omega_0 L_{f1} L_{f2}} \tag{7}$$

To make sure that the double-sided LCC meets the output requirements, the relation between  $\hat{I}_{L_{f2}}$  and  $I_{out}$  is described by (8). This is based on [18] that assumes a continuous conduction of the rectifier's diodes in Fig. 1a).

$$\hat{I}_{L_{f2}} = \frac{\pi}{2} I_{out} \tag{8}$$

By substituting (7) and (1) into (8), it can be found that

$$L_{f1}L_{f2} = \frac{8}{\pi^2} \frac{MV_{in}V_{out}}{\omega_0 P_{out}} \tag{9}$$

In (9), there are two unknown variables: the ratio between  $L_{f1}$  and  $L_{f2}$ , and the input voltage  $V_{in}$ . The ratio between  $L_{f1}$  and  $L_{f2}$  can be found by choosing the desired amount of coils' current. In this case, the coils are chosen to carry the same amount of current to keep the components' voltage and current stress under control. Since [12] defines  $\hat{I}_1$  and  $\hat{I}_2$  as in (10), the condition that makes  $\hat{I}_1 = \hat{I}_2$  is (11). At this



Fig. 3. Simulated  $V_{AB}$  and  $I_{L_{f1}}$  at: a)  $V_{in} = 380$  V, b)  $V_{in} = 650$  V.

point,  $L_{f1}$  and  $L_{f2}$  can be calculated by using (9) and (11). However, before that, the proper  $V_{in}$  needs to be selected.

$$\hat{I}_1 = \frac{\frac{4}{\pi} V_{in}}{\omega_0 L_{f1}}, \qquad \hat{I}_2 = \frac{\frac{4}{\pi} V_{out}}{\omega_0 L_{f2}}$$
 (10)

$$\frac{L_{f2}}{L_{f1}} = \frac{V_{out}}{V_{in}} \tag{11}$$

According to (9), it can be found that  $V_{out}$  is directly proportional to both M and  $V_{in}$ . This means that, when the coils become more misaligned, the target  $P_{out}$  can be reached by increasing  $V_{in}$ . Therefore, at the highest coupling condition, the compensation network has to be tuned at the minimum  $V_{in}$ . Table I shows two possible ranges of  $V_{in}$  depending on the grid connection. Herein, it is assumed an operation at  $V_{in} = 380 \text{ V}$ for the  $1\phi$  grid connection, while  $V_{in} = 650 \,\mathrm{V}$  for the  $3\phi$ grid connection. To identify which grid connection is the most suitable, a double-sided LCC compensation network is designed for both minimum values of  $V_{in}$  based on (6), (9) and (11). Circuit simulations in the time domain show that  $I_{L_{f1}}$  and  $I_{L_{f2}}$  are distorted. This happens because the LCC components have multiple frequencies that influence the resonance at 85 kHz. This means that (9) and (11) are not accurate since they are based on the FHA. Therefore, to reach  $P_{out} = 7.7 \,\text{kW}$ , a higher value of  $P_{out}$  has been used in (9).

To make a fair comparison, both solutions are designed to have soft-switching operation. As explained in [12], [18]–[21], the zero-voltage switching (ZVS) turn-on can be realized by switching off the inverter legs at a turn-off current  $I_{OFF}$  that is high enough to completely discharge the equivalent drainsource internal capacitance  $C_{ds}$  of the MOSFETs during the dead time  $t_{dead}$ . To perform the comparison, the two designs are tuned to the same  $I_{OFF}$  as it is shown in Fig. 3. By using (6), (9) and (11), the design at  $V_{in} = 650$  V reaches the operating condition shown in Fig. 3b). On the other hand, at  $V_{in} = 380$  V, to realize the same  $I_{OFF}$ , the capacitance  $C_2$  has been increased by 6.1 nF as explained in [12]. Since the value of  $I_{OFF}$  depends on  $V_{ds,off}$ ,  $C_{ds}$  and  $t_{dead}$ ,  $I_{OFF}$ varies for different MOSFETs and  $V_{in}$ . Hereby, this is not considered because it is out of the scope of the paper.

The components used for both the inverter and the compensation network are listed in Table III. For both possible  $V_{in}$ , two SiC MOSFETs are considered that differ in current rating and, consequently, in price. The resulting circuit's parameters for both designs of the double-sided compensation network are shown in Table IV, where the number and type of passive components are specified within the brackets. The two designs are analyzed for what concerns the components' voltage  $\hat{V}$  and

	Comp.	Manifacturer + n.	Cost (\$)	$R_s~(\Omega)$
SiC	SW1	Cree/Wolfspeed C3M0120090D	6.025 <sup>1</sup>	0.156 <sup>4</sup>
	SW2	Cree/Wolfspeed C3M0065090D	9.375 <sup>1</sup>	$0.083^{4}$
	SW3	Cree/Wolfspeed C2M0160120D	7.812 <sup>1</sup>	$0.264^4$
	SW4	Cree/Wolfspeed C3M0075120K	11.875 <sup>1</sup>	$0.094^{4}$
	6.8 nF	EPCOS B32671L0682J000	$0.160^{2}$	$0.100^{5}$
TCC	3.3 µH	Würth Elektronik 7443640470	6.832 <sup>3</sup>	$0.073^{5}$
	4.7 μH	Würth Elektronik 74436410330	6.832 <sup>3</sup>	$0.090^{5}$

TABLE III COMPONENTS USED FOR THE INVERTER AND COMPENSATION NETWORK.

N. of units: <sup>1</sup>500+, <sup>2</sup>5000+, <sup>3</sup>270+, from Digi-key [22], <sup>4</sup>Datasheet @125 °C, <sup>5</sup>Measured @85 kHz. TABLE IV

Parameters of the double-sided LCC compensation network for both possible  $V_{in}$ .

$I_{AA} = 14.17 \text{ uH} (4.7, 3) \text{ uH} = 23.54 \text{ uH} (3.3, 3)$	
$L_{f1} = 14.17 \mu I (4.7 \cdot 5) \mu I = 25.54 \mu I (5.5 \cdot 6)$	$3 + 4.7 \cdot 3)\mu H$
$L_{f2}$ 14.91 µH (same as in 3 $\phi$ ) 14.49 µH (3.3 · 3	$3 + 4.7 \cdot 1)\mu H$
$C_{f1}$ 247.50 nF (6.8 nF · 2 × 72) 148.93 nF (6.8	$nF \cdot 2 \times 44)$
$C_{f2}$ 235.13 nF (6.8 nF · 2 × 70) 242.02 nF (6.8	$nF \cdot 2 \times 72)$
$\check{C}_1$ 71.28 nF (6.8 nF · 4 × 42) 88.07 nF (6.8 r	$F \cdot 4 \times 52$ )
$\label{eq:C2} C_2 \qquad 128.60\mathrm{nF}(6.8\mathrm{nF}\cdot3\times57) \qquad 120.70\mathrm{nF}(6.8\mathrm{nF}\cdot3\times57)$	$nF \cdot 3 \times 53)$

TABLE V

Power Loss $P_{loss}$ and	ND COST	OF THE	COMPENSATION	AND	INVERTER
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$V_{in}$	38	0 V	650 V		
	$P_{loss}$ (W)	Cost (\$)	$P_{loss}$ (W)	Cost (\$)	
Coils	508.8	-	539.0	-	
LCC	400.2	147.48	364.1	164.14	
$\Sigma_{L,C}$	909.0	147.48	903.1	164.14	
SIC	SW1/SW2	SW1/SW2	SW3/SW4	SW3/SW4	
SIC	100.2/53.3	48.20/75.00	71.7/25.5	62.5/95.0	
Total	1009.2/962.3	195.68/222.48	974.8/928.6	226.64/259.14	

current  $\hat{I}$  stress, conduction power losses  $P_{loss}$  and total cost. Table V shows the total  $P_{loss}$  and cost of all designs, where the partial quantities of the main coils, compensation network and inverter are highlighted. The cost of the main coils is not considered here, because it is the same for all designs and it is not interesting for the comparison. Additionally, the details of each passive component are shown in Table VI. At  $V_{in} = 650 \,\mathrm{V}$ , the current stress on the inverter  $\hat{I}_{L_{f1}}$  is about 30% lower than in the case of  $V_{in} = 380$  V. On the other hand, at  $V_{in} = 380$  V, the current stress on both the main coils  $I_1, I_2$ is about 3% lower which leads to lower  $P_{loss}$  than in the case of  $V_{in} = 650$  V. From Table V, it can be concluded that the total  $P_{loss}$  on the passive components  $\Sigma_{L,C}$  is slightly lower (0.65%) for the  $3\phi$  grid connection ( $V_{in} = 650$  V) at a cost that is 11% higher. Considering also the  $P_{loss}$  of the inverter, the case of  $V_{in} = 380 \,\mathrm{V}$  with SW4 leads to the lowest total losses and most expensive design. To evaluate if the extra cost can be amortized with the time, the economical analysis of all the designs is performed in Table VII. The payback time is calculated taking as a reference the most affordable design  $(V_{in} = 380 \text{ V} \text{ with SW1})$ . As a result, the payback time of the most expensive and efficient solution with  $V_{in} = 650 \text{ V}$  and SW4 is less than one year, which is much lower than the design lifetime of the wireless charging system. In a future work, this analysis will be verified experimentally with a prototype, where the primary circuitry is shown in Fig. 4.

#### IV. CONCLUSION

This paper explains the design and optimization of the compensation networks for a 7.7 kW wireless charging system which uses standardized coils of the power class WPT2. The conclusions can be summarized as follows:

 $\begin{array}{c} \text{TABLE VI} \\ \text{Voltage } \hat{V} \text{ and current } \hat{I} \text{ stress, power loss } P_{loss} \text{ and cost of} \\ \text{the circuit parameters in Table IV, for both possible } V_{in}. \end{array}$ 

	$V_{in}$		$380 \mathrm{V}$	$650\mathrm{V}$		380 V	$650\mathrm{V}$
$L_f$	$\hat{I}$ (A)		39.9	28.2		39.1	39.4
	$\hat{V}$ (V)		896	896		619	607
	$P_{loss}$ (W)		173.1	133.5		178.2	179.4
	Cost (\$)		20.50	41.00		27.30	27.30
	$\hat{I}$ (A)	1	82.8	77.2	1	84.7	86.4
C.	$\hat{V}$ (V)		609	990		669	665
$C_f$	$P_{loss}$ (W)	imary	8.8	13.5	ary	9.8	10.0
	Cost (\$)		23.04	14.08	puc	22.40	23.04
	$\hat{I}$ (A)	pr	63.7	65.4	sec	63.4	65.2
т	$\hat{V}$ (V)		2257	2375		1582	1664
Ľ	$P_{loss}$ (W)		382.7	405.5		126.1	133.5
	Cost (\$)		[3]	[3]		[4]	[4]
C	$\hat{V}$ (V)		1683	1402		933	1020
	$P_{loss}$ (W)		19.5	16.7		10.7	12.2
	Cost (\$)		26.88	33.28		27.36	25.44

TABLE VII

ECONOMICAL EVALUATION BASED ON THE DAILY POWER CONSUMPTION.

$V_{in}$		$P_{in}^1$	$E_{in}/day^2$	Cost/day <sup>3</sup>	Payback $t^4$
380 V	SW1	8.71 kW	70.1 kW h	16.13\$	0 years
380 V	SW2	8.66 kW	69.7 kW h	16.04\$	0.84 years
650 V	SW3	8.67 kW	69.8 kW h	16.06\$	1.31 years
050 V	SW4	8.63 kW	69.46 kW h	15.98\$	0.67 years

<sup>1</sup>  $P_{in} = P_{out} + P_{loss(total)}$  according to Table I and V.

<sup>2</sup> One EV with a battery of 62 kWh is fully charged per day.

<sup>3</sup>Calculated with the 2019 electricity price (0.23\$/kW h) of the Netherlands

4  $P_{m}$  back  $t = \frac{Cost LCC + SWn - Cost LCC + SW1}{Cost LCC + SW1}$  where n = 1

<sup>4</sup> Payback  $t = \frac{Cost_LCC+SWn - Cost_LCC+SW1}{365 \cdot (Cost/day_{SW1} - Cost/day_{SWn})}$ , where n = 1...4.

![](_page_5_Figure_21.jpeg)

Fig. 4. Primary-side prototype to be used with the coils in Fig. 2.

- The series-series compensation network is proved to be unfeasible with the selected coils because, to match the output requirements, it demands a DC input voltage that is below the minimum value allowed by any grid connection. It also causes a high current stress at the primary circuit which is higher than the coil's rated value.
- The double-sided LCC compensation network is found to be more suitable for this application. The 3-phase grid connection is selected, because the DC input voltage range 650-870 V ensures soft-switching of the inverter at 85 kHz, low current stress on the inverter and the lowest total conduction losses on the passive components.
- Two parallel 1200V SiC MOSFETs C3M0075120K are chosen as switch of the inverter because of their low conduction losses. Taking as a reference the cheapest design, this solution has a payback time less than one.

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