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Impact of Classical Control Electronics on Qubit Fidelity

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Quantum processors rely on classical electronic controllers to manipulate and read out the state of quantum bits (qubits). As the performance of the quantum processor improves, nonidealities in the classical controller can become the performance bottleneck for the whole quantum computer. To prevent such limitation, this paper presents a systematic study of the impact of the classical electrical signals on the qubit fidelity. All operations, i.e., single-qubit rotations, two-qubit gates, and readout, are considered, in the presence of errors in the control electronics, such as static, dynamic, systematic, and random errors. Although the presented study could be extended to any qubit technology, it currently focuses on single-electron spin qubits, because of several advantages, such as purely electrical control and long coherence times, and for their potential for large-scale integration. As a result of this study, detailed electrical specifications for the classical control electronics for a given qubit fidelity can be derived. We also discuss how qubit fidelity is affected by the limited performance of the general-purpose room-temperature equipment typically employed to control the few qubits available today. Ultimately, we show that tailor-made electronic controllers can achieve significantly lower power, cost, and size, as required to support the scaling up of quantum computers.

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I. INTRODUCTION

Quantum computers have the potential to solve problems that are intractable even for the most powerful supercomputers [1]. These problems include the factorization of prime numbers using Shor's algorithm [2], the efficient search in large data sets using Grover's algorithm [3], and the simulation of quantum systems as initially proposed by Feynman [4]. A quantum computer operates by processing the information stored in quantum bits (qubits), which are organized in a quantum processor. Performing operations on the qubits requires a classical electronic controller for manipulating the qubits and reading out their quantum state [5]. In order not to degrade qubit performance, the classical controller must provide high-accuracy low-noise control signals and the readout

must be very sensitive and quiet to detect the weak signals from the quantum processor without altering the qubit states.

Since state-of-the-art quantum processors comprise only a few qubits (< 20 qubits [6–8]), the classical controller is currently typically implemented by general-purpose instruments operating at room temperature or tailor-made room-temperature controllers [9,10]. For a recent review of state-of-the-art electronic interface for quantum processors, see Ref. [11]. The use of these high-performance instruments results in the fidelity of the quantum operations being limited by the quantum processor [12]. However, as the performance of the quantum processor improves, the classical controller can become performance limiting. Consequently, it is crucial to understand how the controller impacts the performance of the whole quantum computer in order to properly codesign the controller and the quantum processor and to identify potential performance bottlenecks.

Moreover, the simplest nontrivial algorithms, such as quantum-chemistry problems, require more than 100 logical qubits [13]. This translates into the need for thousands or millions of physical qubits, if the redundancy added by

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quantum-error correction schemes, such as surface codes [14], is taken into account. For such large-scale quantum processors, implementation of the classical controller with general-purpose instruments would be impractical and would offer limited scalability due to its size and cost. A more practical and power-efficient approach would be to use tailor-made electronics that can be optimized for this specific application in terms of power consumption, form factor, and cost [5,11,15–23]. However, defining the specifications to design such electronics requires a comprehensive analysis of the impact of the electronics performance on the quantum computer.

Furthermore, solid-state qubits need to be cooled to deep cryogenic temperatures. When operating the control electronics at cryogenic temperatures to relax the wiring requirements between the cryogenic quantum processor and its controller, as proposed in Refs. [5,15–20], the need for accurate specifications is even more severe. The power dissipation of such a cryogenic controller is limited by the cooling power of the cryogenic refrigerator. For existing fridges, this is only about 1 W at 4 K and < 1 mW below 100 mK [24]. Although this could improve in the future by, e.g., adopting custom-made refrigerators [25], the power consumption of the controller is also expected to increase to serve an increasing number of qubits. To meet these cooling constraints, the power dissipated by the electronics must be minimized by optimally allocating the available power across the various components of the classical controller. However, carrying out such optimization also demands a clear understanding of the impact of each component on the quantum-computer performance.

Analysis of the impact of the controller on the quantum computer's performance has been undertaken previously, but only for specific aspects of the control signals, i.e., the effect of microwave phase noise [26,27], or for theoretically treating the effects of noise on qubit operations in a filter function framework [28,29], and control sweet spots [30–32] and noise effects in general [33–35]. The work presented here aims to provide a comprehensive analysis of the effect of nonideal circuit blocks in the classical controller on the qubit fidelity for all possible operations, i.e., single-qubit gates, two-qubit gates, and readout. Initialization is assumed to be performed by relaxation or by readout and will not be separately discussed. This includes the effect of signal inaccuracies in the frequency, voltage, and time domain, and covers static, dynamic, systematic, and random errors. Only with a full set of specifications can potential bottlenecks be identified and tailor-made electronics designed. Besides providing a general method for deriving the electronics specifications, the specifications resulting from a case study targeting a 99.9% average gate fidelity are mapped onto existing room-temperature integrated circuits (IC) to assess the feasibility of a practical controller.

Although the proposed approach can be easily extended to any quantum technology, such as NMR [36–38], ion traps [8,39], superconducting qubits [7,40,41], or nitrogen-vacancy (N-V) centers in diamond [42], we focus on the specific case of single-electron spin qubits. This qubit technology offers promising prospects for large-scale quantum computing, due to the long coherence times [12,43], the fully electrical control [44,45], and the potential integration of the quantum processor with a classical controller on a single chip fabricated using standard microelectronic technologies [46]. In addition, the results obtained for the single-qubit gates can be generalized to any qubit system where single-qubit rotations are performed by applying a signal with a frequency matching the energy-level spacing between the $|0\rangle$ and $|1\rangle$ states, e.g., for NMR [36–38], ion traps [8,39], N-V centers in diamond [42], and superconducting qubits [7,40,41]. Similarly, the results obtained for the two-qubit gates can be generalized to any qubit system that exploits the exchange gate.

The paper is organized as follows: Sec. II describes the generalized spin-qubit quantum computer analyzed in this paper; Sec. III introduces the method for deriving the fidelity for the various operations; and in Secs. IV, V, and VI the electrical specifications required for single-qubit operations, two-qubit operations, and qubit readout are derived, respectively. A discussion regarding the feasibility of these specifications follows in Sec. VII. Conclusions are drawn in Sec. VIII.

II. A SYSTEM-LEVEL VIEW OF A QUANTUM COMPUTER

A. The quantum processor

A single-electron spin qubit encodes the quantum state in the spin state of a single electron. A generic model of a quantum processor based on single-electron spin qubits is shown in Fig. 1(a), which captures all of the properties relevant for the interaction with the controller. Moreover, the figure illustrates a linear array of quantum dots but this can be extended to more complex geometries such as a 2D grid of quantum dots, as shown in Refs. [46–48].

Quantum dots are formed using a set of gate electrodes that locally deplete a two-dimensional electron gas (2DEG) on a semiconductor chip (e.g., a GaAs/(Al,Ga)As heterojunction, a Si/SiGe heterojunction, or a Si-metal-oxide semiconductor (MOS) structure [49,50]). Due to the small size of the quantum dot, the charge states become discrete with an energy-level spacing related to the dot charging energy, thereby setting the required increase of the dot potential to add an electron to the dot. The dot potential, and thereby the number of electrons in the dot, is controlled by the plunger gate that capacitively couples to the quantum dot. Without loss of generality for the analysis of the electrical control signals, the following analysis assumes the availability of additional tunnel-barrier gates

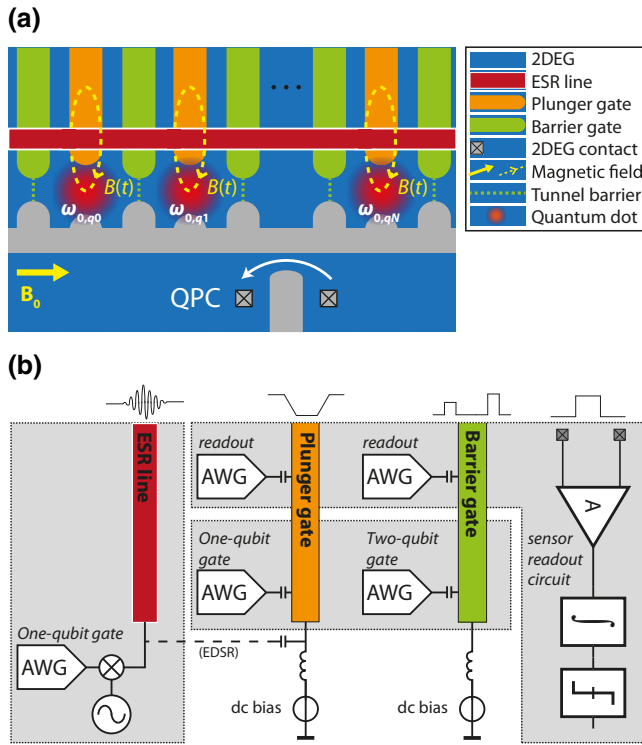


FIG. 1. (a) A generic model of a spin-qubit quantum processor comprising qubits encoded in the spin of electrons trapped in quantum dots and a charge sensor (e.g., a quantum point contact, QPC). The blue background indicates the two-dimensional electron gas (2DEG) where quantum dots, shown in red, are formed locally. Individual control over the dot potential and the tunnel barriers is assumed, using plunger gates (orange) and barrier gates (green), respectively. Furthermore, each qubit can have a unique Larmor frequency ($\omega_{0,qi}$). (b) The classical control electronics required for each line type (electron-spin-resonance (ESR) line, plunger gate, and barrier gate) of the quantum processor. The electronic components in the figure are placeholders for the respective functionalities and are grouped by operation, i.e., single-qubit operation, two-qubit operation, and readout. Thus, they do not necessarily correspond to a physical implementation. Arbitrary-waveform generators (AWG) are shown for the envelope and pulse generation.

that form tunnel barriers between neighboring dots by controlling the width of the depletion layer, thus allowing tunneling of electrons from and to the quantum dot in a tunable way. Early integration schemes involved nonoverlapping gates [as shown in Fig. 1(a)] [44,51,52], while, in order to create better tunability and control, architectures now often include overlapping gates [45,53–55].

An external static magnetic field \mathbf{B}_0 induces an energy difference between electrons with spin up and spin down, with Zeeman energy E_z . Because of the static magnetic field, the electron rotates around the Z axis in the Bloch sphere with Larmor frequency $\omega_0 = \gamma_e |\mathbf{B}_0|$, where γ_e is the gyromagnetic ratio of the electron ($\gamma_e \approx 28$ GHz/T in silicon). As indicated in Fig. 1(a), each qubit can

have a different Larmor frequency, which can be useful for two-qubit operations or for multiplexing single-qubit operations [45,55–57].

Single-qubit operations (Sec. IV) require the application of a varying magnetic field perpendicular to \mathbf{B}_0 and oscillating at the Larmor frequency. In the case of electron spin resonance (ESR), such a field is generated by a varying current in a nearby ESR line [45,55,58]. Alternatively, the same effect can be obtained, e.g., by applying a varying electric field to the electron in a spatial magnetic field gradient, as is the case for electric-dipole spin resonance (EDSR) [44,52,59,60]. In that case, the electric field variations are generated by a voltage on a nearby gate, e.g., through the plunger gate, without requiring an ESR line. Although Fig. 1(a) shows an ESR line, the results of the analysis below are applicable to both ESR- or EDSR-based operations, as explained in the following section. Two-qubit operations (Sec. V) and qubit readout (Sec. VI) can be performed by pulsing the barrier and plunger gates.

The effect of cross talk between different gates or the ESR line is considered negligible or compensated for in the classical controller and is not further discussed here, since it can be treated as a purely classical electrical effect.

Qubit readout relies on a spin-to-charge conversion, followed by the detection of the eventual electron movement [49], using either a gate-dispersive readout [61] or an additional charge sensor. The latter is assumed in this paper, as gate-based readout is still under development [62]. For such a charge sensor, different sensing techniques can be used, e.g., a QPC [63–65] or a single-electron transistor (SET) [66]. As an example, Fig. 1(a) shows a QPC in close proximity to the quantum dots.

B. The classical electronic controller

The classical controller is responsible for generating the required electrical signals to bias and control each gate and, in case, the ESR line, and for reading the state of the charge sensor. The required electronics are schematically summarized in Fig. 1(b).

When no operation is performed, each quantum dot must contain a single electron at the same dot potential and the tunnel barriers must be tuned to ensure a negligible coupling between neighboring dots (Sec. VB). Such conditions are ensured by the use of bias-voltage generators, as shown in Fig. 1(b).

The oscillating magnetic field $B(t)$ required for single-qubit operations can be generated by an oscillating current $I(t)$, following the relation $B(t) = \alpha_I I(t)$ (in the case of ESR), or by an oscillating voltage $V(t)$, resulting in $B(t) = \alpha_V V(t)$ (in the case of EDSR). The conversion factors α_I and α_V depend on many factors, such as the exact geometry of the structures, and can be determined experimentally. Due to this abstraction, the results shown in Sec. IV are valid both in the case of ESR and EDSR.

The required microwave current or voltage is generated by modulating a carrier from a local oscillator (LO) with an envelope produced by an AWG. Although different hardware implementations are possible, this allows us, without loss of generality, to split the carrier specifications, i.e., the local oscillator specifications, from the envelope specifications, i.e., the AWG specifications. In the case in which each qubit has a unique Larmor frequency, a single control line can be used to control multiple qubits independently via frequency-division multiple access (FDMA), i.e., frequency multiplexing, thus simplifying the wiring (Sec. IV B).

The voltage pulses required for the two-qubit gates and readout are generated by AWGs. Distinct AWGs are assumed for two-qubit gates and readout since the specifications for such operations can be different.

Besides the presented control electronics, additional hardware is required to process the signal from the charge sensor. The required hardware depends on the readout method employed, e.g., a direct measurement [64] or radio-frequency (rf) reflectometry [61,63]. As an example, a direct readout, requiring a readout amplifier, is shown in Fig. 1(b).

III. METHODS

The evolution of the qubit state is evaluated by computing the system Hamiltonian (H), which is a function of the electrical signals applied by the classical controller. For static control signals, the Hamiltonian is time independent and the unitary operation describing the evolution after a time T is trivially $U = e^{-iHT}$ ($\hbar = 1$).

For dynamic signals, such as for complex signal envelopes, the operation described by the time-varying Hamiltonian $H(t)$ is approximated by the product of time-independent components, leading to

$$U \approx \prod_{n=N}^0 e^{-iH(n\Delta t)\Delta t}, \quad (1)$$

where Δt is the time step, which must be chosen to be small enough for the required accuracy of the approximation.

As a benchmark to evaluate how close U is to the operation from an ideal controller U_{ideal} , the process fidelity is computed as follows [67,68]:

$$F = \frac{1}{n^2} |\text{Tr}[U_{\text{ideal}}^\dagger U]|^2, \quad (2)$$

for the n -dimensional complex Hilbert space ($n = 2$ for the single-qubit gate and $n = 4$ for the two-qubit gate).

In case of random dynamic errors, the ensemble average over all realizations is evaluated, following Refs. [28] and [29]. When treating random noise, the noise spectrum is relevant, as the operation can be affected differently by

noise at different frequencies. The method presented in Refs. [28] and [29] is used to evaluate the expected process fidelity and is outlined in the Supplemental Material [69].

Detailed derivations of the analytical results presented in this paper are collected in the Supplemental Material [69]. The analytical results presented in the main text are often the result of a series expansion of the fidelity truncated at the second order, as clearly stated in the Supplemental Material [69]. Since this study is focused on high qubit fidelity ($> 90\%$), higher-order processes can be neglected. All presented results are verified by numerical simulations of the Hamiltonian.

IV. SIGNAL SPECIFICATIONS FOR SINGLE-QUBIT OPERATIONS

A. Fidelity of a single-qubit operation

As explained in Sec. II, the qubit rotates around the Z axis due to the applied external magnetic field. Using an LO tuned to a frequency equal to the qubit's Larmor frequency, the qubit phase can be tracked and the qubit appears to be stationary in the reference frame of the LO. In this rotating frame, Z rotations by an angle θ_Z can easily be obtained by instantaneously updating the LO's phase in software by an angle θ_Z [37,38]. For such a software-defined Z rotation, only the accuracy of the phase update of the LO matters, which is limited by the finite resolution in the phase setting. A phase error $\Delta\phi = \Delta\theta_Z$ reduces the fidelity of the Z rotation as follows:

$$F_{Z_\phi} = 1 - \frac{1}{4} \Delta\phi^2. \quad (3)$$

In the remainder of this section, we will focus on rotations around the X or Y axis that are obtained by applying a magnetic field $B(t)$ oscillating at the qubit Larmor frequency ω_0 and with a specific phase, which is generated by applying either a microwave current or a microwave voltage, as explained in Sec. II. The Hamiltonian describing a single electron under microwave excitation in the laboratory frame is given by the following ($\hbar = 1$):

$$H_{\text{lab}} = -\omega_0 \frac{\sigma_z}{2} + \gamma_e B(t) \frac{\sigma_x}{2}, \quad (4)$$

where, here and in the following, σ_x , σ_y , and σ_z are the Pauli matrices. The microwave magnetic field can be described as $B(t) = 2/\gamma_e \omega_R(t) \cos(\omega_{\text{mw}}t + \phi)$. A constant amplitude $[\omega_R(t) = \omega_R]$, i.e., a rectangular envelope, is considered, unless stated otherwise. In the case of a rectangular envelope, ω_R is the Rabi frequency, i.e., the rotation speed for the single-qubit gate. Note that for more complex envelopes, the resulting specifications for the control electronics can differ, as the sensitivity to certain control parameters can be reduced when employing quantum optimum control, such as GRAPE [70].

For single-qubit rotations, the rotation axis is affected by the matching of the microwave frequency (ω_{mw}) to the Larmor frequency (ω_0) and by the phase of the microwave signal (ϕ), i.e., the carrier signal. The rotation angle ($\theta = \omega_R T$), on the other hand, is determined by the amplitude of the signal (ω_R) and the duration for which the microwave signal is applied (T), i.e., the signal envelope.

Table I summarizes the effect of noise and inaccuracy on the fidelity of single-qubit operations. Inaccuracies in the control signal are generally caused by instrument finite resolution and drift [71]. In the case of envelope inaccuracies, the microwave amplitude ($\propto \omega_R$) and duration (T) of the signal together determine the rotation angle ($\theta = \omega_R T$). Hence, any error in either one leads to an under or over rotation, thereby reducing the fidelity. In the case of frequency inaccuracies, a better fidelity is achieved for larger Rabi frequencies, i.e., a larger microwave amplitude and a shorter pulse duration. However, a larger Rabi frequency ultimately requires a sufficiently larger Larmor frequency [72] and it is harder to reach the same phase accuracy $\Delta\phi$ at higher LO frequencies.

Next, dynamic changes in the control signal can further limit the fidelity. In the event that such a change occurs on a time scale larger than the operation time, it can be considered a random static error. For a static but random error Δ for which $F = 1 - c\Delta^2$, the expected fidelity simply follows as $F = 1 - c\sigma^2$, if Δ follows a Gaussian distribution with standard deviation σ and zero mean (see the Supplemental Material [69]). Hence the equations for the inaccuracy as given in Table I apply.

In order to treat random noise with spectral content at frequencies higher than the operation rate, the method presented in Refs. [28] and [29] is adopted to compute the expected operation fidelity as a function of the noise spectrum. The results for dephasing noise are reproduced from Refs. [26,28,29] and are repeated here for completeness, with additional analysis for different rotation angles. The fidelity due to the various noise sources is summarized in Table I, where generally $|H(\omega)|^2$ is the intrinsic qubit-filter function, implying that the qubit has a different sensitivity to noise at different frequencies. The amplitude response of the intrinsic qubit-filtering functions for frequency noise and amplitude noise are shown in Fig. 2, with analytical formulas provided in the Supplemental Material [69]. These responses have a low-pass filter (LPF) characteristic and their properties, the dc gain and the effective noise bandwidth (ENBW), are summarized in Table I and highlighted in the plots as the brick-wall approximation of the filter [73].

Note that for frequency noise, the ENBW is proportional to the Rabi frequency, indicating that for faster operations, noise in a wider band affects the qubit. However, the lower limit of integration (ω_{min}) is inversely proportional to the execution time of the quantum algorithm [74] and is therefore also related to the operation time. In the

TABLE I. The fidelity of a single-qubit operation for inaccuracies and noise in the electrical control signals. In this table, θ is the intended rotation angle, ranging from $-\pi$ to π ; inaccuracies are denoted by Δ ; noise power spectral densities (PSDs) by $S(\omega)$; qubit-filter transfer functions by $H(\omega)$; and ω_{min} denotes the lower limit of integration. Note that in the case of jitter, the full transfer function, with T_{clk} as the reference period, is given, as no effective noise bandwidth (ENBW [72]) is defined for a HPF.

Carrier	Noise-filter properties			
	Inaccuracy	Noise	Type	dc gain $ H(0) ^2$ ENBW [72]
Frequency	$1 - \frac{1}{2} [1 - \cos(\theta)] \left(\frac{\Delta\omega_{\text{mw}}}{\omega_R} \right)^2$	$1 - \frac{1}{\pi} \int_{\omega_{\text{min}}}^{\infty} \frac{S_{\text{mw}}(\omega)}{\omega_R^2} H_{\text{mw}}(\omega) ^2 d\omega$	LPF	$\frac{1}{2} [1 - \cos(\theta)]$ $\omega_R \frac{\pi \theta }{2 [1 - \cos(\theta)]}$
Phase	$1 - \frac{1}{2} [1 - \cos(\theta)] \Delta\phi^2$	$1 - \frac{1}{\pi} \int_{\omega_{\text{min}}}^{\infty} \frac{S_{\text{add}}(\omega - \omega_0)}{\omega_R^2} H_{\text{add}}(\omega) ^2 d\omega$	LPF	$\frac{1}{4} \theta^2 + \frac{1}{2} [1 - \cos(\theta)]$ $\omega_R \frac{2\pi\theta}{\theta^2 + 2[1 - \cos(\theta)]}$
Additive noise				
Envelope				
Amplitude	$1 - \frac{1}{4} \theta^2 \left(\frac{\Delta\omega_R}{\omega_R} \right)^2$	$1 - \frac{1}{\pi} \int_{\omega_{\text{min}}}^{\infty} \frac{S_R(\omega)}{\omega_R^2} H_R(\omega) ^2 d\omega$	LPF	$\frac{1}{4} \theta^2$ $\frac{\pi}{\omega_R \theta }$
Duration	$1 - \frac{1}{4} \theta^2 \left(\frac{\Delta T}{T} \right)^2$	$1 - \frac{1}{\pi} \int_{\omega_{\text{min}}}^{\infty} S_\phi(\omega) H_T(\omega) ^2 d\omega$	HPF	$ H_T(\omega) ^2 = \frac{1}{8} \frac{T_{\text{clk}}^2}{\pi^2} \sin^2 \left(\frac{\omega T}{2} \right)$

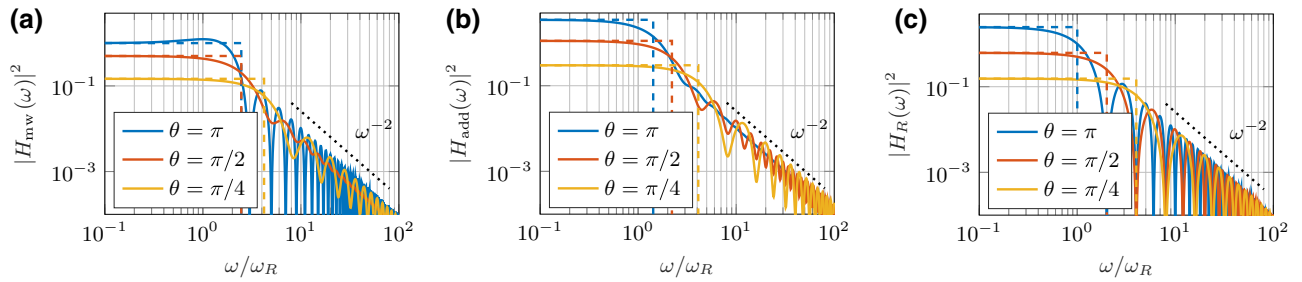


FIG. 2. The amplitude response of the intrinsic qubit filter for (a) frequency noise, (b) wide-band additive noise, and (c) amplitude noise, for various rotation angles θ . The brick-wall approximations are shown with dashed lines.

case of white noise, a good approximation is obtained with $\omega_{\min} = 0$. Due to the factor $1/\omega_R^2$, it is advantageous to use the highest possible Rabi frequency. In the presence of flicker noise, the same conclusion holds, as then a higher ω_{\min} is desirable. Finally, the same discussion holds in the case of amplitude noise. However, a higher tolerance for amplitude noise at larger Rabi frequencies, i.e., larger amplitudes, simply means that the required signal-to-noise ratio ($\omega_R^2/\sigma_{\omega_R}^2$) in the qubit’s band of sensitivity is fixed.

The microwave-frequency noise [$S_{\omega}(\Delta\omega)$], as discussed before, is set by the phase noise [$S_{\phi}(\Delta\omega)$] of the LO generator and they can be related by $S_{\omega}(\Delta\omega) = \Delta\omega^2 S_{\phi}(\Delta\omega)$ at a frequency $\Delta\omega$ from the carrier ω_{mw} [26]. While the effect of phase noise has already been extensively studied in Ref. [26], a more realistic oscillator noise model, including both phase noise and wide-band additive noise, is adopted in this work. Derivation of the intrinsic qubit-filtering function for each noise contribution leads to an improved estimation of the fidelity that deviates from Ref. [26], as elaborated in the following.

Consider, as an example, the typically reported plot for the phase noise of a phase-locked-loop- (PLL) based frequency generator, as shown in Fig. 3 (cf. Ref. [75]). Appended to this figure is a plot of the resulting frequency-noise PSD. At low frequencies, the phase noise is typically limited by the flicker noise of the reference clock (which is proportional to the f^{-3} part). In the plot of the frequency-noise PSD, this has a f^{-1} roll-off, making it important to maximize ω_{\min} , which could be resolved by using dynamical decoupling schemes, as they introduce an additional high-pass filtering [28,29,76,77]. The part of the phase-noise plot highlighted in red may be a source of concern [26], as it results in a frequency noise increasing as f^2 that exactly cancels the roll-off of the intrinsic qubit filter [Fig. 2(a)], thus resulting in a diverging integral for the fidelity (Table I) in the case in which no additional band-pass filtering is applied. However, the noise highlighted in red, visible in the phase-noise plot, originates from thermal noise added to the microwave signal by, e.g., the output driver of the microwave signal generator [75,78]. The additive noise, with generally a wide bandwidth, is more accurately modeled in the applied microwave magnetic

field as follows:

$$B(t) = \frac{2\omega_R}{\gamma_e} \cos[\omega_{\text{mw}}t + \phi + \phi_n(t)] + B_{\text{add}}(t), \quad (5)$$

where $B_{\text{add}}(t)$ represents the additive noise with PSD $S_{\text{add}}(\omega)$. The actual phase noise $\phi_n(t)$, indicated by the blue line in Fig. 3, is clearly band limited by the qubit-filter function due to the absence of the f^2 factor. The PSD of this additive noise has the same frequency dependence as the PSD of the phase noise [79]. The fidelity of the qubit

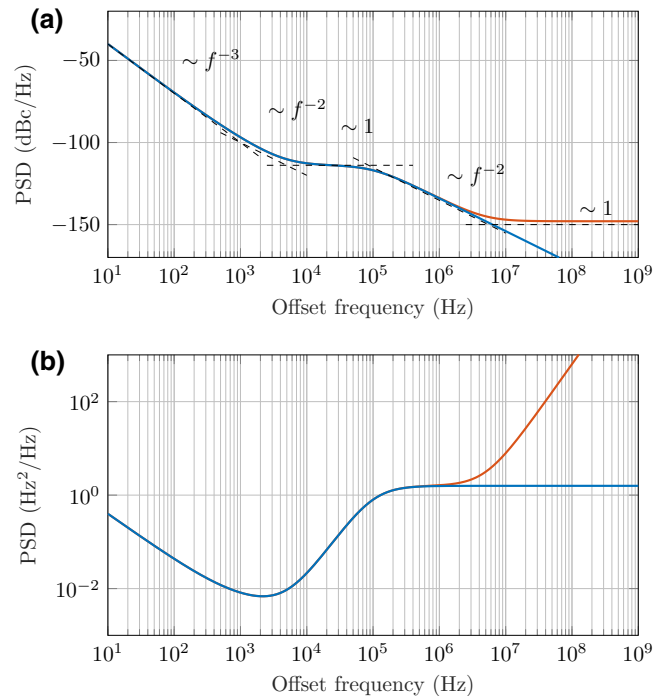


FIG. 3. A typical plot of (a) the phase noise and (b) the resulting frequency-noise PSD of a PLL-based frequency generator. The red line indicates the noise as measured by a phase-noise analyzer, whereas the blue line indicates the part of the noise that is actually phase noise. At high offset frequencies, where the lines diverge, wide-band additive noise shows up in the phase-noise plot, giving rise to a noise floor of around -150 dBc/Hz in this example.

operation in the presence of this type of noise is given in Table I.

Finally, the signal duration T is also subject to random variations, i.e., jitter. However, since the period cannot vary during the operation, the noise in the timing can be simply treated considering the period jitter as a quasistatic error. This period jitter is determined by the single-sideband phase noise $S_\phi(\omega)$ of the reference clock (period T_{clk}) used to set the duration [75,80] (Table I), following a high-pass filter (HPF) characteristic with the corner frequency set by the duration T .

B. Specifications for the idle operation and qubit-frequency multiplexing

In a typical quantum algorithm, a qubit can be idle for a while, waiting for the operations on other qubits to finish, before being operated on, e.g., due to limitations in the hardware or data dependencies. This section discusses processes that cause the state of the qubit to degrade during an idle period lasting T_{nop} . The loss of the quantum state due to interactions with other qubits will be discussed in Sec. VB.

A qubit will perform an undesired Z rotation (related to T_2^*) in the rotating frame if the microwave frequency is not matched to the qubit's Larmor frequency, even when the driving tone is not applied to the qubit. Evaluating the fidelity of an identity operation in the case of a frequency inaccuracy $\Delta\omega_{\text{mw},0}$ leads to $F_{T_{\text{nop,mw}}} = 1 - \frac{1}{4}\Delta\omega_{\text{mw},0}^2 T_{\text{nop}}^2$, which can easily be more stringent than the requirement due to a rotation (Table I).

Besides Z rotations, unintended X and Y rotations of the qubit (related to T_1) are possible in the case in which power is present at the qubit's Larmor frequency. In general, a tone could be present at the qubit frequency, e.g., due to signal leakage from the microwave source or nonlinearities in the system leading to harmonic or intermodulation tones. The presence of a spurious tone that would give a Rabi frequency of ω_{spur} will reduce the fidelity as follows: $F_{T_{\text{nop,spur}}} = 1 - \frac{1}{4}\omega_{\text{spur}}^2 T_{\text{nop}}^2$.

Besides a tone, residual thermal noise could be present on the drive line. Considering a noise signal with spectral density $S_{R_n}(\omega)$, the fidelity is $F_{T_{\text{nop,noise}}} = 1 - (1/\pi) \int_0^\infty S_{R_n}(\omega) |H_n(\omega)|^2 d\omega$, where

$$|H_n(\omega)|^2 = 2 \frac{\sin^2 \left[\frac{T_{\text{nop}}}{2} (\omega - \omega_0) \right]}{(\omega - \omega_0)^2}, \quad (6)$$

which indicates that the noise spectrum is filtered by a sinc-shaped band-pass filter centered around ω_0 , with the following brick-wall approximation:

$$|H_n(\omega)|^2 \approx \begin{cases} T_{\text{nop}}^2/2, & |\omega - \omega_0| \leq \pi/T_{\text{nop}}, \\ 0, & \text{elsewhere.} \end{cases} \quad (7)$$

Lastly, multiple qubits sharing the same control line, i.e., a single ESR line or control gates shorted together, can be controlled independently in the case in which each qubit has a unique Larmor frequency, as mentioned in Sec. II. This technique constitutes FDMA. However, when rotating a qubit with Larmor frequency ω_0 by applying a microwave signal at frequency $\omega_{\text{mw}} = \omega_0$, any unaddressed qubit on the same line with Larmor frequency $\omega_{0,\text{other}} = \omega_0 + \omega_{0,\text{space}}$ will be affected. Similarly, even if not on the same drive line, another qubit could be unintentionally driven due to parasitic coupling such as capacitive or magnetic cross talk.

An expression for the fidelity of the unaddressed qubit with respect to the ideal identity operation is reported in the Supplemental Material [69] for a microwave pulse with a rectangular envelope [Fig. 4(a)] and it is plotted in Fig. 4(c), where we assume the same Rabi frequency ω_R for both qubits. As expected, driving the qubit with a larger amplitude (i.e., larger ω_R) results in a shorter pulse for a given rotation angle, thus leading to a wider pulse bandwidth and, consequently, to a cross talk extending to qubits that are further away in frequency.

Although the expectation may arise that reducing the pulse bandwidth by proper engineering of the pulse envelope can lead to lower cross talk, Fig. 4(d) shows that also a Gaussian envelope [Fig. 4(b)] does not result in a much faster roll-off. As the figure shows, the fidelity can be limited by unintended Z rotations of the unaddressed qubit. However, by applying a simple correction for the Z rotation, the fidelity of the identity operation on the unaddressed qubit improves to the following:

$$F_{T_{\text{FDMA}}} \approx 1 - \frac{\beta^2}{\alpha^2} \sin^2 \left(\frac{\theta}{2} \alpha \right) \geq 1 - \frac{\beta^2}{\alpha^2}, \quad (8)$$

where $\alpha = \omega_{0,\text{space}}/\omega_R$ and $\beta = \omega_{R,\text{unaddressed}}/\omega_R$ and where, in general, the unaddressed qubit can have a different Rabi frequency ($\omega_{R,\text{unaddressed}}$) at the same microwave amplitude, e.g., due to a lower coupling to the drive signal. As expected, the fidelity given by Eq. (8) is approximately proportional to the spectrum of the envelope of the applied pulse [Fig. 4(c)]. Consequently, reduction of the pulse bandwidth by proper engineering of the pulse envelope is an effective solution if the unintended Z rotations are corrected, as shown in, e.g., Figs. 4(b) and 4(d), where a Gaussian envelope is employed.

A certain minimum frequency separation is necessary to achieve a target fidelity, as shown in Fig. 4(e) for the rectangular envelope. The lower bound on the fidelity as given in Eq. (8) is plotted as well, as the notches in the graph move depending on θ . Similarly, if the coupling of the microwave drive is due to parasitic effects and is unwanted, a target fidelity for unaddressed qubits translates into a

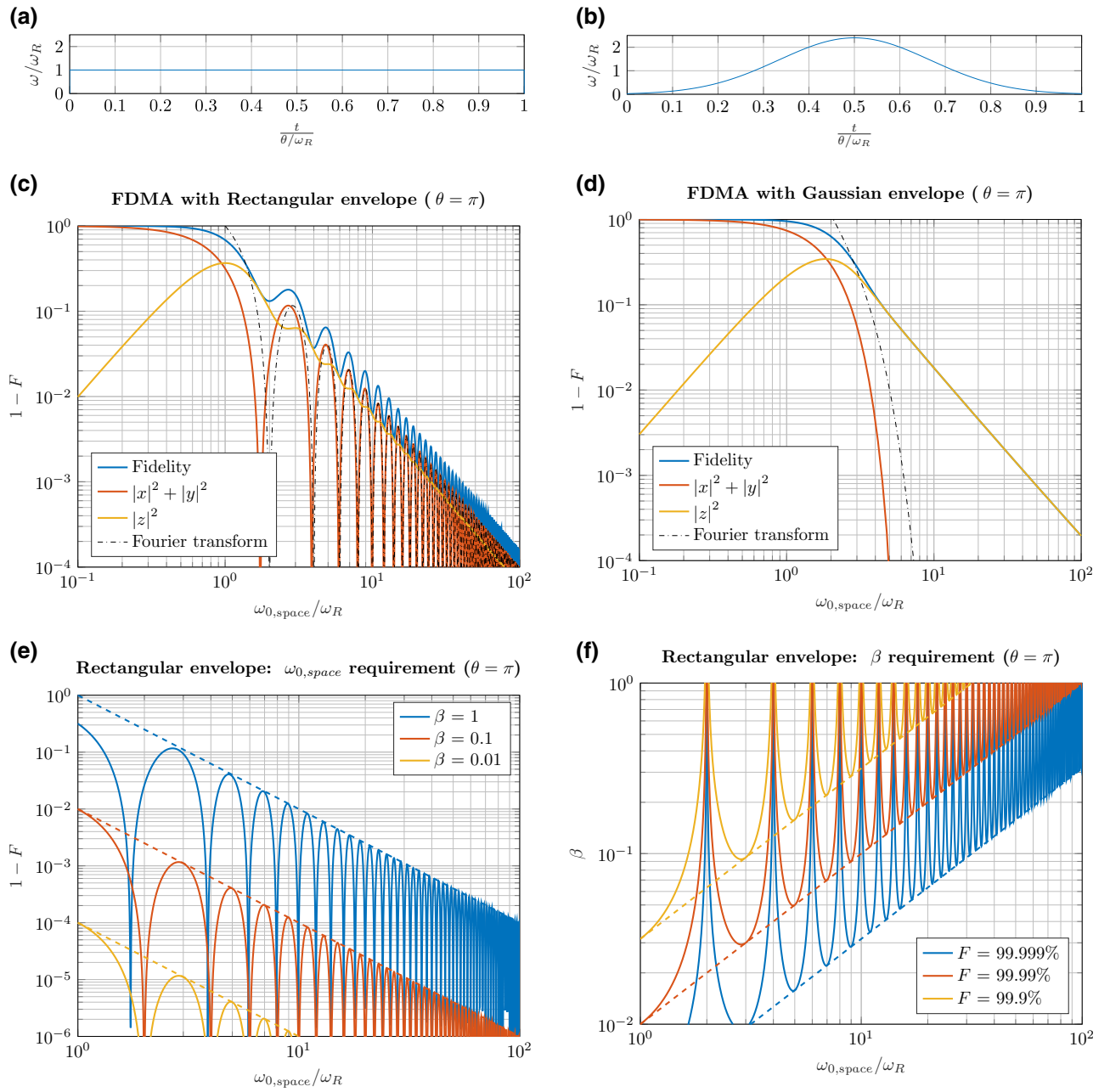


FIG. 4. Qubit-frequency multiplexing: the envelopes, achievable fidelity, and requirements in the case of a rectangular envelope. (a) The rectangular envelope under consideration. (b) The Gaussian envelope under consideration. (c) The infidelity of an identity operation (and the amount of X or Y rotation and Z rotation) on a qubit spaced at $\omega_{0,\text{space}}$ from the carrier for a rectangular envelope, along with the Fourier transform of the rectangular envelope. (d) The infidelity of an identity operation (and the amount of X or Y rotation and Z rotation) on a qubit spaced at $\omega_{0,\text{space}}$ from the carrier for a Gaussian envelope, obtained by numerical simulation, along with the Fourier transform of the Gaussian envelope. (e) The frequency spacing required to achieve a certain fidelity at given relative signal strength β , for a rectangular envelope. The upper bound (dashed lines) is given in Eq. (8). (f) The driving tone attenuation β required at a certain frequency spacing to achieve a given fidelity, for a rectangular envelope. The lower bound (dashed lines) is given in Eq. (9).

requirement in the driving tone attenuation [Fig. 4(f)]:

$$\beta = \sqrt{1 - F_{\text{corr}}} \frac{\alpha}{\left| \sin\left(\frac{\theta}{2}\alpha\right) \right|} \geq \sqrt{1 - F_{\text{corr}}} \frac{\omega_{0,\text{space}}}{\omega_R}. \quad (9)$$

Finally, FDMA has the potential to perform single-qubit gates on several qubits at the same time, using a single drive line. In that case, it is not sufficient to apply a compensating Z rotation afterward, on another qubit, if that qubit is also performing an operation. As the Z

rotation is obtained gradually when an off-resonance tone is applied, the driving tone applied to perform the operation should be altered to compensate for this Z rotation during the operation. This requires proper engineering of all the microwave pulses that are applied simultaneously [37,81–84].

C. Case study of the specifications for a single-qubit operation

With the information provided in Sec. IV, clear specifications for the control electronics can be derived. Table II shows as an example of how the total error budget can be allocated over the electronics specification to achieve a 99.9% fidelity for a π rotation at a Rabi frequency of 1 MHz. The same fidelity is targeted for preserving the state of the qubit when not operating on it for a time equal to the operation time ($T_{\text{nop}} = T$). The example considers the use of simple rectangular pulses, without any echo technique.

A Larmor frequency larger than 80 MHz would be sufficient not to get impaired by fast-oscillating terms neglected by the rotating-wave approximation (RWA, see the Supplemental Material [69]). However, choosing $\omega_0 = 10$ GHz is more in line with values used in practice and allows for a large qubit-frequency spacing. A frequency spacing of 1 GHz is selected, the same as considered in the case study of two-qubit operations (Sec. VC). Such spacing is, however, approximately 10 times larger than required for minimizing the cross talk due to FMDA

(Sec. IVB). The example also shows the effect of the frequency noise as expected from isotopically purified Si-28 (800 ppm ^{29}Si), highlighting that its contribution to the infidelity is negligible in this example.

The values provided for the microwave amplitude assume a qubit plane based on EDSR, where an amplitude of 2 mV at the gate is required for a Rabi frequency of 1 MHz (close to the value reported in Ref. [44]). All specifications are valid at the gate, so that wiring attenuation and filtering might need to be factored in to refer the specifications back to the electronics.

Following these specifications, the microwave envelope (amplitude and duration) can be generated by, e.g., an AWG with a sample rate of at least 150 megasamples/s (MS/s), such that the sample time is less than 6.7 ns, resulting in a maximum inaccuracy of 3.3 ns. Furthermore, the AWG should have a resolution of 8 bits, such that at a full-scale swing of 4 mV, the quantization step is sufficiently low. To meet the noise requirement and the specifications on the residual driving when not operating the qubit (“Off spur” in Table II), an effective number of bits (ENOB) of only 6.5 bits is required.

The LO used for the up-conversion requires a frequency resolution of approximately 20 kHz (for the inaccuracy). Assuming a -20 dB/dec slope of the phase noise, the single-side-band phase noise at 1 MHz from the carrier, $\mathcal{L}(1 \text{ MHz})$, needs to be below -106 dBc/Hz. Furthermore, the LO’s phase inaccuracy needs to be below 0.64° .

TABLE II. Example specifications for the control electronics for single-qubit operations. The PSD values provided as a comment assume a white spectrum for the amplitude and frequency noise (i.e., -20 dB/dec for the phase noise).

	Value	Infidelity contribution		Comment
		To an operation	To idling	
<i>Frequency</i>				
Nominal	10 GHz	0.64×10^{-9}		RWA when driving a qubit
Spacing	1 GHz		1×10^{-6}	FDMA leakage with rectangular envelopes
Inaccuracy	11 kHz	125×10^{-6}	308×10^{-6}	
Oscillator noise	11 kHz _{rms}	125×10^{-6}	308×10^{-6}	ENBW = 2.5 MHz, $\mathcal{L}(1 \text{ MHz}) = -106$ dBc/Hz
Nuclear spin noise	1.9 kHz _{rms}	3.6×10^{-6}	8.9×10^{-6}	From Ref. [45], $T_2^* = 120 \mu\text{s}$
Wide-band noise	12 μV_{rms}	125×10^{-6}		ENBW = 2.9 MHz, $S_{\text{add}} = 7.1 \text{ nV}/\sqrt{\text{Hz}}$
<i>Phase</i>				
Inaccuracy	0.64°	125×10^{-6}	31×10^{-6}	FDMA Z corrections limit the no operation
<i>Amplitude</i>				
Nominal	2 mV			Full scale: 4 mV, rms: 1.4 mV _{rms}
Inaccuracy	14 μV	125×10^{-6}		
Noise	14 μV_{rms}	125×10^{-6}		ENBW = 1.0 MHz, PSD = 14 nV/ $\sqrt{\text{Hz}}$, SNR = -40 dB
Off spur	19 μV		217×10^{-6}	-41 dBc
Off noise	10 μV_{rms}		125×10^{-6}	ENBW = 2.0 MHz, PSD = 7.1 nV/ $\sqrt{\text{Hz}}$
<i>Duration</i>				
Nominal	500 ns			
Inaccuracy	3.6 ns	125×10^{-6}		
Noise	3.6 ns _{rms}	125×10^{-6}		
		$F_{X,Y} = 99.9\%$	$F_I = 99.9\%$	

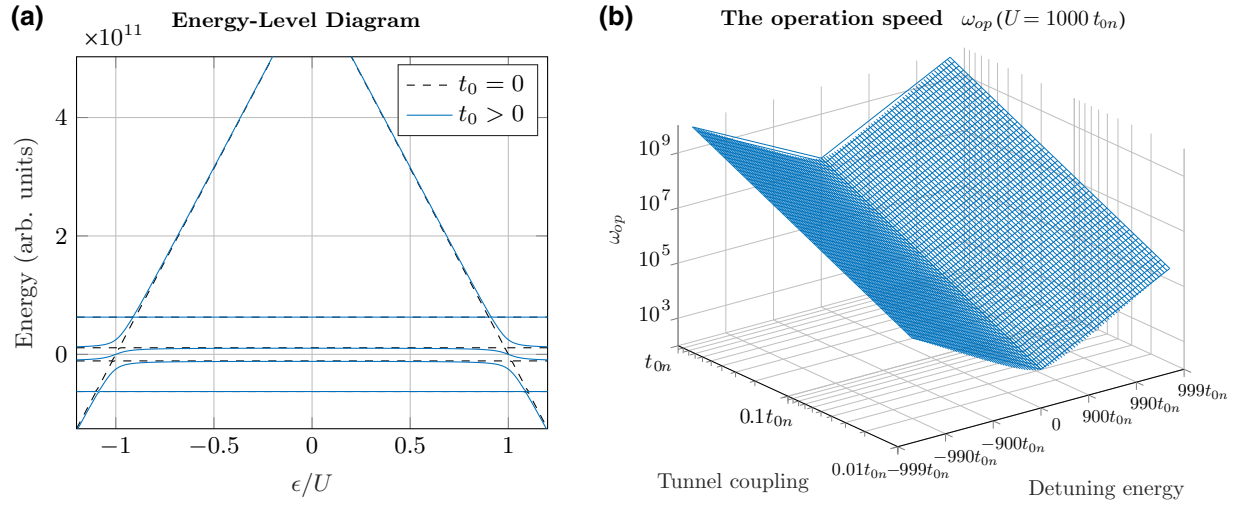


FIG. 5. (a) The energy-level diagram of the two-qubit system. An avoided crossing is visible for $|\epsilon| = U$ when there is a finite tunnel coupling between the dots. (b) The two-qubit operation speed ω_{op} [Eq. (11)] versus the interdot tunnel coupling and detuning. A nominal tunnel coupling t_{0n} of 1 GHz is used.

V. SIGNAL SPECIFICATIONS FOR TWO-QUBIT OPERATIONS

A. Fidelity of a two-qubit operation

As stated in Sec. II, by default the tunnel coupling between the qubits is negligible and the qubits have the same potential, i.e., they are not detuned. By increasing the tunnel coupling and/or by detuning the qubits, the qubit interaction increases and a two-qubit gate can be obtained. In this system, by leveraging this exchange interaction, a two-qubit exchange gate and a C phase gate can be implemented. With either of these gates and single-qubit operations, a universal set is obtained.

To describe the physical interactions required for the two-qubit gate, higher energy levels need to be modeled in the Hamiltonian. The analysis presented here is limited to the interaction between two neighboring qubits, A and B , and to the single-dot singlet states ($|0, 2\rangle$ represents the singlet state in the right dot and $|2, 0\rangle$ the singlet state in the left). In the basis $\Psi = [|\uparrow, \uparrow\rangle, |\uparrow, \downarrow\rangle, |\downarrow, \uparrow\rangle, |\downarrow, \downarrow\rangle, |0, 2\rangle, |2, 0\rangle]$, the Hamiltonian of a double quantum dot is given by the following ($\hbar = 1$) [55,85,86]:

$$H = \begin{bmatrix} -\omega_0 & 0 & 0 & 0 & 0 & 0 \\ 0 & \frac{\delta\omega_0}{2} & 0 & 0 & t_0 & t_0 \\ 0 & 0 & -\frac{\delta\omega_0}{2} & 0 & -t_0 & -t_0 \\ 0 & 0 & 0 & \omega_0 & 0 & 0 \\ 0 & t_0 & -t_0 & 0 & U - \epsilon & 0 \\ 0 & t_0 & -t_0 & 0 & 0 & U + \epsilon \end{bmatrix}, \tag{10}$$

where $\omega_0 = (\omega_{0,A} + \omega_{0,B})/2$, $\delta\omega_0 = \omega_{0,B} - \omega_{0,A}$, and $\omega_{0,A}$ and $\omega_{0,B}$ are the Larmor frequencies of the two qubits. The charging energy (U) is assumed to be the same for both dots. The tunnel coupling between the quantum dots (t_0) has an exponential relation to the voltage on the barrier gate and the detuning energy (ϵ) is controlled by the voltage difference on the plunger gates of the dots (V_d) via the lever arm $\alpha = \Delta\epsilon/\Delta V_d$.

An avoided crossing is observed in the energy-level diagram for $|\epsilon| = U$ and $t_0 > 0$ [Fig. 5(a)], which gives rise to eigenenergies that are different from the case of two isolated dots ($t_0 \sim 0$) for any detuning. This change of eigenenergy and the corresponding eigenstate form the basis of the two-qubit operations. An investigation of the eigenenergies of the Hamiltonian in Eq. (10) reveals that the total change in eigenenergy equals the following ($\hbar = 1$):

$$\omega_{op} = 4t_0^2 \frac{U}{U^2 - \epsilon^2}. \tag{11}$$

Note that the expression used in this paper for ω_{op} derives directly from the Hamiltonian of Eq. (10). However, experiments have reported ω_{op} as an exponential function of detuning [87].

As ω_{op} describes the amount of exchange interaction, it directly sets the speed of the two-qubit operation. A plot of ω_{op} versus the tunnel coupling and detuning is shown in Fig. 5(b). To perform the two-qubit operation, a control pulse must be applied, to move the system away from the default point (negligible tunnel coupling and zero detuning) to the desired operating point, where there is sufficient exchange interaction such that a two-qubit operation is performed. From Fig. 5(b), it is clear that a fast gate can be

obtained at finite detuning, becoming faster closer to the avoided crossing, controlled by the detuning and/or the tunnel coupling. Alternatively, operation at zero detuning (the charge-symmetry point [30]) is possible, controlled by the tunnel coupling alone. Depending on whether the control parameter, the detuning, and/or the tunnel coupling is changed adiabatically or diabatically, a C phase or exchange gate, or a mixture of the two, is obtained.

In the case in which the control parameter changes slowly, i.e., adiabatically, the resulting operation, in the rotating frame, can be described by the following diagonal matrix:

$$U_{CZ}(t) = \text{diag} \left(1, e^{-i\phi_{Z,A}}, e^{-i\phi_{Z,B}}, 1 \right), \quad (12)$$

where $\phi_{Z,A}$ and $\phi_{Z,B}$ are the acquired phases in the rotating frame. Two additional Z rotations with angles $\phi_{Z,A}$ and $\phi_{Z,B}$ can be applied to the right and left qubit, respectively, to obtain the C phase gate with $\theta_{CZ} = -(\phi_{Z,A} + \phi_{Z,B}) = \omega_{\text{op}}t$. These Z rotations can easily be obtained by updating the software reference frame [37,38]. In the case in which $\theta_{CZ} = \pi$, a controlled- Z operation is obtained. Interestingly, the total acquired phase ($\phi_{Z,A} + \phi_{Z,B}$) is independent of $\delta\omega_0$. However, when $\delta\omega_0 = \sqrt{2}t_0$, $\phi_{Z,A} = \phi_{Z,B}$ [55,85,86], whereas for $\delta\omega_0 = 0$, $\phi_{Z,A} = 0$.

If, instead, the control parameter is changed rapidly, i.e., diabatically, and the Larmor-frequency difference is negligible ($\delta\omega_0 \ll \omega_{\text{op}}$), the resulting operation, in the rotating frame, is as follows:

$$U_J(t) \approx \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & \frac{1+e^{i\theta_J}}{2} & \frac{1-e^{i\theta_J}}{2} & 0 \\ 0 & \frac{1-e^{i\theta_J}}{2} & \frac{1+e^{i\theta_J}}{2} & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}, \quad (13)$$

where $\theta_J = \omega_{\text{op}}t$. In the case in which $\theta_J = \pi$, a SWAP operation is obtained. Note that since for an accurate exchange operation the Larmor-frequency difference should be sufficiently small, the possibility of using FDMA for single-qubit operations (Sec. IV B) is limited.

From Eq. (10) it follows that the two-qubit operations are affected by the Larmor frequencies ($\omega_{0,A}$, $\omega_{0,B}$), the tunnel coupling (t_0), the charging energy (U), and the detuning (ϵ). Furthermore, the operation depends on the total duration (T) for which the two-qubit gate is active. The effect of errors, both static and dynamic, on the fully electrically controlled parameters (t_0 , ϵ , and T) is analyzed in the subsequent section. Detailed derivations of the formulas can be found in the Supplemental Material [69].

The resulting fidelity in the case of control signal inaccuracies is summarized in Table III for the exchange gate and the C phase gate, both at zero detuning and finite detuning. For the exchange gate, we assume that no Larmor-frequency difference between the qubits exists, since for

TABLE III. The fidelity in the case of control signal inaccuracies for various two-qubit operations.

Rotation $ \phi_{Z,B} $	Exchange:		C phase	
	$\delta\omega_0 = 0$	$\delta\omega_0 = 0$	$\delta\omega_0 = \omega_{\text{op}}$	$\delta\omega_0 = \sqrt{2}t_0$
Duration	$1 - \frac{3}{16}\theta_J^2 \left(\frac{\Delta T}{T}\right)^2$	$1 - \frac{3}{16}\theta_{CZ}^2 \left(\frac{\Delta T}{T}\right)^2$	$1 - \frac{7-4\sqrt{2}}{16}\theta_{CZ}^2 \left(\frac{\Delta T}{T}\right)^2$	$1 - \frac{1}{16}\theta_{CZ}^2 \left(\frac{\Delta T}{T}\right)^2$
Tunnel coupling	$1 - \frac{3}{4}\theta_J^2 \left(\frac{\Delta t_0}{t_0}\right)^2$	$1 - \frac{3}{4}\theta_{CZ}^2 \left(\frac{\Delta t_0}{t_0}\right)^2$	$1 - \frac{1}{2}\theta_{CZ}^2 \left(\frac{\Delta t_0}{t_0}\right)^2$	$1 - \frac{1}{4}\theta_{CZ}^2 \left(\frac{\Delta t_0}{t_0}\right)^2$
Detuning ($ \epsilon > 0$)	$1 - \frac{3}{4}\theta_J^2 \left(\frac{\epsilon}{U}\right)^2 \left(\frac{\Delta\epsilon}{U}\right)^2$	$1 - \frac{3}{4}\theta_{CZ}^2 \left(\frac{\epsilon}{U}\right)^2 \left(\frac{\Delta\epsilon}{U}\right)^2$	$1 - \frac{1}{2}\theta_{CZ}^2 \left(\frac{\epsilon}{U}\right)^2 \left(\frac{\Delta\epsilon}{U}\right)^2$	$1 - \frac{1}{4}\theta_{CZ}^2 \left(\frac{\epsilon}{U}\right)^2 \left(\frac{\Delta\epsilon}{U}\right)^2$
Detuning ($\epsilon = 0$)	$1 - \frac{3}{16}\theta_J^2 \left(\frac{\Delta\epsilon}{U}\right)^4$	$1 - \frac{3}{16}\theta_{CZ}^2 \left(\frac{\Delta\epsilon}{U}\right)^4$	$1 - \frac{1}{8}\theta_{CZ}^2 \left(\frac{\Delta\epsilon}{U}\right)^4$	$1 - \frac{1}{16}\theta_{CZ}^2 \left(\frac{\Delta\epsilon}{U}\right)^4$

such a gate $\delta\omega_0 \ll \omega_{\text{op}}$ is required, while for the C phase gate, various scenarios are analyzed ($\delta\omega_0 = 0$, $\delta\omega_0 = \omega_{\text{op}}$, and $\delta\omega_0 = \sqrt{2}t_0$). In the table, T denotes the qubit gate operation time and inaccuracies are shown with the prefix Δ . The table provides values for different rotation angles (θ_{CZ} , θ_J). In the case of the C phase gate, additional Z rotations might be required, which only have a very small effect on the fidelity [Eq. (3)].

The error contributions have a quadratic relation with the infidelity except for detuning errors for $\epsilon = 0$, where a fourth-order dependence is found. This implies an improved robustness to detuning errors when operating at the charge-symmetry point ($\epsilon = 0$) [30,31].

For low-frequency variations, i.e., those changing over a time scale longer than the operation time, the same approach as for single-qubit operations holds and the expected fidelity follows the same equations as given in Table III when replacing the inaccuracy, such as $\Delta\epsilon$, with the standard deviation of the variation (assuming a Gaussian distribution). An exception is for detuning errors when operating at the charge-symmetry point [30,31], because of the fourth-order dependence. For a static but random error Δ for which $F = 1 - c\Delta^4$, the expected fidelity is $F = 1 - 3c\sigma^4$, if Δ follows a Gaussian distribution with standard deviation σ and zero mean. Consequently, there is a slightly higher sensitivity to noise than to static errors for the detuning.

For timing variations, only the total duration matters and high-frequency noise is filtered as described in Table I. Moreover, similar to the single-qubit gate, numerical simulations of the Hamiltonian have shown sensitivity to high-frequency noise ($> \omega_{\text{op}}$) only in a pass band with a bandwidth that is inversely proportional to the operation duration, for both the electrically controlled detuning energy and tunnel coupling in the case of the two-qubit gate (see the Supplemental Material [69]). The quantum state is, however, also affected by noise around the frequencies corresponding to the allowed energy transitions, in a pass band with a similar bandwidth. Consequently, it is important that the high-frequency noise components in the signals applied to the barrier gates and plunger gates are properly filtered. However, since the exchange gate requires a diabatic change in the control parameter, only limited filtering can be applied. Closed-form analytical expressions for these effects have not been studied.

B. Specifications for the idle operation

Since, in practice, the tunnel coupling cannot be fully removed, the two-qubit operation is never completely turned off. The interaction strength can, however, be slowed down significantly, thus leading to a fidelity with respect to the ideal identity operation for the exchange and

C phase gates as follows:

$$F_{I_{\text{nop},J}} = \begin{cases} 1 - \frac{3}{16}\omega_{\text{op,off}}^2 T_{\text{nop}}^2, & \delta\omega_0 = 0, \\ 1 - \frac{7 - 4\sqrt{2}}{16}\omega_{\text{op,off}}^2 T_{\text{nop}}^2, & \delta\omega_0 = \omega_{\text{op}}, \\ 1 - \frac{1}{16}\omega_{\text{op,off}}^2 T_{\text{nop}}^2, & \delta\omega_0 = \sqrt{2}t_0, \end{cases} \quad (14)$$

where $\omega_{\text{op,off}}$ is the reduced interaction strength during the time T_{nop} when no operation is applied.

Following Eq. (11), the interaction strength can be reduced by lowering the tunnel coupling while not changing the detuning. A two-qubit operation performed at finite detuning could also be controlled using only the detuning. However, assuming that the interaction is considered to be off at zero detuning, the operation might need to be performed at far detuning. As mentioned before, operating closer to the avoided crossing reduces the tolerance to inaccuracies and noise in the detuning (Table III).

C. Case study of the specifications for a two-qubit operation

Specifications for the control electronics responsible for the two-qubit operation can be derived using the results presented in Sec. V. This example develops on the example given in Sec. IV C and, for instance, assumes that the same oscillator is used to keep the coherence with the qubits. Two examples will be given here, one at zero detuning and one at finite detuning. Both focus on the C phase gate, operating at $\delta\omega_0 = \sqrt{2}t_0$. This choice for the Larmor-frequency difference gives the most relaxed specifications for the control electronics, while at smaller $\delta\omega_0$, the specifications can be up to $\sqrt{3}$ times more demanding (see Table III).

The Larmor-frequency difference is chosen as 1 GHz, to achieve a two-qubit operation speed of $\omega_{\text{op}} = 2$ MHz at zero detuning, while maintaining $\delta\omega_0 = \sqrt{2}t_0$ [Eq. (11)]. Example specifications for this operation are given in Table IV. To further increase the operating speed, an even higher qubit-frequency spacing would be required or $\delta\omega_0 < \sqrt{2}t_0$. Alternatively, the operating speed can be enhanced to, e.g., 20 MHz, by operating the C phase gate at finite detuning [Eq. (11)], as shown in another example (Table IV).

Both examples target a fidelity of 99.9% for a C phase gate with $\theta_{CZ} = \pi$. The examples also indicate the specifications required for idling two qubits at 99.9% fidelity for a duration of 500 ns, the same as for the example in Sec. IV C.

For the charging energy and tunnel coupling, typical values are chosen. As the relation to the gate voltage is device dependent, no values for the required electrical specifications are given. Note that in either example, the tunnel coupling only has to change by a factor of approximately

TABLE IV. Example specifications for the control electronics when operating a C phase gate at zero detuning and at finite detuning. The PSD values provided assume a white spectrum with an ENBW of approximately 10 MHz ($\omega_{\text{op}} = 2$ MHz) when operating at zero detuning and an ENBW of approximately 100 MHz ($\omega_{\text{op}} = 20$ MHz) at finite detuning. A nominal charging energy of 83 mV (4.1 meV, 1.0 THz) is assumed.

	Value		Infidelity contribution to		
	$\epsilon = 0$	$\epsilon > 0$	An operation $\epsilon = 0$	An operation $\epsilon > 0$	Idling
<i>Frequency</i>					
Spacing	1 GHz	1 GHz			
Inaccuracy	11 kHz	11 kHz	77×10^{-6}	0.8×10^{-6}	308×10^{-6}
Oscillator noise	11 kHz _{rms}	11 kHz _{rms}	77×10^{-6}	0.8×10^{-6}	308×10^{-6}
Nuclear-spin noise	1.9 kHz _{rms}	1.9 kHz _{rms}	2.2×10^{-6}	0.02×10^{-6}	8.9×10^{-6}
<i>Duration</i>					
Nominal	250 ns	25 ns			
Error	5.3 ns	0.58 ns	281×10^{-6}	333×10^{-6}	
<i>Detuning energy</i>					
Nominal	0 mV (0 μeV , 0 GHz)	78 mV (3.9 meV, 0.95 THz)			
Error	12 mV (0.60 meV, 0.15 THz)	0.10 mV (5.1 μeV , 1.2 GHz)	281×10^{-6}	333×10^{-6}	
	$\sigma = 9.2 \text{ mV}_{\text{rms}}$	$\sigma = 0.10 \text{ mV}_{\text{rms}}$			
	PSD = $2.9 \mu\text{V}/\sqrt{\text{Hz}}$	PSD = $10 \text{ nV}/\sqrt{\text{Hz}}$			
<i>Tunnel coupling</i>					
Nominal	0.71 GHz (2.9 μeV)	0.71 GHz (2.9 μeV)			
Error	7.5 MHz (31 neV)	8.2 MHz (34 neV)	281×10^{-6}	333×10^{-6}	
Off value	78 MHz (0.32 μeV)	78 MHz (0.32 μeV)			374×10^{-6}
			$F_{CZ} = 99.9\%$	$F_{CZ} = 99.9\%$	$F_I = 99.9\%$

9 to turn the operation on or off. In the case of operation at finite detuning, this assumes that zero detuning is applied when the operation is turned off.

The detuning energy is directly related to the voltage on the plunger gate via the lever arm, for which a typical value of $\alpha = 0.05$ eV/V is assumed [44]. When operating at finite detuning, the detuning energy is chosen at 95% of the charging energy. Even though higher operating speeds can be obtained by moving even closer to the avoided crossing, the electrical specifications become increasingly challenging. When operating at the charge-symmetry point, very large detuning errors can be tolerated (at which point the approximations used to derive the expressions in Table III do not hold any more). When operating at moderate detuning, the error specification for the detuning is more than 100 times stricter. Moreover, as the operation at finite detuning is faster with the same tunnel coupling, the signal bandwidth must be larger, with a larger noise bandwidth. As a rough estimate, the ENBW has been chosen as 5 times the operating speed in both examples, which seems plausible as an adiabatic change is required (for the exchange gate, the situation might be worse). As a result, the maximum allowed noise spectral density, assuming white noise, is much lower. For the given example, this results in a difference of almost 5 orders of magnitude in the noise PSD.

In the example operation at finite detuning, the detuning control can be achieved by an AWG running at a sample rate of 1 gigasamples/s (GS/s) for a maximum

timing inaccuracy of 0.5 ns. Assuming that the AWG has to cover a voltage range of $-U \dots U$ (where U is the charging energy), it must have a 10-bit resolution to meet the accuracy specification of the detuning energy.

VI. SIGNAL SPECIFICATIONS FOR QUBIT READOUT

A. Fidelity for qubit readout

For the readout of the quantum state, the Pauli spin-blockade readout [88] is analyzed, since it offers several advantages with respect to the other possible alternative, i.e., the Elzerman readout [65]: no electron reservoir is required next to the quantum dot; and the Zeeman energy splitting does not have to be much higher than the thermal energy, thus enabling operation at higher temperatures and/or lower Larmor frequencies. As a drawback, the Pauli spin-blockade readout involves two quantum dots, where the measurement involves discrimination between the singlet and triplet states.

Even though relaxation, which is quantified by the relaxation time T_1 , is an important limiting factor in qubit readout, its effect is not considered in the following analysis, as all gates are assumed to be performed in a time significantly smaller than T_1 . Furthermore, in our analysis, we assume that the spin-dependent charge state resulting from a Pauli spin-blockade readout is measured using a charge sensor. As a result, the readout fidelity is determined by various factors:

- (a) P_{charge} : the probability that the spin state is correctly projected to the charge state.
- (b) P_{sense} : the probability that the charge sensor correctly detects the charge state.
- (c) P_{detect} : the probability that the readout circuit correctly discriminates the signal of the charge sensor.

The overall readout fidelity is then as follows:

$$F_{\text{read}} \approx P_{\text{charge}} \times P_{\text{sense}} \times P_{\text{detect}}. \quad (15)$$

The probability P_{sense} is limited by, e.g., interference on one of the charge-sensor bias gates and charge noise in the substrate. As this depends highly on the type of sensor employed and the sensor integration, this error contribution will not be discussed further.

The quantum-dot control electronics limit P_{charge} , as discussed in Sec. VIB, while the readout electronics limit P_{detect} , as discussed in Sec. VIC.

B. Specifications for the electronics controlling the spin to charge conversion

For the analysis of the charge transfer in the Pauli spin-blockade readout, the Hamiltonian of Eq. (10) is extended with the lowest-energy triplet states (either due to the valley splitting or the orbital energy splitting). Those states are spaced by a singlet-triplet energy splitting E_{ST} from the singlet energy level (for the Hamiltonian, see the Supplemental Material [69]). A plot of the energy of the stationary states versus the detuning near the avoided crossing is shown in Fig. 6(a). For the following discussion, only the $|\downarrow, \downarrow\rangle$ and $|\downarrow, \uparrow\rangle$ states, highlighted in Fig. 6(a), need to be considered.

The Pauli spin-blockade readout relies on E_{ST} for the discrimination of the single-dot singlet configuration from three possible single-dot triplet configurations. Considering a pair of neighboring qubits, the state of the right qubit can be measured as follows. The left qubit is initialized in the $|\downarrow\rangle$ state. By detuning adiabatically to a point between the singlet avoided crossing and the triplet avoided crossing (with $t_0 > 0$), only the $|\downarrow, \uparrow\rangle$ state (at $\epsilon = 0$) becomes a singlet and both electrons will move into the same dot. This charge movement can be measured using a charge sensor. Based on the measurement result, it is then clear whether the qubits are in a singlet or one of the three triplet configurations. This scenario is analyzed here.

Starting from the $|\downarrow, \downarrow\rangle$ state, there is a small probability $P(\text{transfer}|\downarrow, \downarrow)$ that both electrons will end up in the same dot. Similarly, starting from the $|\downarrow, \uparrow\rangle$ state, there is a small probability $P(\text{no transfer}|\downarrow, \uparrow)$ that no charge will transfer. The probability of a correct spin to charge conversion can be defined as follows:

$$P_{\text{charge}} = 1 - P(\text{transfer}|\downarrow, \downarrow) - P(\text{no transfer}|\downarrow, \uparrow). \quad (16)$$

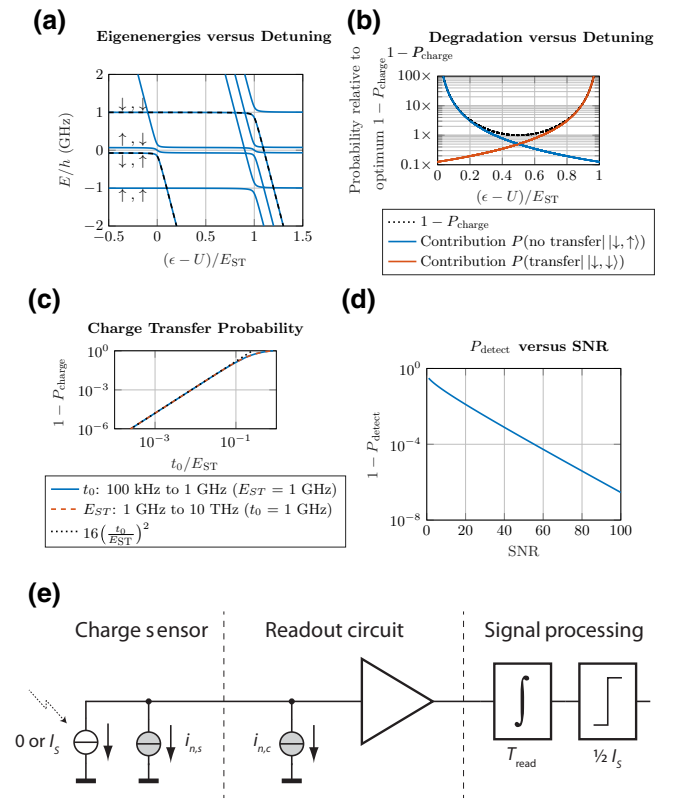


FIG. 6. (a) The energy of the stationary states versus the detuning near the avoided crossing. The black dashed lines indicate the states where the left qubit was originally in the $|\downarrow\rangle$ state. (b) The error probability $1 - P_{\text{charge}}$, along with the individual error contributors, at various points of detuning as simulated for various tunnel rates, Larmor frequencies, charging energies, and singlet-triplet energy splittings (each varied over a decade; the resulting plots are overlapping). The obtained probabilities are plotted relative to the optimum, i.e., the lowest error probability at $\epsilon = U + E_{\text{ST}}/2$, thereby showing the degradation when moving away from the optimum detuning. (c) The simulated probability $1 - P_{\text{charge}}$ versus the singlet-triplet splitting, normalized to the tunnel coupling at $\epsilon = U + E_{\text{ST}}/2$, while sweeping either the tunnel coupling or the singlet-triplet energy splitting. (d) A plot of $1 - P_{\text{detect}}$ versus SNR in the case of Gaussian-distributed noise. (e) A model of a typical readout chain, showing the sensor, the readout electronics, and the required signal processing for the measurement discrimination. Additional sources modeling the noise are shown in gray.

The analysis is again simplified by assuming an ideal adiabatic change in the detuning energy. The results presented in this section are obtained from numerical simulations of the Hamiltonian.

Simulations show that the highest P_{charge} is obtained by detuning to $\epsilon = U + E_{\text{ST}}/2$, i.e., equidistant between the singlet and triplet avoided crossings, as shown in Fig. 6(b). The shape of the probability versus $(\epsilon - U)/E_{\text{ST}}$ plot is independent of the Larmor frequency, assuming that $\omega_0 \ll E_{\text{ST}}$. Although the shape remains the same, the obtainable

TABLE V. Example specifications for the control electronics. The PSD values provided assume a white spectrum with an ENBW of approximately 1 MHz for the detuning control and a measurement time of $T_{\text{read}} = 0.6 \mu\text{s}$. A nominal charging energy of 82.7 mV (4.1 meV, 1.0 THz) is assumed and a singlet-triplet energy splitting of 1.0 mV (50 μeV , 12 GHz).

	Value	Infidelity contribution to the readout	
<i>Detuning energy</i>			
Nominal	83.2 mV (4.2 meV, 1.0 THz)		
Error	0.24 mV (12 μeV , 2.8 GHz)	167×10^{-6}	
	$\sigma = 0.24 \text{ mV}_{\text{rms}}$, PSD = $0.24 \mu\text{V}/\sqrt{\text{Hz}}$		
<i>Tunnel coupling</i>			
Nominal	39 MHz (0.16 μeV)	167×10^{-6}	$P_{\text{charge}} = 99.967\%$
<i>Charge sensor</i>			
<i>QPC</i>			
Signal	400 pA		$P_{\text{sense}} = 99.967\%$
Noise	53 pA _{rms} , PSD = 57 fA/ $\sqrt{\text{Hz}}$	222×10^{-6}	
<i>Readout circuit</i>			
Input-referred noise	26 pA _{rms} , PSD = 28 fA/ $\sqrt{\text{Hz}}$	111×10^{-6}	$P_{\text{detect}} = 99.967\%$ $F = 99.9\%$

maximum P_{charge} scales with the tunnel coupling and the singlet-triplet energy splitting, as can be seen in Fig. 6(c). From this figure, an upper bound for the tunnel coupling can be found, which must be maintained even with errors caused by limitations in the control electronics.

Even though P_{charge} is highly influenced by the achievable tunnel couplings and singlet-triplet energy splittings in the system [Fig. 6(c)], the detuning value has a minor influence (provided that there is a sufficient singlet-triplet energy splitting), since $1 - P_{\text{charge}}$ is relatively flat around its minimum, as shown in Fig. 6(b). For instance, for a twofold increase in $1 - P_{\text{charge}}$, the detuning must stay in the range $(\epsilon - U)/E_{\text{ST}} \approx 0.5 \pm 0.235$. We can then conclude that a large singlet-triplet splitting is desired to limit the influence of the control electronics on the readout.

C. Specifications for the electronics processing the readout signal

In this section, we will consider a direct readout. A model of a typical readout chain is shown in Fig. 6(e). For simplicity, the sensor is modeled as a current source with a value of either 0 or I_s , depending on the sensed charge. The readout fidelity is limited by the noise introduced by the sensor and by the readout circuit, indicated in Fig. 6(e) as $i_{n,s}$ and $i_{n,c}$, respectively. Assuming the typical matched-filter detection [89], i.e., integrating the signal current for a duration T_{read} and comparing the result to a threshold, the probability of a correct measurement under the presence of Gaussian-distributed noise is given by the following:

$$P_{\text{detect}} = \frac{1 + \text{erf}\left(\sqrt{\frac{S/N}{8}}\right)}{2}, \quad (17)$$

with

$$S/N = \frac{I_s^2}{\int_0^\infty S_i(f) \left[\frac{\sin(\pi f T_{\text{read}})}{\pi f} \right]^2 df}, \quad (18)$$

where $S_i(f)$ is the PSD of the total noise $i_n = i_{n,s} + i_{n,c}$. When the noise is white, this simplifies to the following:

$$S/N = \frac{I_s^2}{S_i \mathcal{B}_{\text{ENBW}}}, \quad (19)$$

with effective noise bandwidth $\mathcal{B}_{\text{ENBW}} = 1/(2T_{\text{read}})$.

D. Case study of the specifications for a qubit readout

The example specifications presented in this section build on those presented in Secs. IV C and V C and hence assume the same charging energy and lever arm for the detuning control. For the singlet-triplet energy splitting, a typical value of $E_{\text{ST}} = 50 \mu\text{eV}$ is used. As a result, the optimum detuning is at 83.2 mV. The resulting specifications are summarized in Table V and assume equal contributions from P_{charge} , P_{sense} , and P_{detect} [Eq. (15)].

Following Fig. 6(c), to achieve the required P_{charge} , the tunnel coupling must be even lower than is required to turn off the two-qubit operation (see Sec. V C), thereby extending the required tunnel-coupling tuning range to approximately $18\times$. In the example of Table V, the detuning control can be achieved by an AWG with low sample rate, as the detuning must change adiabatically and the readout generally takes a relatively long time. Assuming that the AWG has to cover a voltage range from 0 to $2U$, it must have a 9-bit resolution to meet the accuracy specification of the detuning energy [Fig. 6(b)]. As a result, the same circuitry as used for the two-qubit operation (Sec. V C) could potentially be used.

In this example, P_{detect} assumes a direct readout of a QPC, following the numbers provided in Ref. [64] ($I_s = 400$ pA and $i_{n,s} = 57$ fA/ $\sqrt{\text{Hz}}$). Assuming that the readout circuit is designed to contribute about half the noise compared to the shot-noise limit of the QPC ($i_{n,c} \approx i_{n,s}/2$), an integration time of at least $T_{\text{read}} = 0.6$ μs is required to achieve an signal-to-noise ratio (SNR) of 46 for a P_{detect} of 99.967% [for $F = 99.9\%$, Eq. (15)]. For such short readout times, the assumption of white noise is valid and the effects of qubit relaxation (T_1) could be negligible.

VII. DISCUSSION

Case studies targeting a 99.9% average gate fidelity have been presented in Secs. IV C, V C, and VI D. This target fidelity is particularly relevant since the minimum error rate to reach fault-tolerant quantum computing is around 99% for a complete error correction cycle, thus requiring a single-operation fidelity above 99.9% for a typical cycle of ten operations [14]. Reaching a 99.9% fidelity for all operations is currently an ambitious goal for all qubit platforms. However, our model can be directly applied to analyze specifications for any given fidelity.

The derived electronic specifications can now be compared to the performance achieved by state-of-the-art electronics. To this end, Table VI summarizes the performance of commonly used AWGs and microwave vector sources [90–94].

For the generation of the detuning control and the microwave envelope, an AWG is required. We compare the specifications of the Tektronix 5014C, as used in, e.g., Refs. [44,52,56], with the specifications derived in the case study. This AWG achieves a sample rate of 1.2 GS/s with 14-bit resolution, thereby providing enough resolution for the amplitude and duration of the microwave envelope (150 MS/s and 8 bits). The worst-case spurious-free dynamic range of -56 dBc is well below the required -41 dBc. The specified random jitter of 5.0 ps_{rms} is well below the required value of 3.6 ns_{rms}. Finally, the output noise level is not clearly specified but can be assumed to be not much larger than the amplitude resolution of 1 mV.

In the case in which the AWG’s output is attenuated by approximately 40 dB, this also meets the specifications. This AWG can also be used for detuning control in two-qubit gates. The sample rate is high enough to meet the required timing resolution (> 1 GS/s) and the resolution is sufficiently high to reach the detuning requirements (a worst case 0.10 mV for a < 100 mV pulse) with a 20 dB attenuator. As the specifications for Pauli-spin blockade readout are found to be more relaxed, the same AWG again suffices.

Finally, for the generation of the microwave carrier, some setups use the Agilent MW vector source E8267D [12,44,45,52,55,56], which has a frequency resolution well below the requirements (1 MHz versus approximately 20 kHz). The single-sided phase noise is also well below the required -106 dBc/Hz at a 1 MHz offset from the carrier (at the worst point, the E8267D achieves better than -100 dBc/Hz at a 100 kHz offset). The broadband noise is specified as 63 nV/ $\sqrt{\text{Hz}}$ (-141 dBc/Hz at 10 dBm) and therefore at least 20 dB attenuation is required to meet the specification of 7.1 nV/ $\sqrt{\text{Hz}}$.

It can be concluded that typically adopted instruments are capable of supporting a 99.9% fidelity. However, for the currently used instrumentation, the specifications on the amplitude noise and wide-band additive noise are the most stringent and consequently require the use of attenuators to reduce the noise reaching the quantum devices. Moreover, these instruments are bulky, consume several watts of power, and cannot be operated at cryogenic temperatures, therefore hindering scalability.

Fully integrated complementary metal-oxide semiconductor (CMOS) circuits operating at cryogenic temperatures can be adopted to tackle this problem [5,15–20]. In order to assess the feasibility of such a solution, the power consumption of the required circuit blocks will be estimated by using room-temperature CMOS circuits as a reference. This is valid, since cryogenic CMOS circuits are expected to show significantly less noise for the same power budget, as shown in Refs. [5] and [19]. As a result, the estimates given here likely overestimate the required power consumption. Furthermore, we assume a 50- Ω load

TABLE VI. Specifications of commonly used AWGs and microwave vector sources.

	Sample rate	Resolution	Jitter	Output noise	Spurious-free dynamic range
Tektronix AWG5014C [90]	1.2 GS/s	14 bit	5.0 ps _{rms}	– ^a	< -56 dBc
Keysight M9330A [91]	1.25 GS/s	15 bit	–	-150 dBc/Hz	< -65 dBc
Tabor WX1282C [92]	1.25 GS/s	14 bit	–	–	< -44 dBc
	Maximum output frequency	Frequency resolution	Phase noise (100 kHz)	Wide-band noise (10 dBm)	
Agilent E8267D [93]	44 GHz	1 mHz	< -100 dBc/Hz	< -141 dBc/Hz	
R&S SMW200A [94]	40 GHz	1 mHz	< -100 dBc/Hz	< -134 dBc/Hz	

^aAmplitude resolution: 1 mV.

for each circuit, which is not the case for a fully integrated controller.

The core component determining the specifications of an AWG is its digital-to-analog converter (DAC). The 10-bit 500 MS/s DAC presented in Ref. [95] meets the specifications for the microwave envelope generation at a power consumption of 24 mW. For the detuning control, the DAC specifications are stricter, but can be met by the 12-bit 1.6 GS/s DAC presented in Ref. [96], with a power consumption of 40 mW. Although for the tunnel barrier the specifications will depend highly on the gate structure, a similar DAC is assumed to be sufficient.

The core component of a microwave carrier generator, the PLL, is also available as a CMOS circuit operating over the required frequency range (9.2–12.7 GHz) at a power consumption of around 13 mW [97]. Its phase-noise performance is slightly worse than required. However, with operation at cryogenic temperatures, the noise level is expected to improve.

In a linear qubit array, one DAC is required for the barrier gate and one for the plunger gate for each qubit. This leads to an estimated power of 80 mW per qubit. For the microwave signals, the envelope DAC and PLL together consume approximately 40 mW. Without any form of multiplexing, this indicates a power consumption of 120 mW/qubit. For a state-of-the-art dilution refrigerator with a cooling power of a few watts at 4 K, this suggests a maximum of a few tens of qubits when operating the classical controller at 4 K.

However, the power consumption of the DACs controlling the barrier gates and plunger gates could be highly reduced if it is not being limited to a 50- Ω system. To get a sufficient signal swing, in Ref. [96] a 16-mA current is delivered to a 50- Ω load, thereby setting a lower bound to the power consumption. A much lower current would be required for a higher impedance, or even for a lower swing as acceptable in this application, ultimately limited by the speed or noise requirement. Furthermore, the same fast DAC can be used to generate frequency-multiplexed microwave envelopes. With a sample rate of 1.6 GS/s, a bandwidth of roughly 640 MHz is available [96]. This can be used to drive 64 qubits with a Rabi frequency of 1 MHz spaced by 10 MHz using, e.g., a Gaussian envelope [Fig. 4(d)]. The combined power of the fast DAC and PLL, i.e., 53 mW, is then shared over 64 qubits, thus resulting in a power consumption below 1 mW/qubit. For the readout, on the other hand, cryogenic CMOS circuits have already been proposed that can achieve a power consumption < 1 mW/qubit [5,19].

In summary, a cryogenic CMOS controller for a large-scale quantum processor appears to be feasible for a target fidelity of 99.9%. However, for minimum power consumption, the trade-offs in the electronics design must be systematically investigated. The analysis proposed in this paper provides the foundations for such optimization

and will help electronics designers to build a functional controller.

VIII. CONCLUSION

In this paper, the effect of nonidealities in the classical controller for a quantum processor is analyzed. Even though this work focuses on single-electron spin-qubits, the presented approach can be used to analyze the performance of a quantum processor in any qubit technology. A comprehensive approach is proposed, by covering the effect of both static and dynamic errors on all quantum operations, i.e., single-qubit gates, two-qubit gates, and readout.

With the results of this analysis, the impact of the controller on the performance of the quantum computer as a whole can be quantified. This is required to ensure that the controller does not become the performance bottleneck as the qubit performance keeps improving. Moreover, with the presented results, a full set of electrical specifications can be derived, targeting a given qubit fidelity. The availability of these specifications enables the design of next-generation controllers that are tailor-made for the quantum processor and optimized for performance, power, cost, and size, so as to improve the scalability of the quantum computer.

As future controllers might have to operate physically close to the quantum processor, i.e., at cryogenic temperatures where the cooling power is limited, the power optimization of the controller will be critical in enabling large-scale quantum computing. With the results obtained in this paper, the trade-offs between qubit fidelity and power spent in the controller can be analyzed, this representing the foundation for such a power optimization.

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