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Residual Stress Characterization in Microelectronic Manufacturing: An Analysis Based on Raman Spectroscopy

Zhoudong Yang, Xinyue Wang, Wei Chen, Hongyu Tang,* Rongjun Zhang,* Xuejun Fan, Guoqi Zhang, and Jiajie Fan*

In the rapidly evolving era of information and intelligence, microelectronic devices are pivotal across various fields, such as mobile devices, big data computing, electric vehicles, and aerospace. However, the electrical performance of these devices often suffers due to residual stress from microelectronic manufacturing. This issue is compounded by the additional thermal stress that accumulates during device operation. Therefore, it is essential to understand, characterize, and control this residual stress to ensure the reliability and efficiency of microelectronic devices. Raman spectroscopy emerges as an invaluable tool for nondestructive, fast, noncontact, and precise testing of micro-scale mechanics, significantly aiding in stress and strain analysis within microelectronic manufacturing. This article aims to provide a thorough overview of the theory and application beyond a mere compilation of recent advances. Theoretically, it critically evaluates existing models that describe the Raman-stress relation. Practically, it explores the application of Raman spectroscopy in researching residual stress in various components, including substrate materials, epitaxial films, and packaging. Through a detailed examination of current applications, it highlights the significance of Raman spectroscopy in understanding micro-scale mechanics. Finally, it offers both theoretical and practical insights into the future developments of Raman-stress detection technology.

devices, especially in demanding operating environments.^[1-3] To meet these heightened demands, it is essential to leverage sophisticated microelectronic structure designs and advanced manufacturing techniques. These approaches are aimed at improving integration, reducing size, and enhancing other critical characteristics. However, a major challenge that arises in this context is the unavoidable formation of residual stresses during the manufacturing of microelectronic devices.^[4] As these devices are put to use, the resulting complex stress distribution can lead to defects, cracks, delamination fractures, and other detrimental outcomes, ultimately impairing device performance.^[5-7] It is evident that addressing the issue of residual stresses during the manufacturing processes is crucial for ensuring the reliability of the microelectronic devices in practical application. Furthermore, considering that microelectronic manufacturing predominantly occurs at the micro-scale, it becomes imperative to explore and understand the impacts of residual stress at this level. Employing

high-resolution characterization technologies for this purpose is critically important. Research in this area has the potential to significantly reduce and refine the design and manufacturing processes, offering valuable insights for practical applications.

1. Introduction

In recent years, the rapid advancement of the Internet, big data, artificial intelligence, and green energy technologies has significantly elevated performance expectations for microelectronic

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However, the exploration of micro-scale residual stress in practical microelectronic devices is fraught with challenges that must be addressed. These challenges can be broadly categorized into 3 main aspects: First, developing new micro/nano-scale mechanical experiments and testing techniques is crucial for capturing the evolution of microstructure deformation. As electronic devices continue to become more complex and miniaturized, the existing stress testing technologies are often inadequate in terms of spatial resolution at the micro-scale level.^[8,9] Furthermore, the non-destructive and accurate detection of intrinsic residual stress within electronic devices remains a significant hurdle.^[10] Intrinsic stress, which results from various manufacturing processes, such as deposition, growth, modification, corrosion, aging, material interface mismatch, and inherent defects, is not adequately addressed by current testing technologies. Lastly, there is a pressing need for testing methods capable of assessing microstructural mechanics under conditions that involve multiple fields. Electronic devices often operate in environments subject to a range of interactions—such as electrical, magnetic, thermal, humidity, and chemical or electrochemical reactions.^[11,12] Therefore, characterizing the mechanical properties of microstructures under such multi-field conditions has emerged as a key research focus in the field of micro-scale experimental mechanics.

To deepen our understanding of micro-scale mechanics and effectively address the associated challenges, it is imperative to develop new experiments, testing techniques, and non-destructive evaluation methods that feature enhanced spatial resolution. Raman spectroscopy emerges as a promising technique, capitalizing on the inelastic scattering mechanism between photons and molecules.^[13] This technique captures characteristic information about materials at the atomic and molecular scale, enabling research on microstructure, species, and physical and chemical properties.^[14,15] Given the widespread Raman activity of semiconductor materials,^[16,17] Raman spectroscopy is particularly valuable for investigating the mechanical behavior and evolution process of microelectronic manufacturing at the micro-scale. It offers a powerful means to explore and comprehend the intricate mechanisms underlying microelectronic devices, boasting advantages such as high efficiency, a non-destructive and non-contact approach, and broad universality,^[8,18–21] as shown in **Figure 1**. These inherent benefits enable the analysis of samples in multiple states, such as liquids and solids, without necessitating special preparation or destructive techniques. However, it is noteworthy that obtaining Raman spectra requires that the test sample exhibit Raman activity. Moreover, numerous factors must be optimized for acquiring high-quality Raman spectra in practical experiments, including mitigating fluorescence effects, ensuring sample uniformity, and fine-tuning test parameters, and equipment performance. Raman spectroscopy achieves a spatial resolution of 1 μm , making it exceptionally suited for researching mechanical behavior at the micro-scale. Through integration with near-field technology, its spatial resolution can be further enhanced to the level of hundreds of nanometers. The sensitivity of the Raman shift to stress provides a solid foundation for measuring residual stress and analyzing deformations in microelectronic devices. Its nondestructive and noncontact testing capabilities further facilitate the in situ detection of residual stress during the manufacturing process. Moreover, the use of fast line

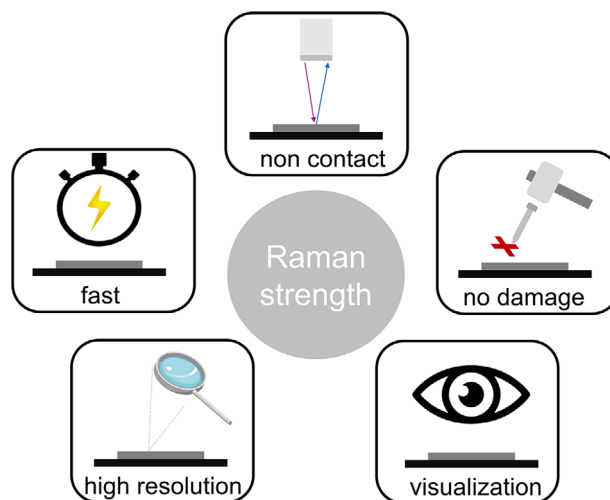


Figure 1. Overview of advantages of Raman spectroscopy technology.

scans, surface scans, and advanced image processing technologies allows comprehensive visualization of mechanical information across the entire object.

This review presents a comprehensive overview of the progress in Raman spectroscopy technology for characterizing and modeling micro-scale stress in microelectronic manufacturing. We aim to provide researchers with a systematic guide to this characterization technology, expanding the practical applications of Raman spectroscopy in residual stress research within microelectronics. Complementing existing reviews on the micro-scale mechanics of microelectronics, this article highlights the distinct advantages of Raman spectroscopy in the field of stress research in microelectronics. With a focus on basic principles of Raman-stress relations, we delve into the advancements in Raman spectroscopy and its significant role in micro-scale stress research, aiming to provide a thorough understanding of the distinctive benefits it introduces to the research of micro-scale mechanics.

This review delves into the promising capabilities of Raman spectroscopy as a novel method for characterizing residual stress in microelectronic manufacturing. We provide a systematic and thorough introduction, beginning with foundational concepts and toward practical applications (**Figure 2**). Initially, we discuss the core principles of Raman spectroscopy, followed by an explanation of the basics of Raman-stress relations. This includes the characteristic equations related to lattice dynamics and elastic mechanics. In the subsequent section, we consider the strategic selection of test wavelengths under certain application conditions, focusing on 3 critical factors: detection depth, spatial resolution, and fluorescence effects. We then survey the main development trajectories of existing Raman-stress relation models. Subsequently, we delve into the main application areas of micro-scale stress research within microelectronic manufacturing, providing detailed examples to elucidate these areas. Concluding the review, we reflect on the current challenges and highlight promising directions for further developments in this field. Our goal is to shed light on the significance and potential of Raman spectroscopy in advancing the methods for characterizing residual stress in microelectronic manufacturing processes.

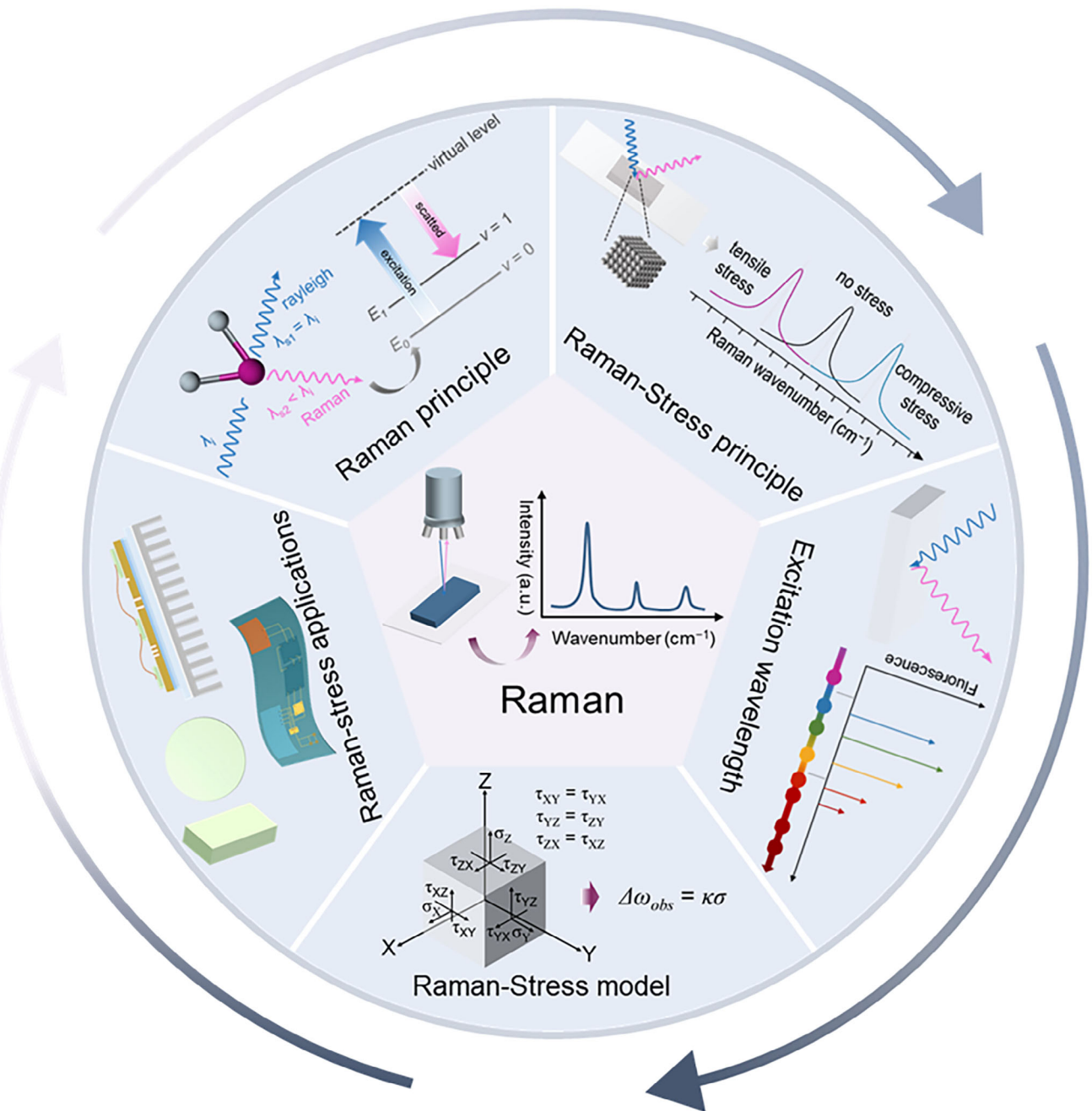


Figure 2. Schematic of the content of this review: From basic theory to practical application.

2. Basic Principles of Raman Spectroscopy

Raman measurements involve analyzing the scattered light that results from the interaction between the incoming light and the sample. This scattered light, known as Raman-scattered light, exhibits an energy different from that of the incoming light, leading to a shift in the optical frequency.^[15] The frequency shift can be described as follows:

$$\nu_s = \nu_i \pm n\nu_j \quad (1)$$

where ν_i represents the frequency of the incoming light, ν_s is the frequency of the scattered light, ν_j denotes the frequency of phonons in the sample, and n stands for the number of scattering series.

When $n = 0$, the scattering occurs without any change in energy, referred to as Rayleigh scattering. For $n \neq 0$, inelastic scattering takes place, involving a change in energy; this is known as Raman scattering, as shown in Figure 3. The Stokes shift when the frequency of scattered light decreases, losing energy, whereas the anti-Stokes shift occurs when the frequency of scattered light

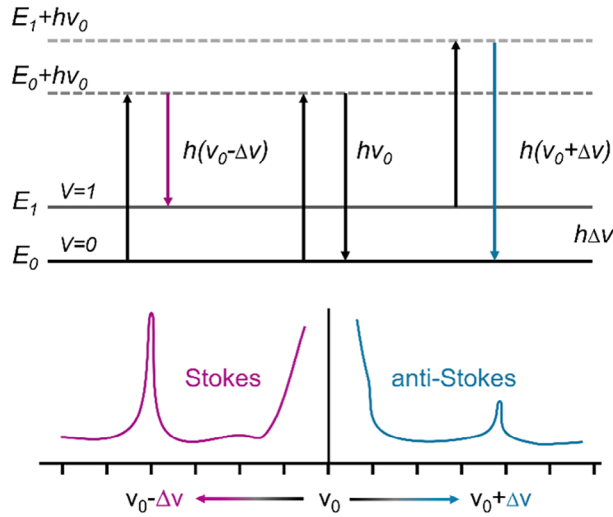


Figure 3. Schematic of Raman scattering.

increases, gaining energy. Given that energy distribution typically follows the Boltzmann distribution, the Stokes shift is more common.

Raman data are presented in a Raman spectrum, where the horizontal axis (abscissa) of the spectrogram indicates the Raman wavenumber or frequency. This unit measures the shift in wavenumber relative to the incident excitation light. Notably, the wavenumber of the Raman spectrum remains constant during the same sample test, regardless of changes in the excitation wavelength. Therefore, the abscissa of the Raman spectrum signifies the wavenumber difference (Unit: cm^{-1}) between the incoming wavelength (λ_i) and the scattered wavelength (λ_s), calculated as follows:^[22]

$$\omega_0 = \omega_i - \omega_s \quad (2)$$

where ω_0 is the observed Raman frequency, ω_i is the Raman frequency of the incoming light, and ω_s is the Raman frequency of the scattered light.

The Raman spectrum originates from changes in molecular polarizability during the measurement process.^[23] Given that Raman lasers emit monochromatic light, the electromagnetic theory posits that molecules experience polarization deformation under the influence of external electric fields (incoming light), producing an induced dipole moment μ . When the external electric field is small, μ is directly proportional to the electric field strength E :

$$\mu = \alpha E \quad (3)$$

Assuming the molecule vibrates in a simple harmonic manner, the change in polarizability can be expressed as a Taylor expansion around the equilibrium position:

$$\alpha = \alpha_0 + \left(\frac{\partial \alpha}{\partial \theta} \right)_0 \theta \quad (4)$$

where α_0 represents the polarizability at the equilibrium position; θ denotes the normal coordinate when the molecule vibrates.

Therefore, μ under an external electric field can be articulated as:

$$\begin{aligned} \mu = & \alpha_0 E_0 \cos 2\pi\nu_0 t + \frac{1}{2} E_0 \theta_0 \left(\frac{\partial \alpha}{\partial \theta} \right)_0 \theta \cos 2\pi(\nu_0 - \nu_m) t \\ & + \frac{1}{2} E_0 \theta_0 \left(\frac{\partial \alpha}{\partial \theta} \right)_0 \theta \cos 2\pi(\nu_0 + \nu_m) t \end{aligned} \quad (5)$$

where ν_0 is the frequency of the incoming light, and ν_m is the vibration frequency of the molecule. The first term of Equation (5) corresponds to Rayleigh scattering, the second term to Stokes scattering causing the frequency of scattered light to be smaller than that of incoming light, and the third term to blue-shifted anti-Stokes scattering.

In addition, it is observed that the probability of a molecular vibration mode's appearance in a Raman spectrum is dependent on the value of $(\partial \alpha / \partial \theta)_0$. Only when $(\partial \alpha / \partial \theta)_0 \neq 0$, which means the change of polarizability at the equilibrium position to the normal coordinate θ is nonzero. This principle is called the Raman selection rule.

The actual scattering efficiency (I) of the Raman spectrum is influenced by the polarization vectors, e_i and e_s , which correspond to the polarization vector of the incoming and scattered light, respectively:^[24]

$$I = Q \sum_k |e_i \cdot R_k \cdot e_s|^2 \quad (6)$$

where Q is a constant determined by the Raman spectrometer, and R_k represents the sample's Raman tensor, which varies according to the crystal type and vibration mode of the sample.

For further insight into the basic principles of Raman spectroscopy, we refer the readers to consult the seminal works of Manuel Cardona^[25-27] and I. R. Lewis^[28] for a more comprehensive understanding of Raman measurements.

3. Theoretical Basis of Raman-Stress Relation

The Raman spectrum unveils the lattice vibration characteristics of the tested sample, making Raman mechanics measurements pivotal in understanding the relationship between stress and the Raman wavenumber. This relationship, grounded in lattice dynamics theory, is elucidated by combining elastic mechanics (via the generalized Hooke's law) with material characteristic parameters. Finally, the conversion factor between material stress and Raman frequency shift can be obtained, also known as the Raman shift-stress coefficient (Unit: $\text{cm}^{-1} \text{ GPa}^{-1}$), as shown in Figure 4a.

Ganesan et al. derived the expression for the lattice dynamics characteristic equation of a 3D crystal under stress, employing the harmonic oscillator model. In this framework, the potential energy function of the system is approximated using a quadratic term to account for simple harmonic vibrations:^[29]

$$\begin{vmatrix} \epsilon_{uv} K_{uv11} - \lambda & \epsilon_{uv} K_{uv12} & \epsilon_{uv} K_{uv31} \\ \epsilon_{uv} K_{uv21} & \epsilon_{uv} K_{uv22} - \lambda & \epsilon_{uv} K_{uv32} \\ \epsilon_{uv} K_{uv31} & \epsilon_{uv} K_{uv32} & \epsilon_{uv} K_{uv33} - \lambda \end{vmatrix} = 0 \quad (7)$$

where $u, v = 1, 2, 3$, and K_{uvwx} represents a component of the phonon deformation tensor, contingent on the crystal structure and optical phonon modes ($w, x = 1, 2, 3$). The strain tensors ϵ_{11} ,

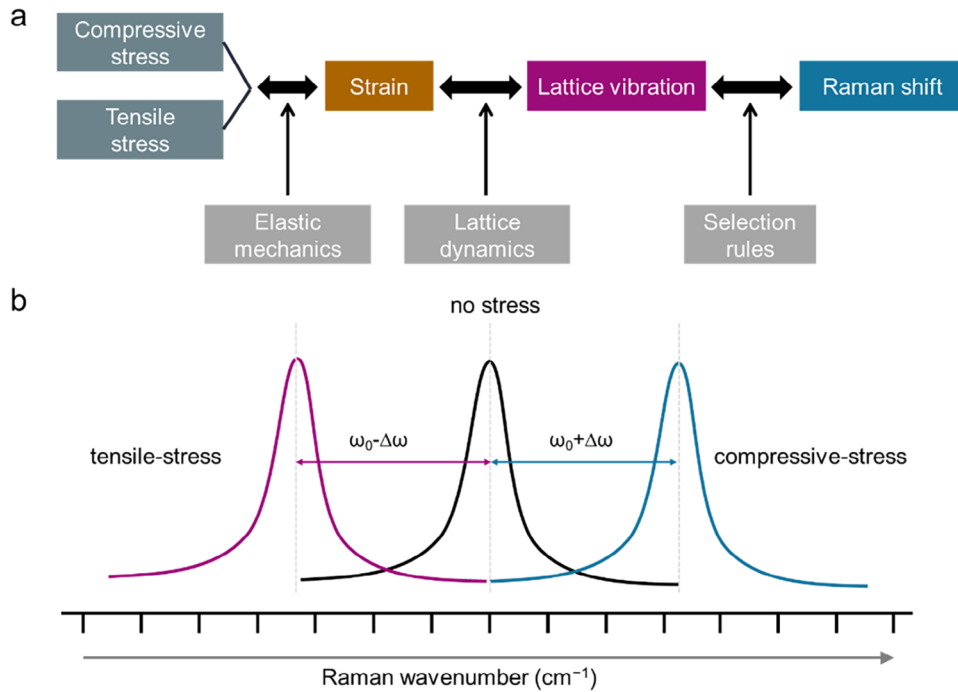


Figure 4. Raman-stress relation theory. a) Basic theoretical framework of Raman-stress relation. b) Relation between Raman shift and stress.

ϵ_{22} and ϵ_{33} correspond to strains along the X, Y, and Z directions, respectively, while ϵ_{12} , ϵ_{23} , and ϵ_{13} denote the strain tensors along the XY, YZ, and XZ planes, respectively. The eigenvalue of the determinant is denoted as λ_k :

$$\lambda_k = \omega_k^2 - \omega_0^2 \quad (8)$$

where ω_k signifies the Raman wavenumber under stress, and ω_0 indicates the Raman wavenumber without stress.

The characteristic equation of lattice dynamics elucidated above provides insights into the stress response to internal spacing within the sample. Raman spectroscopy thus aims to detect changes in atomic spacing (strain) within the material lattice under stress. This technique seeks to establish a functional relationship between changes in the Raman wavenumber and applied stress in the Raman spectra. By defining the change in the Raman wavenumber as the difference between the wavenumber under stress and the intrinsic wavenumber without stress, we observe macroscopic phenomena in the Raman spectrum attributable to stress. Specifically, the Raman wavenumber shifts toward red (decreases) under tensile-stress. Conversely, under compressive-stress, the Raman wavenumber undergoes a blue-shift (increases),^[30–32] as shown in Figure 4b. Given that the Raman wavenumber change caused by stress is typically small compared to the peak position value (ω_0) without stress, this change can be further approximated and simplified to understand the wavenumber change before and after stress application:^[29]

$$\Delta\omega_k = \omega_k - \omega_0 \approx \frac{\omega_k^2 - \omega_0^2}{2\omega_0} = \frac{\lambda_k}{2\omega_0} \quad (9)$$

In Raman spectroscopy, each characteristic peak signifies the interaction between incoming light and specific atomic vibra-

tional modes. It has been observed in the literature that^[33–35] the peak shift value under stress, as derived from lattice dynamics equations, can present multiple solutions, resulting in a degenerate state. Specifically, the number of solutions corresponds to the number of Raman tensors associated with the chosen vibrational modes. Consequently, when the Raman tensor count for a selected vibration mode is more than one, the observed shift in Raman peaks reflects the combination of multiple degenerate states.

Due to the small deformation of the material, there exists a linear relationship between stress and strain, facilitating the application of the generalized Hooke's law in elastic mechanics:

$$\sigma = C\varepsilon \quad (10)$$

where C denotes the elastic coefficient tensor specific to the material, while σ and ε represent the stress and strain tensors, respectively.

Following the derivation of the Raman shift from the lattice dynamics equation, when the count of Raman tensors associated with the chosen vibrational modes exceeds one, the Raman characteristic peak in the measured spectrum is formed by the superposition of multiple degeneracy states. Consequently, even under conditions of micro-deformation, while the Raman wavenumber shifts, the degeneracy state persists without significant peak splitting. Therefore, the observed changes in the Raman wavenumber under these conditions also arise from the superposition of multiple degenerate states. To calculate this shift, the following equation must be emphasized:^[29,36]

$$\Delta\omega_{obs} = \frac{\sum_{k=1}^3 \Delta\omega_k I_k}{\sum_{k=1}^3 I_k} \quad (11)$$

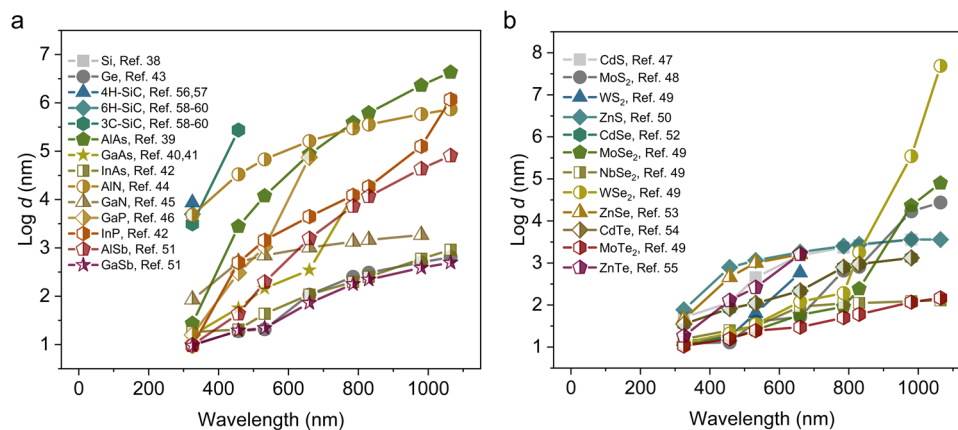


Figure 5. Maximum penetration depth of laser light with different wavelengths in semiconductor materials. a) Log d -laser wavelength relationship diagram of Si, Ge, SiC, and N compound semiconductor. b) Log d -laser wavelength relationship diagram of chalcogenide semiconductor.

Equation (11) indicates that the degenerate states are amalgamated and linearly superimposed. When integrated with Equations (3–7), a final linear relationship is established, correlating the change in Raman displacement to the stress applied before and after, as outlined below:

$$\Delta\omega_{\text{obs}} = \kappa\sigma \quad (12)$$

where the slope κ provides the Raman shift-stress coefficient.

4. Raman Excitation Light Selection Basis

In microelectronic micro-scale stress research, Raman spectroscopy is a non-destructive and contact-free detection method. A key test parameter is the maximum penetration depth (d), which determines the specific range of the actual stress test. The laser used determines the Raman spectrometer's excitation wavelength, resulting in various penetration depths for different material samples. Hence, selecting an appropriate laser wavelength is important for obtaining accurate experimental results in micro-scale stress research. The formula for calculating the maximum penetration depth for different wavelengths is:^[37]

$$d = \frac{2.3}{2\alpha} \quad (13)$$

where α (Unit: cm^{-1}) represents the light absorption coefficient of the test material at the incoming wavelength (λ_i).

Figure 5 illustrates the maximum penetration depths for main semiconductor materials at various excitation wavelengths. Data analysis from the figure indicates a consistent trend: the maximum detection depth increases with the wavelength for most semiconductor materials. This trend is instrumental in selecting an appropriate excitation wavelength to meet the depth requirements of Raman measurements for different materials. The data corresponding to the specific excitation wavelength and maximum penetration depths are detailed in Table 1. The actual detection depth is also influenced by factors such as the sample's surface morphology, the laser power employed, the choice of scattering angle, and the focus depth during experimentation.

Moreover, the excitation wavelength directly influences the Raman spatial resolution (D), which is influenced by the laser spot size. The diameter of the laser spot can be calculated under the diffraction-limited condition:^[61]

$$D = \frac{0.61\lambda_i}{NA} \quad (14)$$

where λ_i is the wavelength of the incident laser and NA is the numerical aperture of the microscope objective used. According to Equation (14), the laser spot size is a function of λ_i and the NA of the microscope objective lens. To maintain an appropriate D value with long-wavelength excitation light, it becomes imperative to appropriately increase the NA of the microscope objective.

The choice of excitation wavelength also needs to consider the laser's interaction with the sample. If the excitation light satisfies the conditions for the sample to absorb photons of characteristic frequency, electrons move from the ground-state to the first electronic excited state or higher. These electrons then relax to the lowest energy level of the first excited state before moving to various vibrational energy levels of the ground-state, emitting fluorescence. This fluorescence can interfere with identifying characteristic Raman peak positions and their full width at half maximum (FWHM).^[62,63] However, using lasers in the ultraviolet and infrared bands can significantly mitigate this fluorescence effect on data analysis, as shown in Figure 6a. With ultraviolet light excitation, although fluorescence occurs, the Raman and fluorescence signals do not overlap^[64] (Figure 6b). Conversely, infrared light excitation less readily excites ground-state electrons, reducing fluorescence effects. Furthermore, Figure 6c provides a concise overview of the strengths and weaknesses of the 3 excitation bands: ultraviolet, visible, and infrared.

In conclusion, the data provide valuable insights for selecting suitable excitation wavelengths for Raman measurements in Table 1, considering detection depth and spatial resolution. Moreover, understanding the impact of excitation wavelength on fluorescence background helps in making informed decisions for accurate Raman measurements.

Table 1. Detection depth in semiconductor materials under different wavelengths.

		325 nm	457 nm	532 nm	660 nm	785 nm	830 nm	980 nm	1064 nm
Si [38]	α cm ⁻¹	1.23 × 10 ⁶	2.31 × 10 ⁴	7.99 × 10 ³	2.59 × 10 ³	9.46 × 10 ²	6.24 × 10 ²	91.5	9.76
	<i>d</i> nm	9.33	4.98 × 10 ²	1.44 × 10 ³	4.44 × 10 ³	1.22 × 10 ⁴	1.84 × 10 ⁴	1.26 × 10 ⁵	1.18 × 10 ⁶
AlAs [39]	α cm ⁻¹	4.2 × 10 ⁵	4.14 × 10 ³	9.68 × 10 ²	1.33 × 10 ²	29.5	18.5	4.97	2.69
	<i>d</i> nm	27.4	2.78 × 10 ³	1.19 × 10 ⁴	8.65 × 10 ⁴	3.90 × 10 ⁵	6.20 × 10 ⁵	2.31 × 10 ⁶	4.28 × 10 ⁶
GaAs [40,41]	α cm ⁻¹	7.38 × 10 ⁵	2.02 × 10 ⁵	7.94 × 10 ⁴	3.30 × 10 ⁴	1.42 × 10 ⁴	8.66 × 10 ³	0	0
	<i>d</i> nm	15.6	56.9	1.45 × 10 ²	3.49 × 10 ²	1.22 × 10 ⁴	1.84 × 10 ⁴	/	/
InAs [42]	α cm ⁻¹	6.33 × 10 ⁵	5.64 × 10 ⁵	2.66 × 10 ⁵	1.06 × 10 ⁵	5.79 × 10 ⁴	4.56 × 10 ⁴	1.93 × 10 ⁴	1.26 × 10 ⁴
	<i>d</i> nm	18.2	20.4	43.3	1.08 × 10 ²	1.98 × 10 ²	2.52 × 10 ²	5.95 × 10 ²	9.13 × 10 ²
Ge [43]	α cm ⁻¹	1.16 × 10 ⁶	6.04 × 10 ⁵	5.55 × 10 ⁵	1.06 × 10 ⁵	4.57 × 10 ⁴	3.76 × 10 ⁴	2.29 × 10 ⁴	1.79 × 10 ⁴
	<i>d</i> nm	9.94	19.0	20.7	1.08 × 10 ²	2.52 × 10 ²	3.06 × 10 ²	5.03 × 10 ²	6.44 × 10 ²
AlN [44]	α cm ⁻¹	2.40 × 10 ⁴	3.45 × 10 ²	1.70 × 10 ²	70.9	38.6	32.2	19.6	15.6
	<i>d</i> nm	4.79 × 10 ³	3.33 × 10 ⁴	6.78 × 10 ⁴	1.62 × 10 ⁵	2.98 × 10 ⁵	3.57 × 10 ⁵	5.88 × 10 ⁵	7.38 × 10 ⁵
GaN [45]	α cm ⁻¹	1.35 × 10 ⁵	2.27 × 10 ⁴	1.65 × 10 ⁴	1.13 × 10 ⁴	8.57 × 10 ³	7.88 × 10 ³	6.20 × 10 ³	1.21 × 10 ⁴
	<i>d</i> nm	85.2	5.07 × 10 ²	6.96 × 10 ²	1.02 × 10 ³	1.34 × 10 ³	1.46 × 10 ³	1.85 × 10 ³	/
GaP [46]	α cm ⁻¹	7.29 × 10 ⁵	3.77 × 10 ⁴	1.10 × 10 ⁴	1.54 × 10 ²	0	0	0	0
	<i>d</i> nm	15.8	3.05 × 10 ²	1.04 × 10 ³	7.46 × 10 ⁴	/	/	/	/
InP [42]	α cm ⁻¹	6.92 × 10 ⁵	1.96 × 10 ⁵	9.53 × 10 ⁴	5.83 × 10 ⁴	4.23 × 10 ⁴	3.26 × 10 ⁴	8.47 × 10 ²	7.14 × 10 ²
	<i>d</i> nm	9.33	4.98 × 10 ²	1.44 × 10 ³	4.44 × 10 ³	1.22 × 10 ⁴	1.84 × 10 ⁴	1.26 × 10 ⁵	1.18 × 10 ⁶
CdS [47]	α cm ⁻¹	2.27 × 10 ⁵	1.01 × 10 ⁵	2.53 × 10 ⁴	7.56 × 10 ³	4.95 × 10 ³	4.40 × 10 ³	3.18 × 10 ³	/
	<i>d</i> nm	50.7	1.14 × 10 ²	4.54 × 10 ²	1.52 × 10 ³	2.33 × 10 ³	2.62 × 10 ³	3.60 × 10 ³	/
MoS ₂ [48]	α cm ⁻¹	9.39 × 10 ⁵	8.94 × 10 ⁵	2.84 × 10 ⁵	2.21 × 10 ⁵	1.75 × 10 ⁴	1.42 × 10 ⁴	6.79 × 10 ³	4.18 × 10 ³
	<i>d</i> nm	12.2	12.9	40.5	52.1	6.59 × 10 ²	8.10 × 10 ²	1.69 × 10 ⁴	2.75 × 10 ⁴
WS ₂ [49]	α cm ⁻¹	8.36 × 10 ⁵	6.50 × 10 ⁵	1.80 × 10 ⁵	1.96 × 10 ⁴	0	0	0	0
	<i>d</i> nm	13.8	17.7	64.0	5.88 × 10 ²	/	/	/	/
ZnS [50]	α cm ⁻¹	1.49 × 10 ⁵	1.46 × 10 ⁴	1.00 × 10 ⁴	6.30 × 10 ³	4.57 × 10 ³	4.15 × 10 ³	3.16 × 10 ³	/
	<i>d</i> nm	77.1	7.86 × 10 ²	1.15 × 10 ³	1.82 × 10 ³	2.52 × 10 ³	2.77 × 10 ³	3.64 × 10 ³	3.60 × 10 ³
AlSb [51]	α cm ⁻¹	1.17 × 10 ⁶	2.66 × 10 ⁵	5.90 × 10 ⁴	7.36 × 10 ³	1.58 × 10 ³	9.89 × 10 ²	2.65 × 10 ²	1.44 × 10 ²
	<i>d</i> nm	9.80	43.2	1.95 × 10 ²	1.56 × 10 ³	7.29 × 10 ³	1.16 × 10 ⁴	4.33 × 10 ⁴	8.09 × 10 ⁴
GaSb [51]	α cm ⁻¹	1.21 × 10 ⁶	5.90 × 10 ⁵	4.29 × 10 ⁵	1.58 × 10 ⁵	6.46 × 10 ⁴	5.20 × 10 ⁴	2.97 × 10 ⁴	2.32 × 10 ⁴
	<i>d</i> nm	9.54	19.5	21.9	72.6	1.78 × 10 ²	2.21 × 10 ²	3.87 × 10 ²	4.96 × 10 ²
CdSe [52]	α cm ⁻¹	3.27 × 10 ⁵	1.39 × 10 ⁵	1.05 × 10 ⁵	5.59 × 10 ⁴	1.45 × 10 ⁴	1.22 × 10 ⁴	8.70 × 10 ³	/
	<i>d</i> nm	35.2	82.6	1.10 × 10 ²	2.17 × 10 ²	7.94 × 10 ²	9.40 × 10 ²	1.32 × 10 ³	/
MoSe ₂ [49]	α cm ⁻¹	9.92 × 10 ⁵	7.02 × 10 ⁵	4.52 × 10 ⁵	1.97 × 10 ⁵	1.26 × 10 ⁵	4.74 × 10 ⁴	4.97 × 10 ²	1.45 × 10 ²
	<i>d</i> nm	11.6	16.4	25.4	58.3	91.0	2.43 × 10 ²	2.31 × 10 ⁴	7.91 × 10 ⁴
NbSe ₂ [49]	α cm ⁻¹	7.44 × 10 ⁵	4.62 × 10 ⁵	3.69 × 10 ⁵	1.26 × 10 ⁵	1.05 × 10 ⁵	1.04 × 10 ⁵	9.34 × 10 ⁴	8.81 × 10 ⁴
	<i>d</i> nm	15.5	24.9	31.1	91.6	1.09 × 10 ²	1.11 × 10 ²	1.23 × 10 ²	1.30 × 10 ²
WSe ₂ [49]	α cm ⁻¹	9.01 × 10 ⁵	5.44 × 10 ⁵	3.31 × 10 ⁵	9.60 × 10 ⁴	6.00 × 10 ⁴	6.42 × 10 ³	33.1	2.35 × 10 ⁻²
	<i>d</i> nm	12.7	21.1	34.7	1.20 × 10 ²	1.92 × 10 ²	1.79 × 10 ³	3.48 × 10 ⁵	4.89 × 10 ⁷
ZnSe [53]	α cm ⁻¹	2.52 × 10 ⁵	2.51 × 10 ⁴	1.13 × 10 ⁴	7.16 × 10 ³	/	/	/	/
	<i>d</i> nm	45.6	4.58 × 10 ²	1.01 × 10 ³	1.61 × 10 ³	/	/	/	/
CdTe [54]	α cm ⁻¹	6.33 × 10 ⁵	1.30 × 10 ⁵	8.76 × 10 ⁴	6.00 × 10 ⁴	2.18 × 10 ⁴	7.57 × 10 ³	4.83 × 10 ³	/
	<i>d</i> nm	35.2	82.6	1.10 × 10 ²	2.17 × 10 ²	7.94 × 10 ²	9.40 × 10 ²	1.32 × 10 ³	/
MoTe ₂ [49]	α cm ⁻¹	1.08 × 10 ⁶	7.31 × 10 ⁵	4.69 × 10 ⁵	3.87 × 10 ⁵	2.30 × 10 ⁵	1.91 × 10 ⁵	9.87 × 10 ⁴	7.82 × 10 ⁴
	<i>d</i> nm	10.6	15.7	24.5	29.7	50.1	60.2	1.17 × 10 ²	1.47 × 10 ²
ZnTe [55]	α cm ⁻¹	6.29 × 10 ⁵	9.01 × 10 ⁴	4.40 × 10 ⁴	7.16 × 10 ³	/	/	/	/
	<i>d</i> nm	18.3	1.28 × 10 ²	2.62 × 10 ²	1.61 × 10 ³	/	/	/	/
4H-SiC [56,57]	α cm ⁻¹	1.33 × 10 ³	0	0	0	0	0	0	0
	<i>d</i> nm	8.65 × 10 ³	/	/	/	/	/	/	/
6H-SiC [58-60]	α cm ⁻¹	2.30 × 10 ³	0	0	0	0	0	0	0
	<i>d</i> nm	5.00 × 10 ³	/	/	/	/	/	/	/
3C-SiC [58-60]	α cm ⁻¹	3.66 × 10 ³	42.0	0	0	0	0	0	0
	<i>d</i> nm	3.14 × 10 ³	2.74 × 10 ⁵	/	/	/	/	/	/

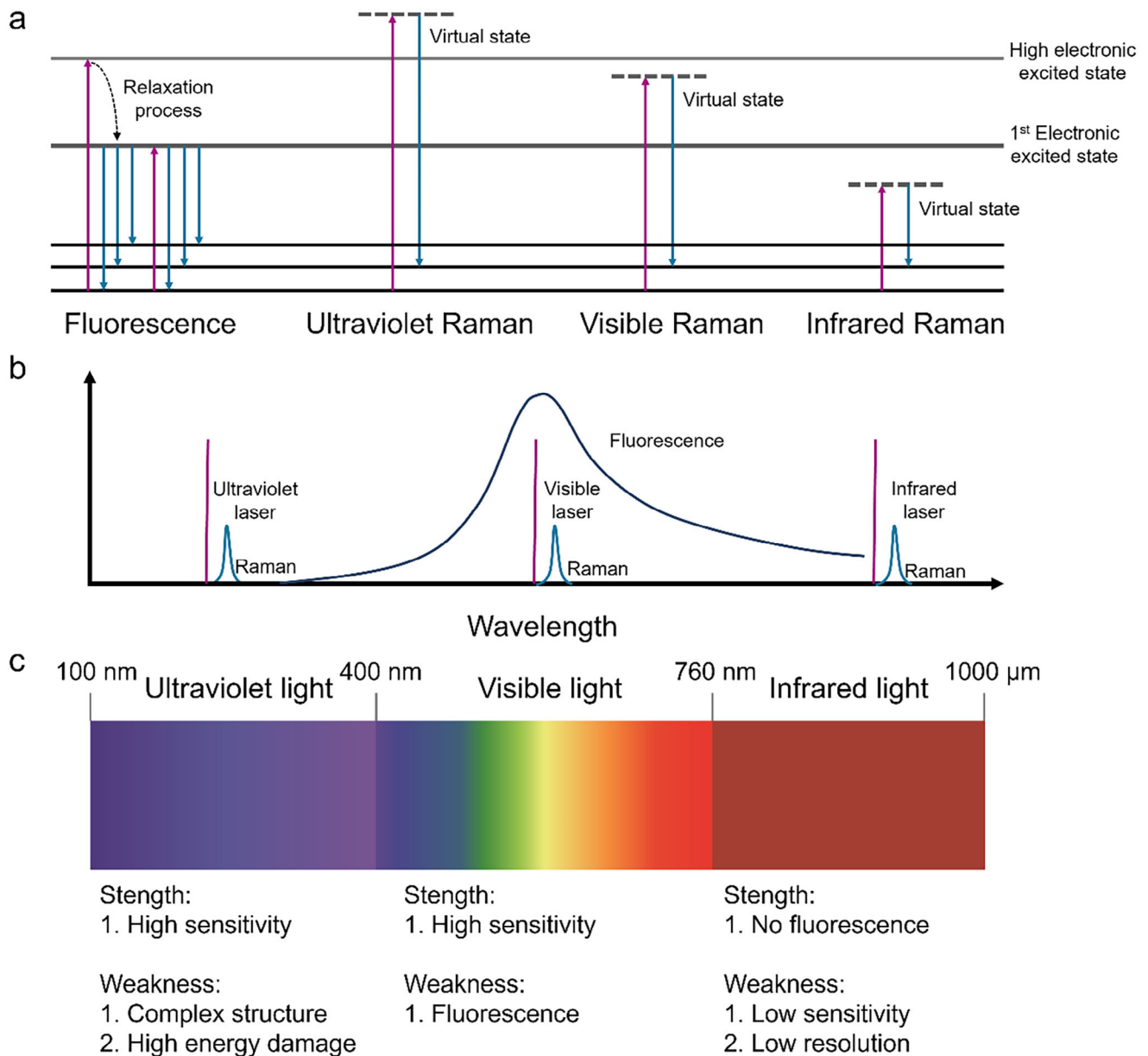


Figure 6. Reasonable selection of light band. a) Fluorescence effect and Raman scattering under different light bands. b) Influence of fluorescence effect on spectra under different light bands. c) Strength and weakness of each excitation light band.

5. Development Process of Raman-Stress Relation Model

The core of researching micro-scale mechanics in microelectronics using Raman spectroscopy centers on developing stress prediction models tailored to specific research subjects. The accuracy of the model directly influences the precision of stress numerical calculations. Thus, a high-precision model holds significant reference value and practical applications for stress research, mechanical reliability analysis, and guidance in manufacturing processes. This section delves into the Raman-stress relation models cited in various research, summarizing its key developmental process, as shown in **Figure 7** insights for refining existing models and proposing new high-precision stress models.

Historically, the exploration of Raman-stress relation models has advanced significantly. Initially, the research work on Raman-stress relation models can be traced back to the research on silicon (Si) Raman spectroscopy under static uniaxial stress conducted in 1970.^[65] Researchers noted the shift and splitting of Raman peaks of optical phonons due to applied uniaxial stress, leading to the identification of a phenomenological coefficient that describes the “spring constant” of phonons that changes with strain. This marked the beginning of mechanical stress in microelectronic devices based on Raman technology has gradually attracted attention and development. Raman spectroscopy has been applied to investigate stress-related phenomena in various microelectronic components, such as the local stress in “bird’s beak” structures during Si local oxidation,^[66] residual

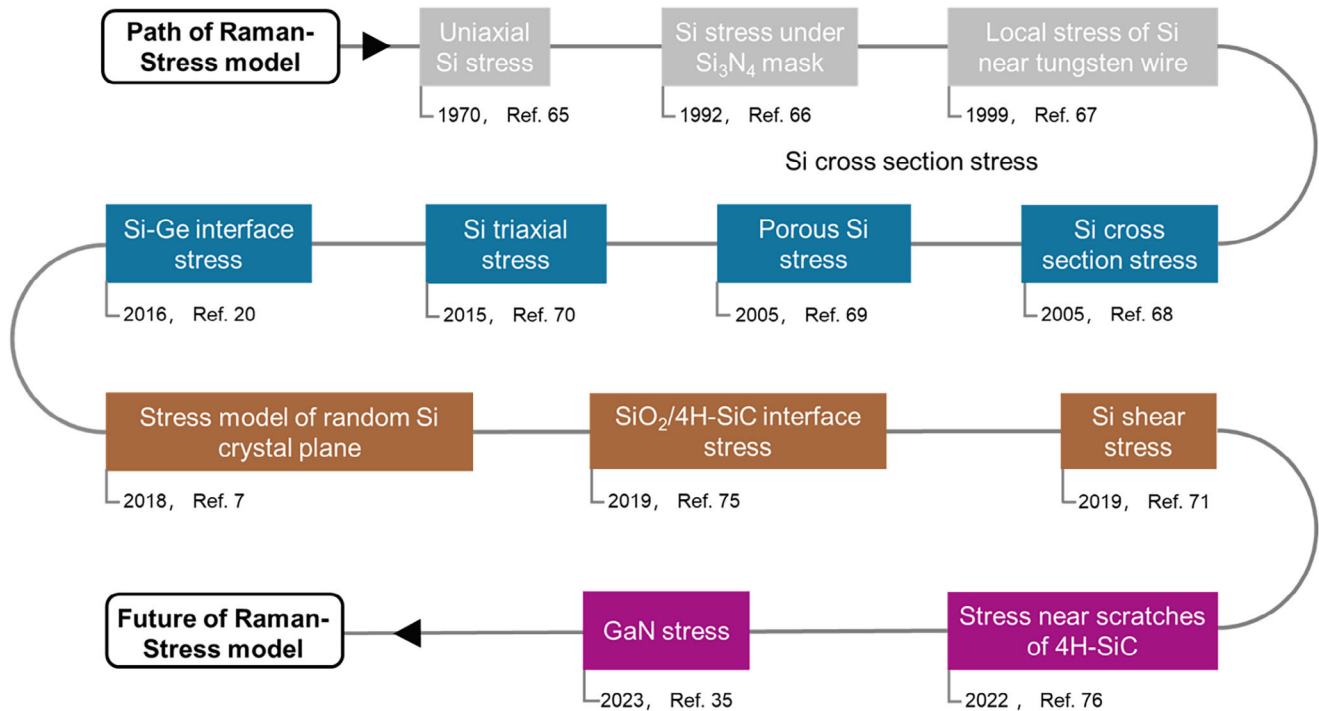


Figure 7. Roadmap of Raman-stress relation models with major achievements.

stress in Si substrates,^[67,68] stress across silicon-based heterojunction interfaces,^[20] and interface stress in microelectromechanical systems (MEMS).^[69]

In silicon-based Raman-stress relation research, 3 major advancements are noteworthy: a) the development of a Raman-stress relation model accounting for actual triaxial stress conditions,^[70] optimizing the simpler plane stress prediction models based on uniaxial or biaxial stresses assumptions; b) the introduction of a prediction equation for assessing stress on different crystalline Si planes.^[7] This advancement establishes a comprehensive framework for stress analysis of different crystal planes in Si, greatly enhancing the understanding of stress distribution in silicon-based devices and its impact on different device structures. c) In actual situations, when an object deforms due to external factors (load, temperature changes, etc.), internal stress will be formed in any section inside it. Internal stress can be decomposed into normal stress perpendicular to the cross-section and shear stress tangent to the cross-section. Therefore, a sole consideration of the three-axis normal stress proves inadequate for precise force analysis of the object. To address this limitation, researchers further incorporated actual shear stress conditions to develop a new type of Si crystal Raman-stress relation model.^[71] This significant improvement greatly reduces calculation errors caused by previously neglecting the effects of shear stress. These developments have propelled forward the understanding of stress in silicon-based materials, offering significant promise for optimizing the design, performance, and reliability of microelectronic devices.

Moreover, the immense potential of third-generation semiconductors^[72,73] has expanded the application of Raman spectroscopy to stress characterization in novel semiconductor materials such as silicon carbide (SiC)^[74–77] and gallium ni-

tride (GaN).^[35,78,79] Researchers have developed Raman-stress relation models for these materials, facilitating accurate stress characterization and broadening their applications.

In summary, while the development of Raman-stress relation models has predominantly focused on silicon—attributed to its longstanding predominance in semiconductor device markets and manufacturing, recent advancements in third-generation semiconductors, such as SiC and GaN, have catalyzed a growing interest in investigating stress/strain in these emerging materials.

6. Application of Raman Characterization and Modeling in Microelectronic Manufacturing

By combining theoretical Raman-stress relation models with experimental Raman measurement techniques, we can efficiently calculate and visualize the distribution of residual stress within a research object. This process of residual stress testing is simple, swift, and non-destructive, making it highly suitable for actual production and manufacturing processes. In this section, we categorize and discuss the main applications of this technology in the key processes of microelectronics manufacturing (**Figure 8**). This discussion aims to deepen the understanding of this technology and inspire new insights and approaches for its application across various research fields.

6.1. Substrate Materials

Substrate materials are crucial in the production of semiconductor devices. The stress within these materials significantly impacts both the manufacturing process and the performance of

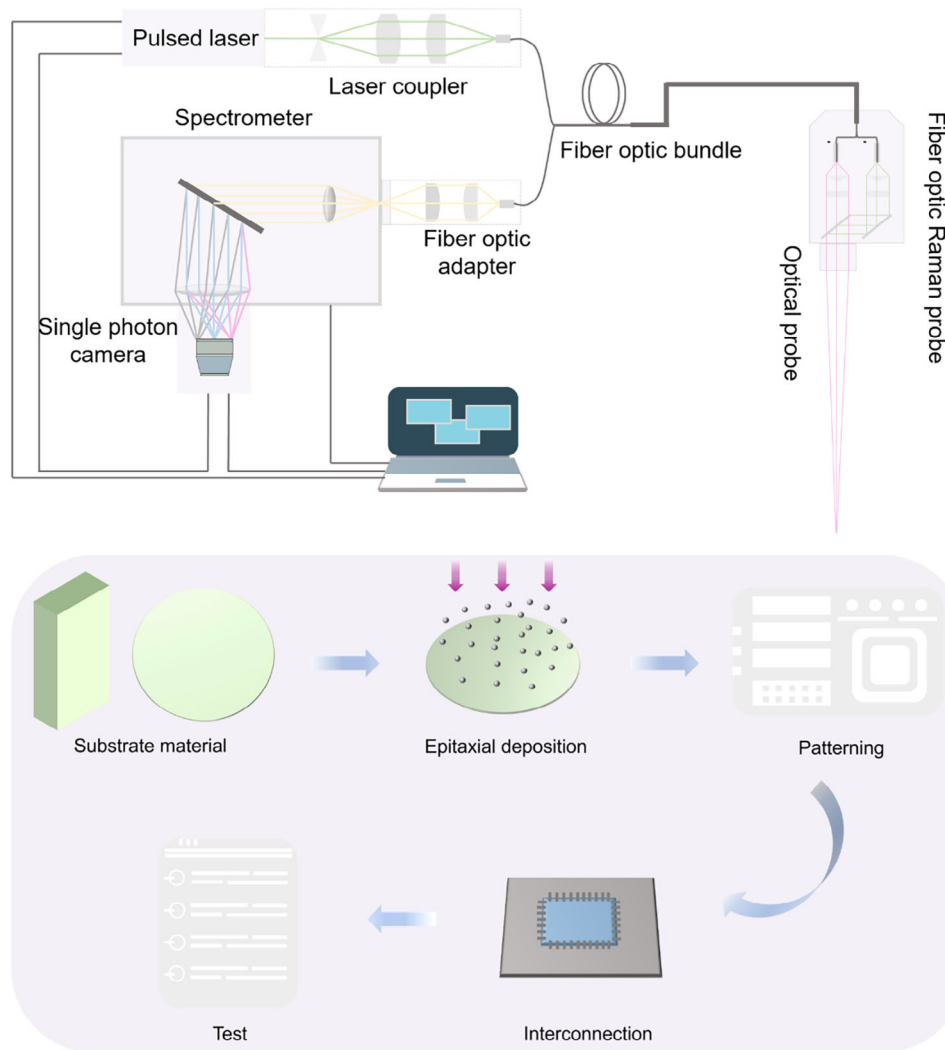


Figure 8. Schematic of application of Raman in the detection of residual stress in the field of microelectronics manufacturing.

the final product. Therefore, accurately detecting and evaluating this stress is crucial. This assessment helps identify areas of high-stress concentration, offering essential guidance for implementing strategies to alleviate and mitigate stress in industrial settings. A comprehensive analysis of stress levels in substrate materials allows manufacturers to optimize the performance and reliability of their devices. Stress detection is crucial for enhancing the yield and quality of semiconductor products, making it a vital component of semiconductor manufacturing.

6.1.1. Silicon (Si)

The significance of silicon-based semiconductors in practical applications and research is well recognized, leading to a focused effort on stress analysis in Si substrates. The exploration of Raman-stress relationships within single-crystal Si dates back to 1970 when Anastassakis et al.^[65] conducted pioneering research on the impacts of static uniaxial stress on the optical phonon frequencies of single-crystal Si. However, due to the limitations of

Raman spectroscopy at that time, this method of stress detection was not widely adopted for many years. Nevertheless, advancements in Raman technology from the late 20th century onward have facilitated significant progress in the research of Si stress distribution. Key achievements in this period encompass the measurement of Si Raman-stress relation,^[80,81] stress analysis in Si defect regions,^[82–84] exploration of residual stress in Si machining processes,^[85] and comprehensive characterization of Si wafer stress,^[86] among others. However, these studies have predominantly focused on the conventional (001) crystal plane of Si, constraining their broader applicability due to the anisotropic nature of different crystal planes.

To address the previously mentioned limitations, Huang et al.^[87] conducted experiments to establish the Raman-stress relation for the Si (111) crystal plane. By conducting multiple measurements with varying directions of scattered light and employing the least-squares method for linearly fitting the data, they derived conclusive results, as shown in **Figure 9**. This precise determination of the Raman-stress relation within single-crystal Si facilitated subsequent investigations by Pogue

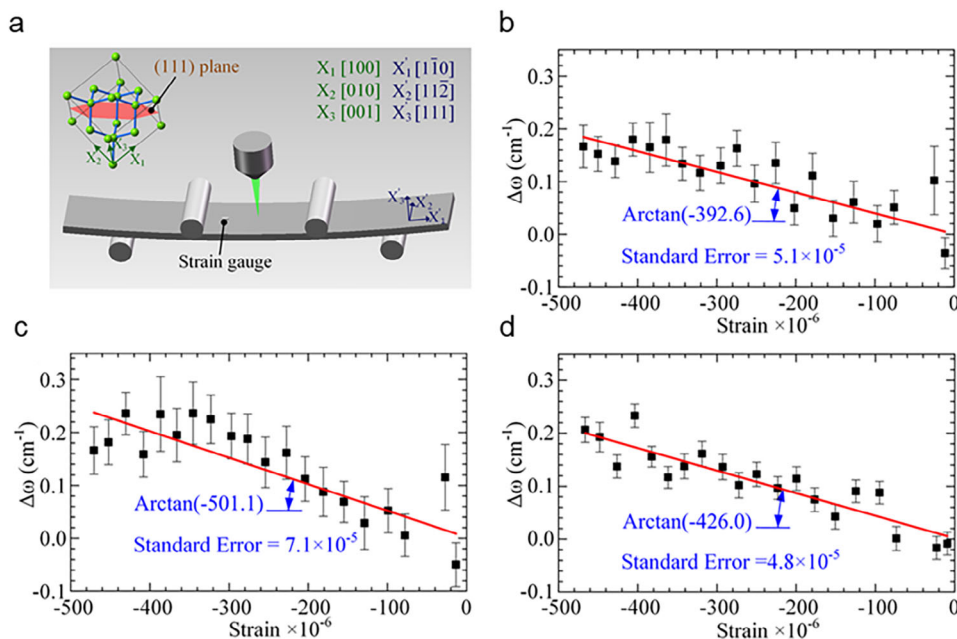


Figure 9. Raman-stress relation of Si. a) Schematic of sample test experiment. Calibration results of Raman shift-stress relationship when incoming light is in X_1 direction and scattered light is in b) X_1 , c) X_2 , and d) $X_1 + X_2$. Reproduced under the terms of the CC-BY 4.0 license.^[87] Copyright 2017, The Authors, published by AIP Publishing.

et al.,^[88] who expanded this relationship to examine the residual stress induced by thermal gradients and wire sawing processes in photovoltaic polycrystalline Si wafers during casting. The research involved stepwise etching of multi-crystalline Si diamond wire sawing (DWS) and loose abrasive wire sawing (LAWS) wafers at different depths, followed by characterization of residual stress in specific regions on each wafer using polarized confocal Raman microscopy. The results indicated that both DWS and LAWS processes induced compressive-stress on the multi-crystal Si wafer's surface layer, with LAWS resulting in less surface residual stress compared to DWS. Additionally, while residual stress in DWS wafers transitioned from compression to tension with increasing etch depth, LAWS wafers exhibited a consistently compressive trend.

Given that current silicon-based Raman-stress relations ignore shear stress, a notable gap exists between actual and measured results. Therefore, it is imperative to investigate the real impact of neglecting the shear stress component in Raman-stress measurements of crystalline Si. Addressing this, Qiu et al.^[89] conducted experiments with (001) and (111) Si wafer samples, each shaped into 4 mm regular octagons and subjected to a uniaxial compressive force along the X-axis at a 45° angle from the X-axis (Figure 10a,b). Utilizing a micro-Raman laser instrument, they performed point-by-point surface scans along the X-axis and calculated the average Raman shift, comparing experimental values to theoretical predictions for each crystal plane based on the triaxial stress model, as shown in Figure 10c. Their results indicated that shear stress did not affect the Raman shift on the (001) plane, but significantly impacted the (111) plane, resulting in an error of $\approx 3.16\%$. This led to the quantitative determination of each stress tensor in crystalline Si through polarized Raman spectroscopy, dubbed as a "strain rosette", which incorporates the shear stress component,^[71] shown in Figure 10d. By adjusting the angles be-

tween the width direction of samples S_1 and S_2 , and the X-axis to 0° and 45°, respectively, they achieved varying stress states in the sample coordinate system under uniaxial compressive-stress (Figure 10e). The polarized Raman spectroscopy test results closely matched the model predictions, demonstrating the significant influence of shear stress on the Raman shift-stress coefficient, which varied with the polarization angle of the incoming light, as evidenced in Figure 10f.

6.1.2. Silicon Carbide (SiC)

As a prominent example of third-generation wide-bandgap semiconductors, SiC has garnered substantial interest from both industry and academia due to its remarkable electrical properties compared to conventional silicon-based semiconductors.^[90] The detection of residual stress in SiC is significantly important. Leveraging the silicon-based Raman-stress detection method, efforts have been made to adapt this technology for stress analysis in SiC crystals. For instance, Corro et al.^[91] investigated the 3D stress distribution state along the c-axis of 6H-SiC under stress of 3.7 GPa, revealing through Raman spectroscopy that stress decreases exponentially with depth. To further understand the Raman-stress relation in SiC crystals, Onda et al.^[92] conducted experiments to determine the relationship between stress in 4H-SiC and 6H-SiC crystals and the frequency shift in Raman spectroscopy. They employed a four-point bending test and performed Raman spectroscopy on single-crystal planes (size 5×5×45 mm), as shown in Figure 11a. The Raman spectrum of the 4H-SiC (0001) plane is shown in Figure 11b. Their analysis of the E_2 mode Raman shifts for both (0001) and (11-20) planes indicated that the Raman shifts decrease linearly with applied tensile stress, resulting in Raman shift-stress coefficients of

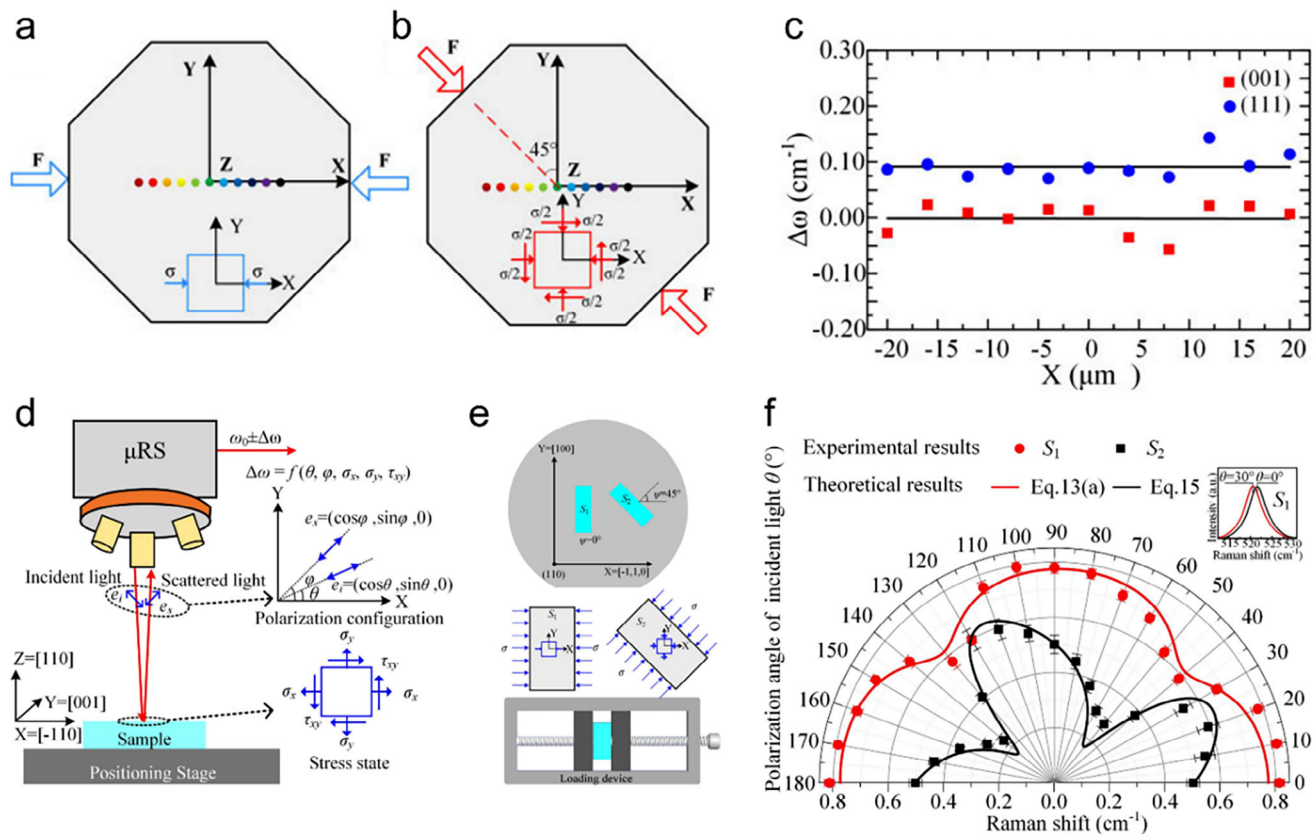


Figure 10. Schematic of stress loading experiment: a) uniaxial loading along X-axis and b) uniaxial loading along 45° direction. c) Measured experimental results. d) Schematic of Raman spectrum measuring plane stress state of Si surface with polarized device. e) Schematic of sample stress loading conditions. f) Comparison of experimental results of Raman wavenumber shift against theoretical prediction (θ from 0° to 180°) for S_1 and S_2 . (a-c) Reproduced with permission.^[89] Copyright 2018, IOP Publishing. (d-f) Reproduced with permission.^[71] Copyright 2019, The Optical Society.

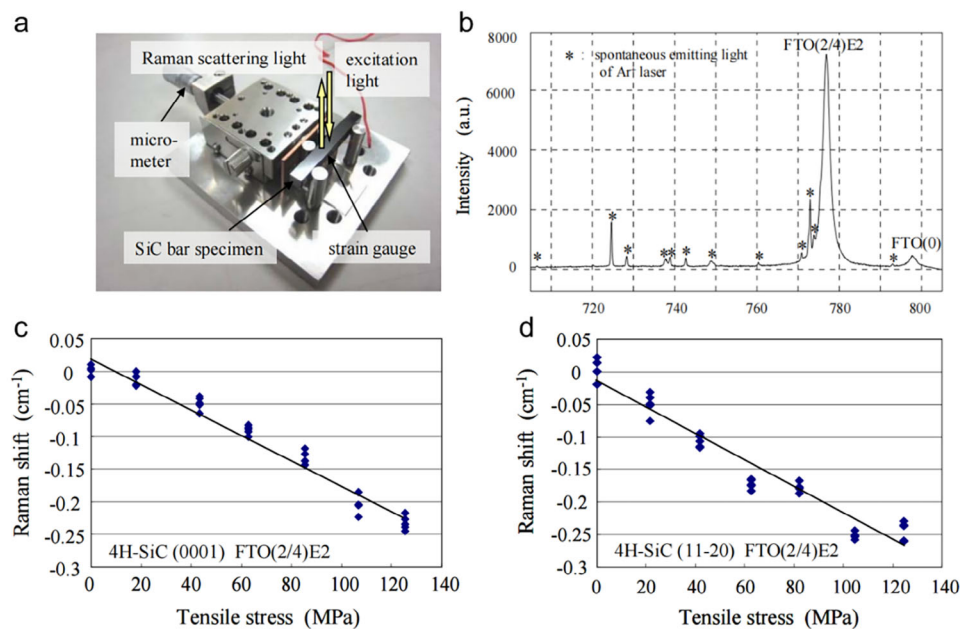


Figure 11. Experimental investigation of external stress loading of SiC and the Raman shift-stress relationship. a) Experimental setup for applying uniaxial stress to SiC square bar specimen under measuring Raman shift. b) Raman spectrum of 4H-SiC (0001) face. Correlation between tensile stress and Raman shift in 4H-SiC crystal: c) experimental results of 4H-SiC (0001) FTO(2/4)E $_2$ and d) experimental results of 4H-SiC (11-20) FTO(2/4)E $_2$. Reproduced with permission.^[92] Copyright 2014, Springer Nature.

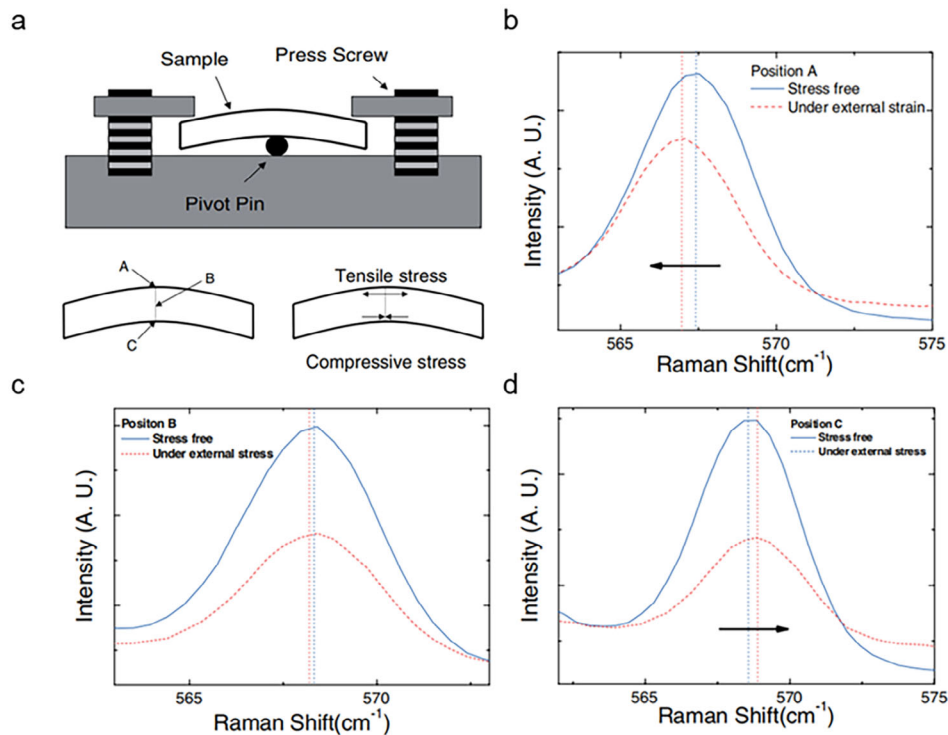


Figure 12. GaN Raman shift measurement under external stress and detection of stress distribution. a) Schematic of the device for applying external strain, position to be measured, and direction of stress during bending. Raman shift at Position b) A, c) B, and d) C with/without external strain. Reproduced with permission.^[98] Copyright 2006, John Wiley and Sons.

-1.96 and $-2.08 \text{ cm}^{-1} \text{ GPa}^{-1}$ for the (0001) and (11-20) planes, respectively (Figure 11c,d). Similarly, they found the Raman shift-stress coefficients for the (0001) and (11-20) planes of 6H-SiC crystals to be -2.70 and $-1.30 \text{ cm}^{-1} \text{ GPa}^{-1}$, respectively. This research lays a crucial groundwork for further advancements in Raman-based stress detection in SiC.

Addressing the influences of external damage on stress distribution, Tomar et al.^[93] investigated the impact of ion irradiation and corrosion on SiC stress distribution. Using nanoindentation to measure local mechanical properties, they established a finite element method (FEM) model to predict stress distribution in SiC materials. Zhang et al.^[94] conducted an extensive investigation into the influence of single-pulse femtosecond laser processing on the structural transformation and residual stress in 4H-SiC, including how laser flux affects residual stress.

6.1.3. Gallium Nitride (GaN)

GaN, recognized for its suitability as a substrate material for high-power, high-frequency, high-temperature, and optoelectronic semiconductor devices,^[95-97] owes its preference to its wide direct bandgap ($\approx 3.4 \text{ eV}$), high electron mobility, low impurity concentration, excellent electrical properties, exceptional thermal stability, and high conductivity. Consequently, measuring and analyzing residual stress in GaN materials has garnered increasing interest. In 2006, Kim et al.^[98] researched the Raman spectrum of bulk GaN material under external bending stress con-

ditions, as shown in Figure 12a. Their results tensile stress of $\approx 73 \text{ MPa}$ (red-shift, $\Delta\omega$ is -0.45 cm^{-1}) at the top of the bulk GaN template and compressive stress of $\approx 40 \text{ MPa}$, (blue-shift, $\Delta\omega$ is 0.25 cm^{-1}) at the bottom, as shown in Figure 12b-d. This investigation establishes a critical foundation for exploring the relationship between optical properties and internal stress in GaN materials.

The pursuit of low-stress GaN crystals is significantly driven by advancements in epitaxy or deposition techniques, emphasizing the need for an in-depth exploration of how growth conditions affect stress distribution. Lee et al.^[99] utilized gradient V/III ratio technology to grow high-quality bulk GaN with a thickness of 3.5 mm , as shown in Figure 13a. Raman spectroscopy analysis revealed that the peak position of the E_2 mode in the bulk GaN crystals decreased with a reduction in the V/III ratio, a trend observed across conditions I, II, and III (Figure 13b). Compared to unstressed GaN, all samples exhibited a red-shift, with residual tensile stresses of ≈ 0.70 , 0.37 , and 0.47 GPa , respectively.

Furthermore, the research on GaN processing properties is important. Nanoindentation is recognized as an effective method for exploring material processing properties at micro- and nano-scales. Leveraging this method, Qiu et al.^[35] explored the in-plane residual stress components around a Berkovich indentation on the (0001) plane of GaN using polarization Raman spectroscopy. Their experiments uncovered significant differences in stress component distribution compared to Raman shift distribution closely linked to GaN crystal structure and exhibiting a gradient along each crystal direction. The distribution of the E_2 mode

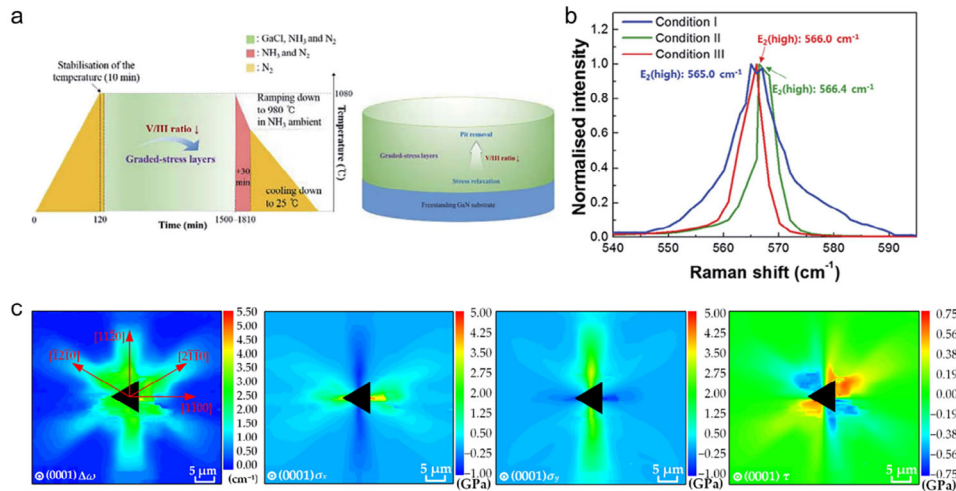


Figure 13. Detection of Raman shifts during growth and stress evolution in nanoindentation regions. a) Schematic of GaN growth process. b) Raman spectra of GaN grown with different V/III ratios. c) E_2 mode Raman shift map on (0001) plane, as well as distribution of normal stress σ_x , σ_y , and shear stress τ_{xy} . (a,b) Reproduced under terms of CC-BY 3.0 license.^[99] Copyright 2018, The Authors, published by The Royal Society of Chemistry. (c) Reproduced under terms of CC-BY 4.0 license.^[35] Copyright 2023, The Authors, published by MDPI.

Raman displacement on the (0001) plane around the indentation, as shown in Figure 13c, highlights an area of increased displacement near the indentation, indicating compressive-stress dominance in the residual stress field. The Raman shift increased along the, [2–1–10], [11–20], and [–12–10] directions at the center of the indentation, and decreased with distance from the center. The Raman spectrum decayed more slowly along the [2–1–10] and [–12–10] directions and more quickly along the [11–20] direction. Specifically, σ_x exhibited positive values along the, [2–1–10] [–12–10], and [1–100] directions, and negative values along the [11–20] direction, while σ_y was positive along the, [2–1–10] [–12–10], and [11–20] directions, and negative along the [1–100] direction. Near the indentation's edge, σ_x and σ_y primarily aligned with the [1–100] and [11–20] directions, respectively, whereas at the far end of the indentation, they predominantly aligned along the, [2–1–10], [11–20] and [–12–10] directions. Based on stress distribution characteristics, the 2 stress components were symmetric along the [1–100] and [11–20] directions, with the shear stress showing both positive and negative values.

6.1.4. Sapphire (Al_2O_3)

Sapphire (Al_2O_3) substrates offer numerous advantages, including high thermal conductivity, exceptional transparency, physical and chemical stability, excellent electrical insulation, and strong mechanical strength. These substrates are increasingly used in LEDs and other optoelectronic devices, highlighting their significance in the field.

The processing of Al_2O_3 poses challenges due to its high hardness and brittleness, with conventional techniques often leading to stress-related problems such as cracks, fractures, and surface damage. To address these issues, Mann et al.^[100] introduced Micro-Laser Assisted Machining (μ -LAM) as a solution to the significant stress issues encountered with traditional sapphire processing techniques, as shown in Figure 14a. Micro-Raman spec-

troscopy was utilized to quantify the residual stress induced by this technology. The stress distribution in sapphire subjected to μ -LAM technology, illustrated in Figure 14b, reveals a compressive-stress zone both within and along the edge of the cut, spanning ≈ 2 –3 μm in width with a uniform stress distribution. Moving away from the cut, the stress rapidly transitions from compressive to tensile stress with a 1–2 μm range, with the highest tensile stress located $\approx 5 \mu m$ from the center of the cut. This research indicated that laser-assisted processing can effectively mitigate residual stress, reducing the occurrence of fractures, cracks, and chipping in sapphire. The investigation into the residual stress on various sapphire crystal faces during processing has garnered significant interest due to the material's anisotropic nature. For instance, Min et al.^[101] employed micro-Raman spectroscopy to investigate the anisotropy of residual stress resulting from ultraprecision machining on 2 crystal planes of sapphire. Figure 14c illustrates the equivalent residual stress on R-plane, showing predominantly tensile stress except in the brittle region of the 90° cut, with stress magnitudes varying significantly with the cutting direction. For the 150° cutting direction, residual stress ranged from tens to 150 MPa, decreasing with increased cutting depth as stress was relieved through crack formation. Conversely, the 180° cutting direction showed the most significant residual stress in the brittle region. Figure 14d shows the equivalent residual stress on C-plane, which, unlike the R-plane, was negligible and uniform across various cutting directions. These results reveal that residual stress varies across different crystal planes and is influenced by the cutting direction during machining.

In summary, the precise measurement and understanding of internal stress in substrate materials represent a dynamic field requiring continuous advancements and refinements. These advancements are crucial for uncovering the origins of stress in semiconductor materials and optimizing material production processes, offering invaluable insights into the semiconductor industry.

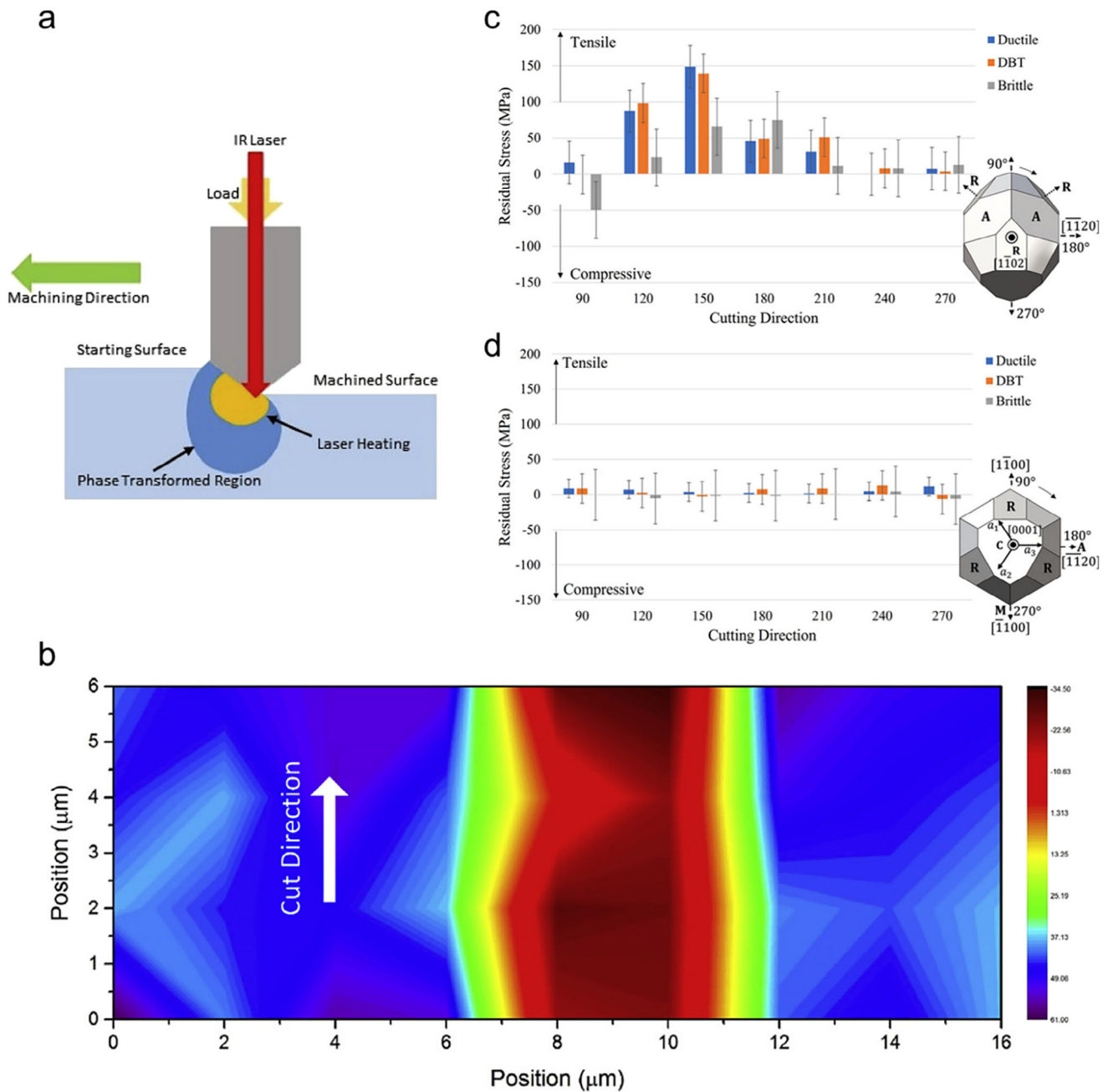


Figure 14. Machining process and Raman-stress measurement of sapphire. a) Schematic of μ -LAM process; b) Stress profile of sapphire processed using μ -LAM technology with thrust/applied force of 20 mN, low-power laser assistance, a diamond tip with a radius of 5 μm , and nominal cutting depth of ≈ 100 nm. The unit of stress is MPa. c) Residual stresses in various cutting directions on R-plane of the sapphire. d) Residual stresses in various cutting directions on C-plane of the sapphire. (a,b) Reproduced with permission.^[100] Copyright 2019, Elsevier. (c,d) Reproduced with permission.^[101] Copyright 2022, Elsevier.

6.2. Epitaxial Thin Film

With the rapid growth of the electronics industry, the demand for advanced semiconductor materials has significantly increased. Epitaxial semiconductor thin films, in particular, are pivotal for the development of high-performance devices. However, stress within these epitaxial films has become a major concern

due to its potential impact on material properties and device performance.^[102] The primary stress sources in these films include lattice mismatch between the epitaxial film and the substrate, differences in thermal expansion coefficients, and defect density.^[103–105] In response, there are ongoing efforts to optimize and develop innovative thin-film epitaxy processes aimed at minimizing the residual stress induced by these factors.

Techniques under exploration include migration-enhanced metal-organic chemical vapor deposition,^[106,107] growth mode alternating multilayer technology,^[108–110] selective chemical vapor deposition,^[111,112] and molecular beam epitaxy.^[113,114] To facilitate the production of high-quality epitaxial films with minimal residual stress, accurately characterizing these films' residual stress and understanding stress evolution during growth are crucial. Such investigation provides valuable insights into refining epitaxial film production processes.

6.2.1. Diamond Film

Diamond thin films have garnered significant interest due to their exceptional optical, electrical, and mechanical properties,^[115,116] making them highly promising for various applications in optical, photonic, and electronic devices.

Atomic doping of diamond films, a common modification method, can introduce lattice distortion and residual stress. Therefore, the impact of doping on stress distribution in diamond films is a focal point of research. For instance, Mermoux et al.^[117] investigated the stress profile of homoepitaxial (111)-oriented phosphorus-doped diamond films, observing that the epitaxial layer signal (1326 cm^{-1}) weakened with increasing focal depth, while the diamond substrate signal (1332 cm^{-1}) strengthened. The epitaxial film surface exhibited the highest tensile stress, which rapidly diminished to zero with depth before transitioning into compressive-stress, suggesting that the interface primarily experiences compressive-stress. Moreover, the size of the epitaxial diamond film on the substrate significantly influences the stress at the film's edge. Holtz et al.^[118] utilized ultraviolet (363.8 nm) micro-Raman stress mapping to examine the selective growth of diamond stripes on the Si substrates with different widths and an average thickness of $\approx 1.5\text{ }\mu\text{m}$ (Figure 15a–d). The internal stress distribution varied with stripe width (20, 40, 80 μm) are shown in Figure 15e,f, with the maximum shift (compressive-stress) concentrated near the center of each stripe and decreasing toward the edges. For Si substrates, lower tensile stresses were observed beneath the diamond, with increased compressive stresses at the fringe edges. The diamond's compressive-stress decreased as the stripe width narrowed.

Beyond investigating stress in epitaxially grown diamond films, understanding the impact of processing techniques on the stress distribution within these films is essential. Bendavid et al.^[119] proposed a novel approach to customize the stress properties of diamond films through boron doping and focused ion beam milling for microbridge microfabrication (Figure 16a,b). They studied the resultant stress distribution using detailed confocal micro-Raman spectroscopy. Figure 16c shows the numerical stress distribution ($\sigma_{xx} + \sigma_{yy}$) within microbridge 1 (MB1), revealing significant variation across different areas of MB1, with stress peaking at 1.74 GPa in the bridge (B) region and decreasing to $0.96 \pm 0.07\text{ GPa}$ in the left (PL) and right pads (PR), compared to an average stress of 1.1 GPa in nonpatterned areas. Similarly, Figure 16d presents the stress ($\sigma_{xx} + \sigma_{yy}$) distribution for microbridge 2 (MB2), where the absence of rectangular pads PL and PR notably influenced the ($\sigma_{xx} + \sigma_{yy}$) distribution. The average stress in the left triangular (TL) and right triangular (TR)

pad regions was slightly lower than in the nonpatterned region (1.70 GPa), while the stress of the B region was significantly reduced to $1.31 \pm 0.04\text{ GPa}$. These results demonstrate that stress in the boron-doped diamond/diamond layer could be modulated through appropriate microbridge geometry and boron doping.

6.2.2. Silicon Carbide (SiC) Film

SiC thin films are recognized for their excellent high-temperature stability, high electron mobility, low electron drift, and wide-bandgap,^[120] making them suitable for high-temperature, high-frequency, high-voltage, and high-power applications.

Research on stress distribution in SiC epitaxial films has primarily focused on the 3C-SiC phase. Lee et al.^[77] employed chemical vapor deposition (CVD) to selectively grow a 3C-SiC film with a low interface defect density on a Si (111) substrate, employing Raman spectroscopy to assess the residual stress field at the interface. The results showed a consistent presence of tensile stress between the epitaxial film and the silicon substrate. Pezzotti et al.^[121] explored the stress effects associated with different crystal plane orientations of silicon substrates on the epitaxy of 3C-SiC films through Raman spectroscopy, concluding that high-quality cubic silicon carbide single crystals could be obtained on silicon substrates irrespective of orientation. Moreover, the stress distribution in 3C-SiC thin films influenced by Si substrate surface defects has attracted research interest. Grudinkin et al.^[122] examined the effects of voids in Si substrates on stress distribution in 3C-SiC thin films during CVD growth. Their results confirmed that 3C-SiC films epitaxially grown on Si substrates predominantly exhibited tensile stress, aligning with prior findings. Additionally, stress levels outside the voids exceeded those inside, indicating that cavities contributed to stress relief.

Other studies have delved into how growth rate,^[123] epitaxial thickness,^[124] and doping^[123,125] affect the stress in 3C-SiC films.

6.2.3. Gallium Nitride (GaN) Film

GaN thin films, alongside SiC, stand as significant examples of third-generation semiconductors, attracting considerable research focus.^[126–129] Therefore, comprehensive wafer-wide epitaxial stress characterization has become pivotal in the study of stress in GaN thin films. Holtz et al.^[130] investigated the stress of GaN films epitaxially grown on diamond substrates utilizing Raman spectroscopy (Figure 17a,b). By employing both visible light and ultraviolet Raman spectroscopy, they were able to visually map the stress distribution across the entire wafer. Figure 17c,d illustrates that the epitaxial thin-film GaN generally exhibits tensile stress. However, there is a notable difference in stress distribution from the upper to the lower sides of the films, with a gradient distribution (ranging from 1.0 to 0.075 GPa) observed from the surface down to the bottom interface.

While nanoindentation is a relatively straightforward technique for studying the mechanical properties of bulk GaN,

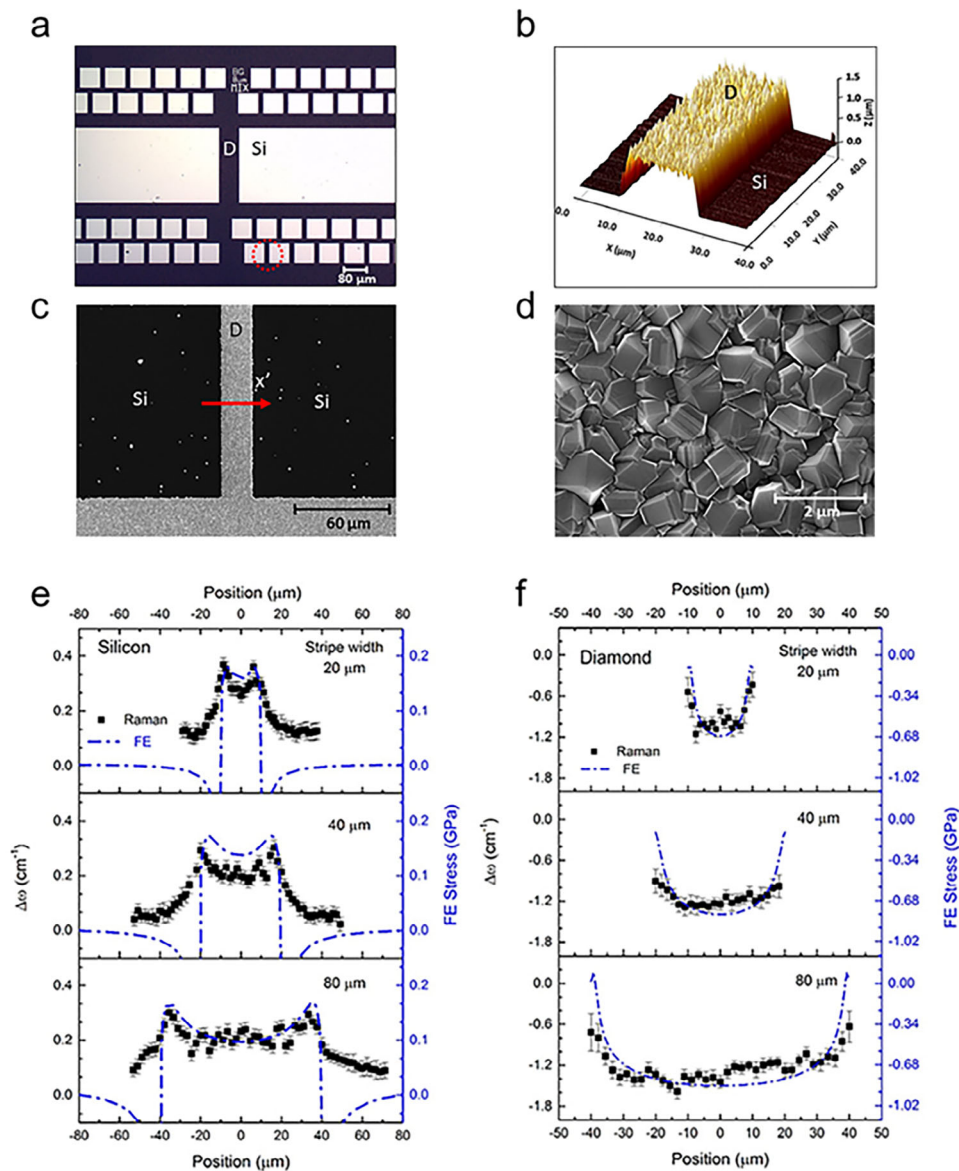


Figure 15. Research on stress distribution of epitaxial diamond films under different sizes. a) Optical image of the grown diamond. b) AFM image of 20 μm wide diamond stripe. c) SEM images of 20 μm wide diamond stripes and silicon substrates. d) High magnification SEM image of epitaxial diamond film. Comparison of Raman measured and theoretical stress maps for e) Si substrates and f) diamond. Stripe width: 20, 40 and 80 μm. Reproduced with permission.^[118] Copyright 2018, AIP Publishing.

applying it to nanofilms is challenging due to the significant influence of the substrate on indenter behavior, which complicates the isolation of signals directly from the film. Addressing this, Redkov et al.^[131] investigated the nanoindentation characteristics of epitaxial GaN films on SiC buffer layers atop Si substrates utilizing confocal Raman spectroscopy. They detailed the mechanical stress distribution and crystal quality (based on FWHM) of GaN and Si near the indenter at different indentation depths and investigated the anisotropy of elastic stress propagation during indentation. **Figure 18a** demonstrates that at a low pressure of 50 mN, the mechanical stress propagation showed significant anisotropy, influenced by the material's

crystallographic orientation and the relative orientation between the indenters. Beyond 200 mN, the stress propagation in the GaN film became radially symmetric, showing less dependence on a direction. At 500 mN, a clear separation between the film and substrate was observed in the indentation region, concentrating the original mechanical stress nearby before it sharply decreased to zero with distance. The minimal stress in the Si substrate was mainly due to pores introduced during the growth of the SiC buffer layer, which facilitated stress relaxation nearby. **Figure 18b** delves deeper into the mechanical stress propagation and crystal quality along the depth of the Si substrate. The results clearly illustrate that both stress distribution within the silicon

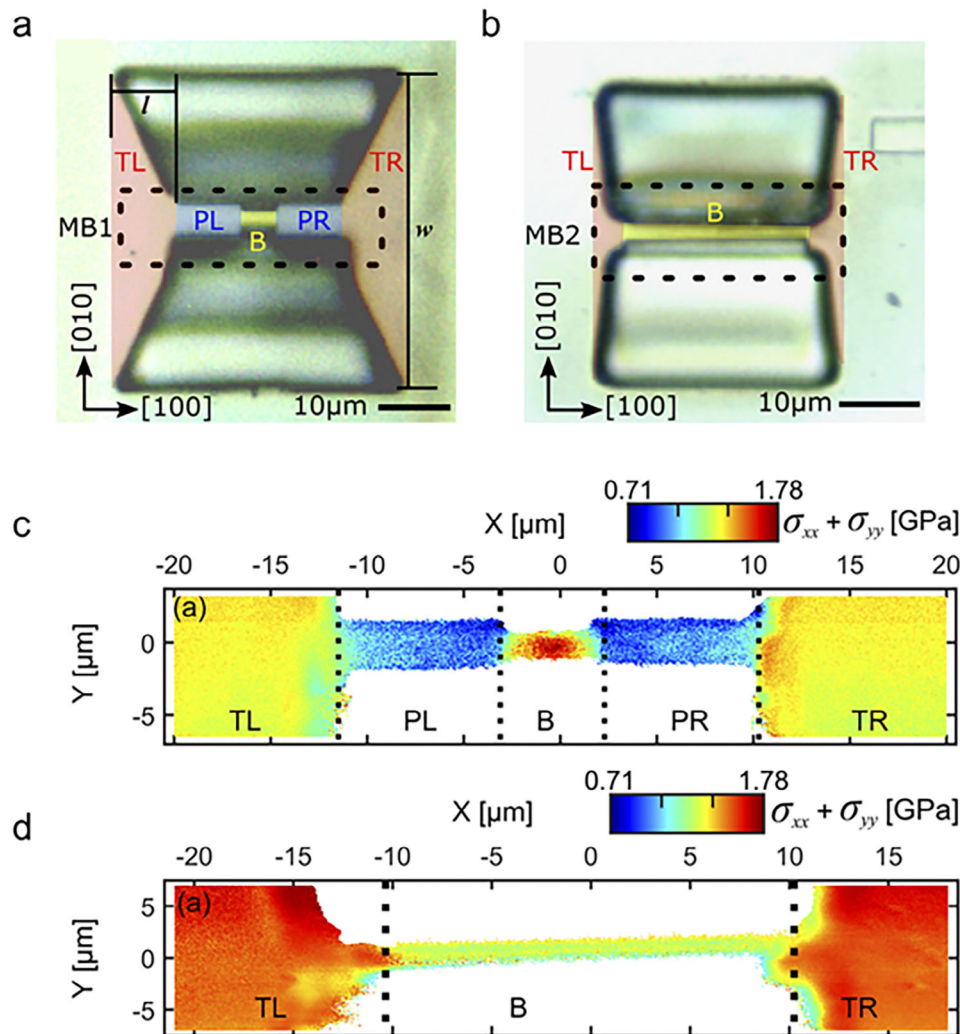


Figure 16. Stress distribution in microbridge-processed diamond films. Optical image of a) MB1 and b) MB2 structure. Raman-stress relation map of c) MB1 and d) MB2 structure ($\sigma_{xx} + \sigma_{yy}$). Reproduced under terms of CC-BY 4.0 license.^[119] Copyright 2021, The Authors, published by AIP Publishing.

substrate and its FWHM are significantly influenced by the depth.

6.2.4. Aluminum Nitride (AlN) Film

Aluminum nitride (AlN) thin films are highly valued for their biocompatibility, high thermal conductivity, and excellent dielectric properties, making them suitable for a variety of applications.^[132–136] Their thermal expansion coefficients closely align with those of silicon, potentially reducing the probability of thermal stress-induced failures in silicon-based devices. This has led to a surge in research on thin-film materials in recent years.

Research on the quality and stress distribution of AlN epitaxial films grown by different processes has become an important area of focus. Solonenko et al.^[137] investigated epitaxial hexagonal AlN films on Si (111) substrates prepared through magnetron sputtering, particularly examining the effects of annealing on crystal quality via Raman spectroscopy. Analysis of the an-

nealed AlN film (Figure 19a) indicated a shift in the Raman wavenumber to a lower frequency compared to unstressed AlN, indicating the presence of tensile stress with an estimated average stress of ≈ 0.6 GPa based on the Raman-stress factor value of $-6.3 \text{ cm}^{-1} \text{ GPa}^{-1}$ for the E_2 mode. The position of the AlN mode during annealing, as shown in Figure 19b, was closely related to annealing temperature, with stress release observed up to $800 \text{ }^\circ\text{C}$ and a transition to the characteristic frequency range of strain-free AlN crystals at higher temperatures, indicating a potential relationship change at the AlN-Si interface $\approx 800 \text{ }^\circ\text{C}$. Feng et al.^[138] grew AlN films on 6H-SiC and Al_2O_3 substrates using metal-organic CVD and employed Raman scattering to explore the film stress and temperature relationship. The Raman spectrum of AlN/ Al_2O_3 samples, excited at 532 nm (Figure 19c), showed a higher frequency shift in the E_2 mode, indicating compressive-stress, while AlN/6H-SiC samples exhibited a lower frequency shift, suggesting tensile stress. The observed shifts for AlN/ Al_2O_3 and AlN/6H-SiC samples under unstressed conditions were -0.5 and -9.28 cm^{-1} , respectively (Figure 19d),

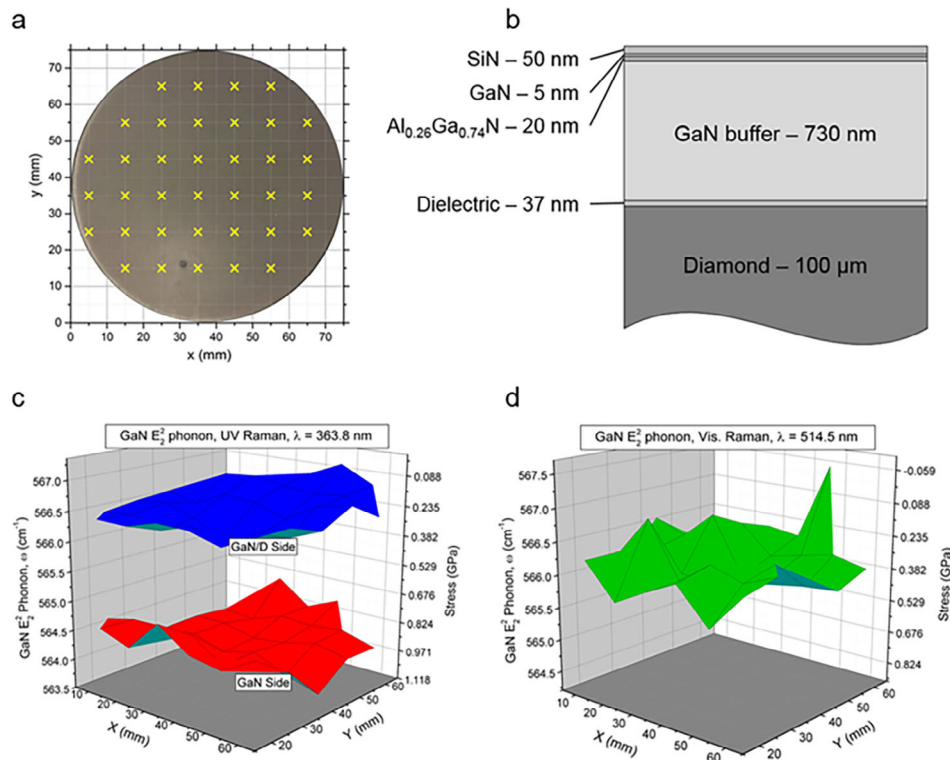


Figure 17. Growth stress full wafer inspection of epitaxial GaN films a) Coordinate system used for Raman-stress relation mapping in the wafer. b) Schematic of epitaxial layer structure. Raman measurement images of sample E_2 phonon mode: c) UV excitation and d) visible light excitation. Reproduced with permission.^[130] Copyright 2016, AIP Publishing.

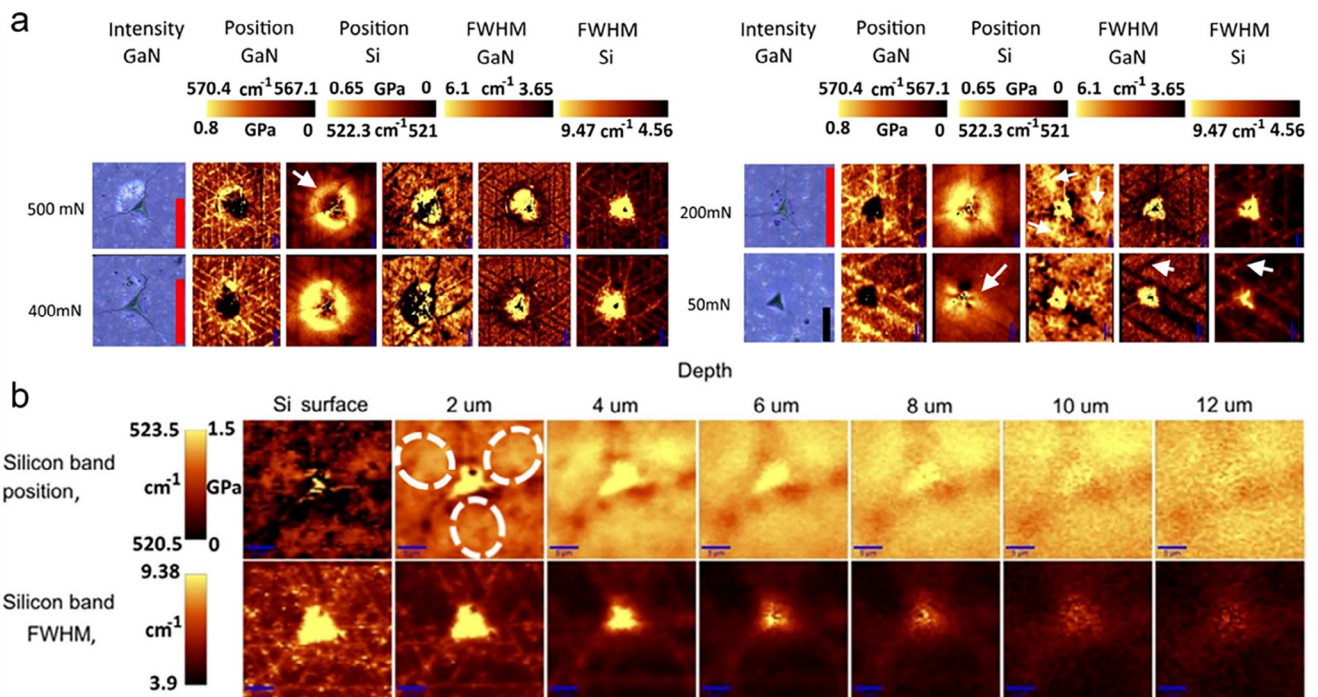


Figure 18. Stress distribution in nanoindentation region of epitaxial GaN films. a) Raman intensity, peak position, and FWHM test pattern of the Si substrate and GaN epitaxial layer under different pressures. b) Peak position and FWHM evolution with depth for Si substrate under 200 mN pressure. Reproduced with permission.^[131] Copyright 2017, Elsevier.

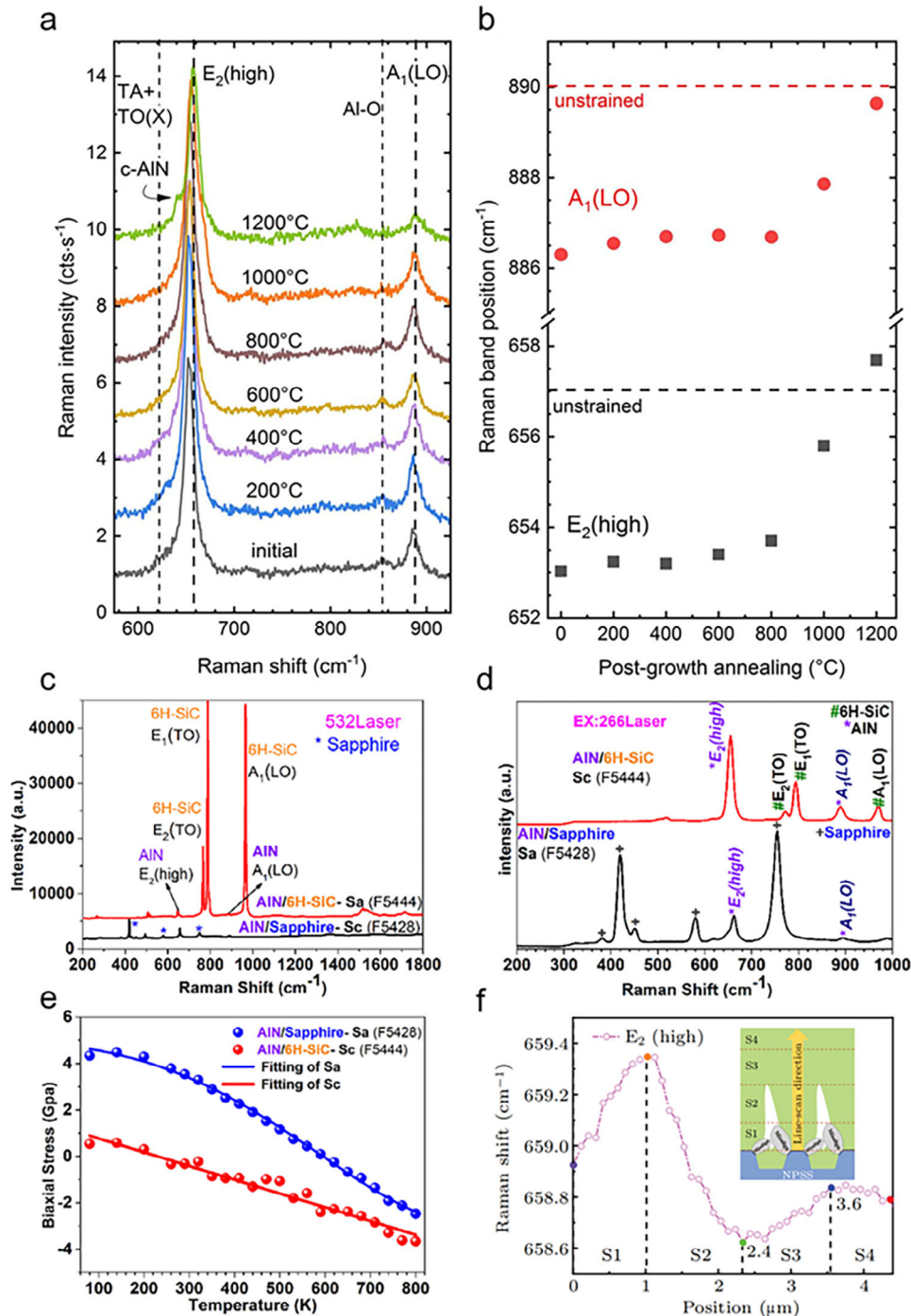


Figure 19. Stress-annealing temperature relationship of epitaxial AlN films and stress evolution during growth. a) Raman results of AlN/Si(111) samples after post-growth annealing. b) Relationship function between peak position of E₂ and A₁ phonon mode with post-growth annealing temperature. Raman spectra excited by c) 532 nm and d) 266 nm. e) Relationship function between stress in AlN film and temperature. f) The shift of the E₂ phonon mode peak along the growth direction. (a,b) Reproduced with permission.^[137] Copyright 2019, John Wiley and Sons. (c-e) Reproduced with permission.^[138] Copyright 2021, Elsevier. (f) Reproduced with permission.^[139] Copyright 2023, IOP Publishing.

attributed to thermal expansion coefficient mismatches between the films and substrates. The film stress change curve with temperature for both sample types revealed varying stress behaviors, with AlN/Al₂O₃ samples, transitioning from compressive to tensile stress within the 600–800 K range and continuing to increase with temperature, as shown in Figure 19e. While AlN/6H-SiC samples showed a decrease in compressive-stress within the 80–250 K range before transitioning to tensile stress. Subsequently, the compressive-stress in the AlN film transitioned to tensile stress, which gradually increased as the temperature rose. This comparison revealed that the temperature at which the stress undergoes an abrupt change for the Al₂O₃ substrate (600 K) is higher than that for the 6H-SiC substrate (250 K), a discrepancy due to the differences in thermal expansion coefficients among the 3 materials. Xu et al.^[139] utilized hydride vapor phase epitaxy to grow a crack-free AlN film on a 4-inch hole-shaped nanopatterned sapphire substrate, investigating the stress evolution law with film thickness through cross-sectional Raman line scanning (Figure 19f). The Raman frequency of the E₂ mode varied significantly along the growth direction, indicating overall compressive-stress within the film as a whole (stress-free peak position: 657.4 cm⁻¹). These fluctuations were divided into 4 stages (S1–S4), with the peak compressive-stress near the interface at a depth of 1 μm in the S1 stage, attributed to the thermal expansion coefficient mismatch between AlN and sapphire and early growth stage competitions. The frequency decreased gradually with layer coalescence in the S2 stage, and a slight increase in stress was noted in the S3 stage, likely due to misorientation in regions adjacent to the coalescence region.

In summary, stress in semiconductor epitaxial films plays a dual role in device performance. Strategic stress engineering can enhance electronic device performance by fine-tuning epitaxial growth conditions to optimize carrier mobility, bandgap energy, and device functionality for specific applications. Conversely, excessive stress in epitaxial films can detract from device performance and reliability, leading to issues such as dislocations, cracks, reduced carrier mobility, increased leakage current, and diminished device lifespan. Therefore, identifying and addressing stress-induced defects is crucial for the long-term performance and reliability of semiconductor devices.

6.3. Patterning

Semiconductor patterning techniques play an important role in modern electronics, directly affecting material properties and device performance, with stress being a crucial factor affected by patterning. Understanding the impact of patterning on stress is essential for optimizing device design and reliability.

Recognizing that accumulated residual stress can lead to crack formation, Hossain et al.^[140] utilized Raman spectroscopy to examine the stress distribution in GaN thin films on patterned Si substrates with various mesa sizes, trench heights, and trench widths. They conducted linear Raman scans along the long diagonal axis of the rhomboid mesa (400 μm) and utilized the biaxial plane stress model and E₂ mode to calculate the GaN stress variation (Figure 20a). The findings revealed a symmetric distribution of tensile stress, peaking at the rhombus's center (1.8 GPa) and exhibiting a consistent U-shaped stress distribution trend across

different mesa sizes (Figure 20b). Moreover, the maximum center stress increased with mesa size. In the (110) crystal plane of the patterned 200 μm mesa Si substrate, Figure 20c demonstrated the E₂ peak shift of GaN at different groove heights (5, 10, 15 μm) suggesting that maintaining the trench height above the GaN thickness (12 μm) significantly reduced residual stress, while the groove width minimally impacted it (Figure 20d). However, the study faced statistical limitations due to the randomness in crack generation. Therefore, further research based on statistics in subsequent studies was conducted.^[141] The results (Figure 20e) revealed that the percentage of fractured mesas increased with mesa size, with in-plane stress nearing 1.8 GPa for mesas larger than 100 μm (Figure 20f). Variations in in-plane stress for 400 μm mesas were linked to differences in the growth at film edges or depths. Additionally, deeper trench heights (15 μm) resulted in reduced mesa stress compared to 5 and 10 μm heights, demonstrating significant stress alleviation with increased trench height. Conversely, variations in trench widths (Figure 20g,h) did not significantly affect the percentage of fractured mesas or in-plane stress, indicating that cracks and in-plane stress are nearly independent of trench width.

Beyond this focused patterned stress research, Griffin et al.^[142] explored local stress changes in AlN after patterned metallization using a planar biaxial stress model (Figure 21a). The transition region from plasma-enhanced tetraethylorthosilicate (PETEOS) to the bottom metal exhibited a notable tensile shift in AlN residual stress compared to the stress on the PETEOS/Si substrate (Figure 21b,c), indicating an inhomogeneous stress distribution within the AlN/metal/PETEOS/Si wafer (Figure 21d).

Semiconductor patterning technology is pivotal in contemporary electronic device fabrication, yet the residual stress it induces significantly influences device performance and reliability. While current research in this domain has made notable outcomes, continued in-depth exploration is imperative to drive semiconductor patterning technology forward, leading to substantial advancements in device design and reliability enhancement.

6.4. Interconnections in Packaging

The surge in demand for portable and wireless electronic devices has propelled the need for high-density integrated-circuit (IC) technology, spurring the development of new interconnect technologies. As we strive for higher integration levels, understanding the impact of interconnection techniques on the residual internal stresses of semiconductor devices becomes crucial. Various interconnection technologies, including wire bonding, flip chips, and through-silicon vias (TSVs), introduce varying residual stress distributions in these devices. Therefore, investigating the residual stress resulting from actual interconnection processes, considering changes in stress/strain layers, and utilizing stress characterization techniques to assess their potential effects on the long-term performance of microelectronic devices are critical steps.

6.4.1. Die Attach

The adhesive fixation between the chip and substrate is a crucial step in establishing electrical and mechanical connections,

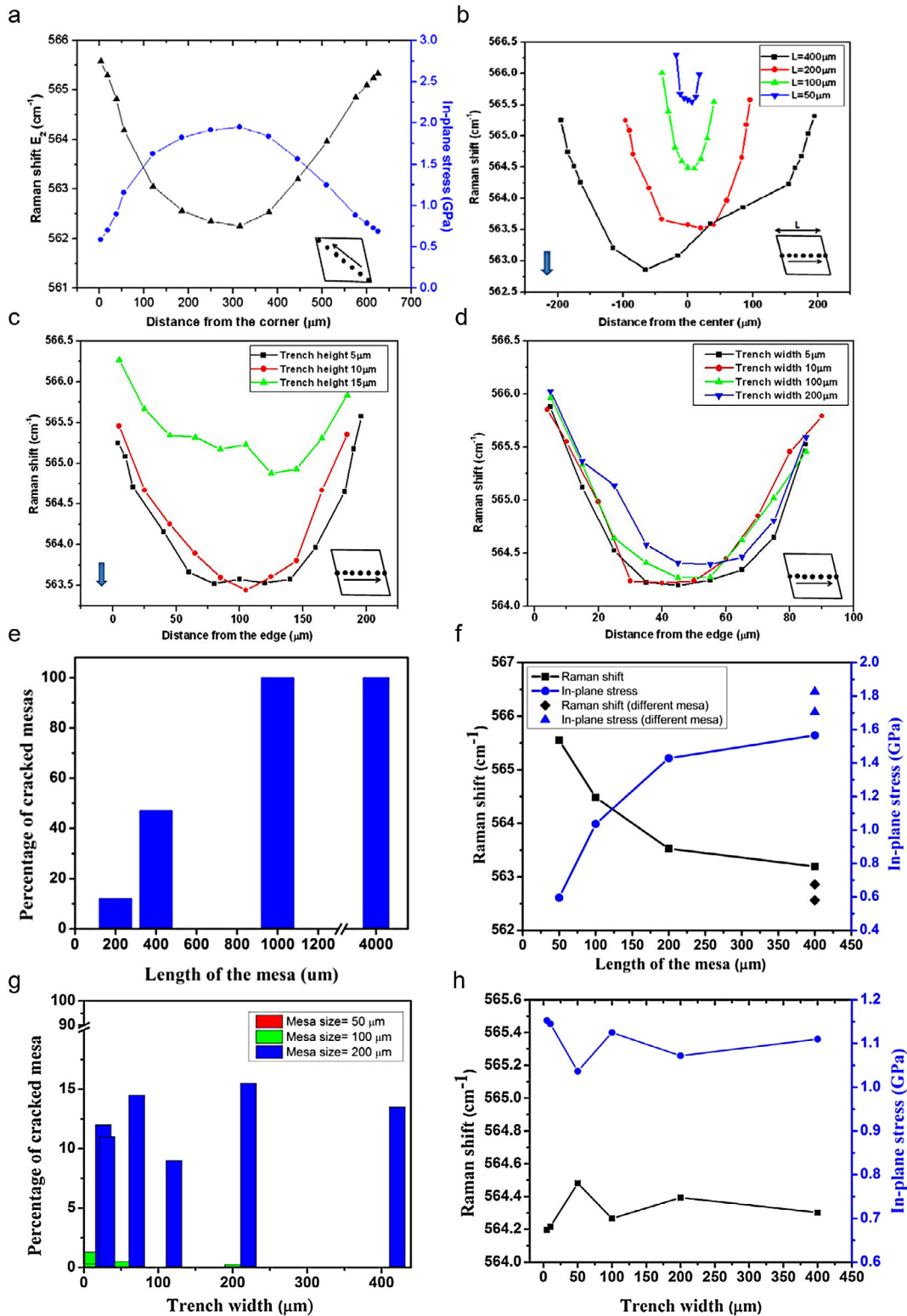


Figure 20. Effect of the surface patterning process for internal stresses in GaN. a) Raman shift and in-plane stress distribution of long diagonal axis of rhombohedral GaN (400 μm) grown on patterned Si (110). b) Raman shifts of E_2 phonon modes of rhombohedral GaN with different sizes. Raman shifts of E_2 phonon modes of rhombic GaN mesas (200 μm) at different c) trench heights and d) trench widths. e) Percentage of ruptured mesas. f) Raman shift and in-plane stress plots of crack-free mesa with different sizes. g) Percentage of fractured mesas of different sizes at different trench widths. h) Raman shift and in-plane stress of mesa (100 μm) with different trench widths. (a-d) Reproduced with permission.^[140] Copyright 2012, John Wiley and Sons. (e-h) Reproduced with permission.^[141] Copyright 2018, John Wiley and Sons.

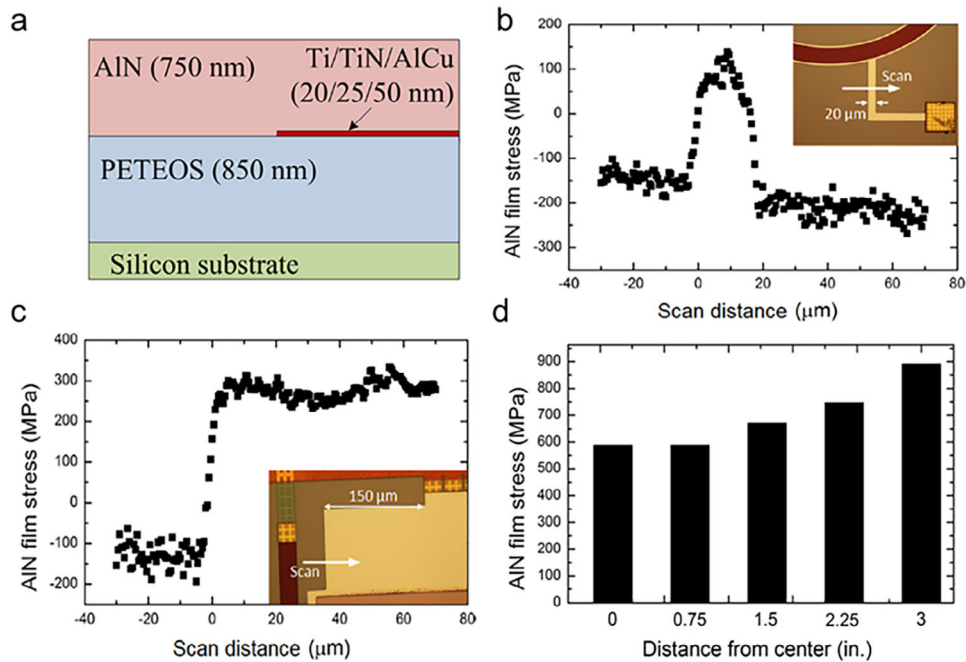


Figure 21. Stress distribution of AlN/metal/PETEOS/Si. a) Schematic of cross-section. Raman-stress relation map of AlN deposited on patterned bottom metal. b) trace and c) rectangular pad. d) Stress distribution on 6 in-AlN/metal/PETEOS/Si sample. Reproduced with permission.^[142] Copyright 2016, Institute of Physics Publishing.

significantly contributing to chip protection. Stress analysis during the chip-bonding process is paramount for producing electronic devices. For example, Uchida et al.^[143] utilized Raman spectroscopy to evaluate the stress within a silicon-based chip mounted on a copper plate (Figure 22a). Their findings, compared with FEM calculations, as depicted in the model in Figure 22b, showed a remarkable alignment between the σ_{11}' values from FEM and Raman measurements. The stress discrepancies at each point were minimal, consistently below 10 MPa, with σ_{11}' closely mirroring σ_{22}' , and σ_{33}' being significantly less than σ_{11}' and σ_{22}' . These findings suggest an isotropic biaxial stress on the (001) plane, as shown in Figure 22c. Integrating these analytical techniques provides valuable insights into accurately determining stress in electronic devices, enhancing their precision and reliability. During the die bonding process, the solder type significantly impacts chip-bonding strength. Drawing on prior research, Conti et al.^[144] investigated the effect of 3 solder types (AuSn soldering, silver sintering, and copper sintering) on stress distribution during Si chip-bonding to copper substrates. Through 2D Raman scanning of a square Si chip, they observed distinct Raman signal distributions among the solder types. AuSn soldering exhibited an even distribution pattern, exhibiting reverse central symmetry, while copper sintering revealed a symmetry axis on the Si surface, slightly tilted along the diagonal direction of the chip. In contrast, the Si Raman signal's position change in silver sintering remained largely uniform, displaying geometric symmetry.

Silicon-based solar cells also face stress-related challenges during processes such as wafer joining. Hsiao et al.^[145] proposed an innovative approach to reduce the thermomechanical stress

induced in Si solar cells during the interconnection process (Figure 22d,e), verified through residual stress measurements using Raman spectroscopy. Their approach showed that interconnected Si solar cells underwent reduced tensile stress when using the balanced contact method (Figure 22f,g).

6.4.2. Through Silicon Vias (TSV)

In recent years, TSVs have emerged as critical components in achieving the functionality of 3D high-density ICs. This technology reduces wiring length, interconnection delay, and power consumption, facilitating the creation of high-density stacked ICs and enhancing device performance.^[146] However, thermomechanical reliability remains a significant challenge due to stress caused by the mismatch in coefficients of thermal expansion between copper, dielectric materials, and semiconductor substrates. This mismatch can lead to reliability issues, including cracks, delamination, and voids,^[147] making the precise characterization of stress in vias critically important.

Kwon et al.^[148] explored the stress evolution in Si surrounding a thermally annealed copper-filled TSV structure through micro-Raman spectroscopy. Their findings reveal that the evolution of Si stress post-annealing is closely tied to the initial stress state, as well as the geometry and orientation of the TSV array. Figure 23a illustrates the Raman shift and stress in TSVs with a diameter of 10 μm and a pitch of 20 μm , both before and after the annealing process. Before annealing, compressive-stress was observed across the entire Si area with the outer region around the TSV copper structure experiencing higher stress than the

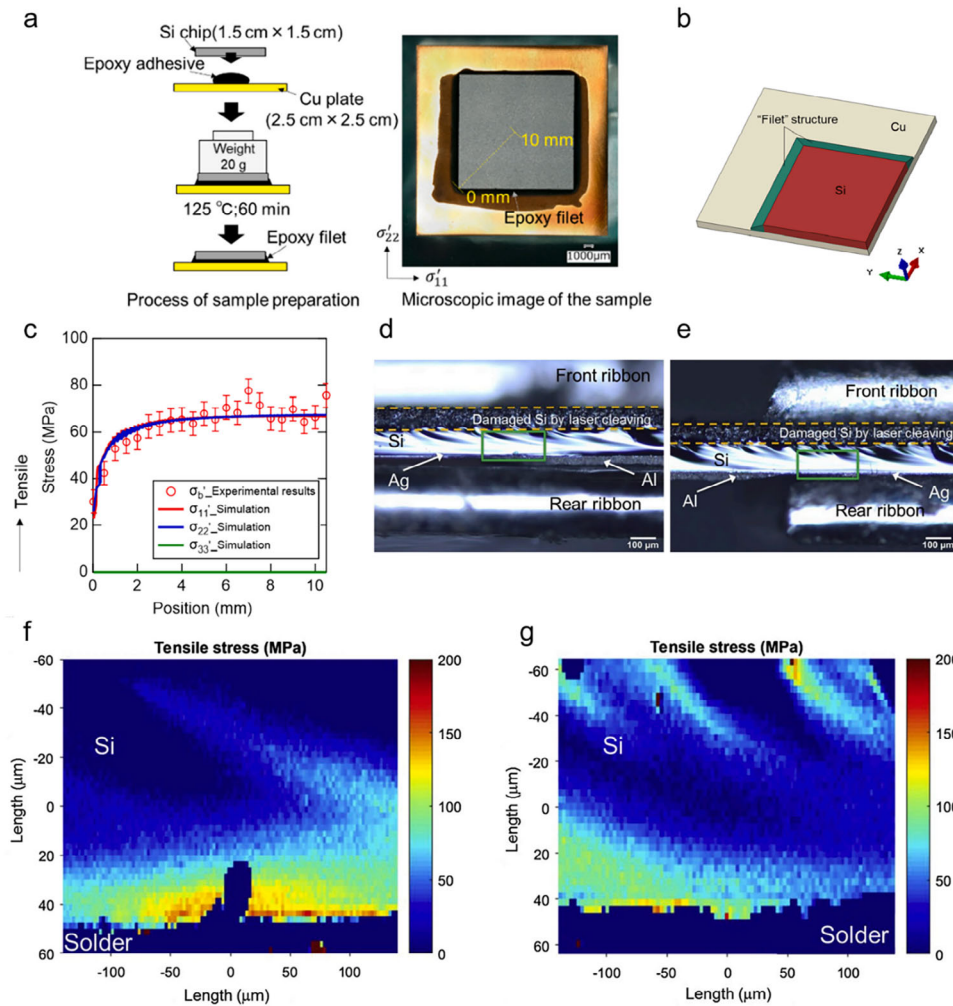


Figure 22. Stress distribution is caused by the die attach process. a) Schematic of the sample preparation process and optical micrograph of the sample. Stress measurements are taken along a yellow dashed line. (b) FEM for Si (001) crystal plane stress simulation. (c) Comparison of stress results between Raman measurement and FEM. Cross-sections of solar cells d) without and e) with balanced contact connections. Raman-stress relation mapping of cross-sections of solar cells f) without and g) with balanced contacts. (a-c) Reproduced with permission.^[143] Copyright 2021, Elsevier. (d-g) Reproduced with permission.^[145] Copyright 2020, Elsevier.

area between TSVs. Post-annealing, a significant increase in compressive-stress near the TSVs and the emergence of tensile stress between diagonal TSVs were noted, showing a four-fold rotational symmetry. Figure 23b, displays the Raman shifts of TSVs with a diameter of 5 μm and a pitch of 10 μm , both before and after thermal annealing. Initially, the stress distribution resembled that of the TSV array with a diameter of 10 μm . However, post-annealing, the Raman shift and stress distribution showed a notable dependence on the excitation wavelength. After annealing, the Si's stress distribution experienced significant changes, as shown in Figure 23c,d. Especially noteworthy is the larger offset observed in the TSV array with wider spacing, attributed to the differing expansion coefficients of Cu and Si during annealing, which led to compressive-stress around the TSV edges. Interestingly, between the 10-micron diameter TSVs, a mix of compressive and tensile stresses was observed, unlike in the

5 μm diameter TSVs. Building on this research, Bayat et al.^[149] specifically investigated the effect of annealing temperature on Cu-TSVs. Figure 24a illustrates the distribution of Raman frequency shifts of Si around cylindrical copper-filled TSVs before annealing, assessing how annealing at 380 $^{\circ}\text{C}$ for 1 h influenced the reduction of Si's residual stress. The stress in the sample prior to annealing was found to be greater than post-annealing, with a notable decrease in stress change near the Cu/Si interface after annealing. The effect of stress reduction was more pronounced at higher annealing temperatures (Figure 24b).

These findings underscore the significant impact of interconnections in semiconductor packaging on device stress levels. By carefully selecting interconnect technologies, and materials, and incorporating design considerations, it is possible to mitigate stress and improve the performance, reliability, and lifespan of semiconductor devices.

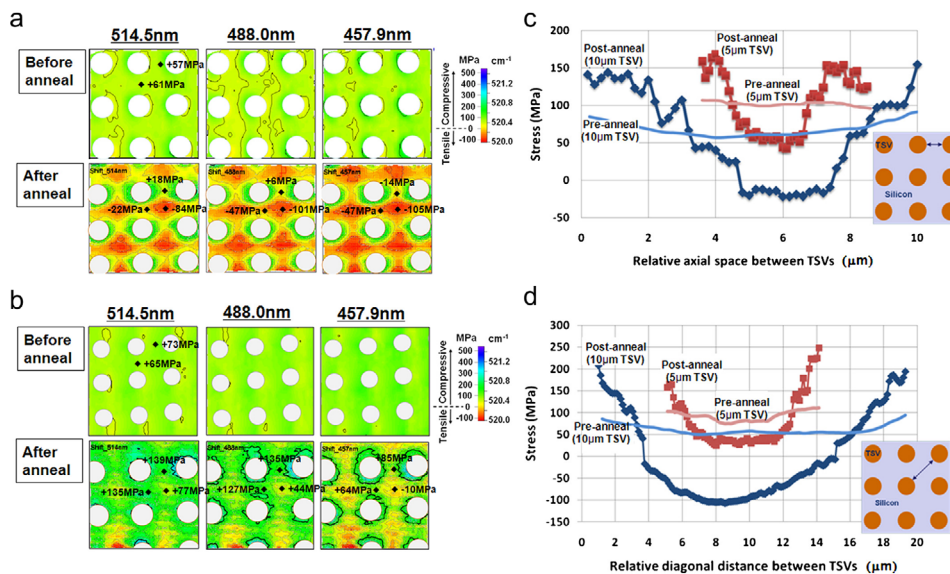


Figure 23. Raman shift and stress evolution diagrams of TSVs arrays before and after annealing. Diameters of TSVs arrays are a) 10 μm and b) 5 μm . Raman laser wavelengths are 514.5, 488.0, and 457.9 nm. Cross-sectional stress variation plots in region between TSVs centers before and after annealing, along c) axial and d) diagonal directions. Reproduced with permission.^[148] Copyright 2011, AIP Publishing.

6.5. Flexible Electronics

As electronic device applications continue to evolve and expand, the flexibility of these devices has emerged as a crucial factor for their application in various fields, such as flexible biomedical chips, smart textiles, and flexible displays. However, the enhanced flexibility introduces significant stress challenges during bending and deformation, making stress analysis an essential aspect of their structural design. This analysis is critical in determining the structural integrity and performance of the internal electronic components.

Given the prevalent use of conventional silicon-based devices in the electronics industry, there has been a push toward developing new flexible silicon-based electronic devices. Colinge et al.^[150] have made strides in this direction by successfully transferring thin monocrystalline Si films onto flexible and transparent polymer substrates using polymers as adhesives (Figure 25a). They employed ultraviolet (UV) micro-Raman spectroscopy to study the residual stress in the transferred substrates. During their analysis, Raman spectra were collected using excitation photons with a wavelength of 514.5 nm, comparing the Raman spectra of bulk and thin-film Si postchemical mechanical polishing and wet etching, respectively (Figure 25b). The observed Raman shifts in the thin Si samples were minor (0.20 and 0.41 cm^{-1} respectively) compared to bulk Si reference values, with calculated stresses of -32 and -65 MPa for the respective samples. Although this research represents a significant advancement in flexible Si thin-film technology, the internal stress distribution within such flexible silicon-based devices remains largely unexplored. Burghartz et al.^[151] investigated the uniaxial stress induced by bending ultrathin single-crystal Si chips through Raman spectroscopy. Initial measurements on a flat Si chip (Figure 25c) revealed shifts in the Raman frequency indicating both compressive and tensile stress. Further measurements on the same sample bent over a cylinder with a 10 mm radius showed all measured peaks

shifting toward lower frequencies, suggesting dominant tensile stress under bending (Figure 25d). Figure 25e clearly illustrates this uneven stress distribution, decreasing from the center along the bending direction to the edge of the chip and then increasing toward the other edges, underscores the complexity of stress management in ultrathin silicon-based flexible devices.

The emergence of new 2D materials, such as graphene,^[152–158] black phosphorus,^[159–162] and 2D metal chalcogenides,^[162–168] has significantly advanced the development of flexible electronic devices.^[169–171] These materials hold great promise for applications in wearable devices, photodetectors, pressure sensors, and beyond. Alongside the advancement of these new devices, extensive research focuses on the impacts of bending stress on actual device performance.^[172–175]

The rapid development of novel 2D materials has propelled flexible electronic devices into a new era of progress across various applications. However, to unlock their full potential and secure high performance in these devices, accurate characterization of stress distribution at the microscopic level becomes crucial. Additionally, addressing the issue of fatigue caused by stress during the bending of Si films stands as a critical area for future exploration. By delving into the internal stress dynamics, we can enhance our understanding of device performance within flexible electronics and identify strategies to optimize device functionality.

7. Summary and Outlook

7.1. Summary

This review presents a comprehensive overview from theory to application, including Raman measurement principles, the basic theory of Raman-stress relations, principles for selecting Raman excitation light, and the primary fields of application

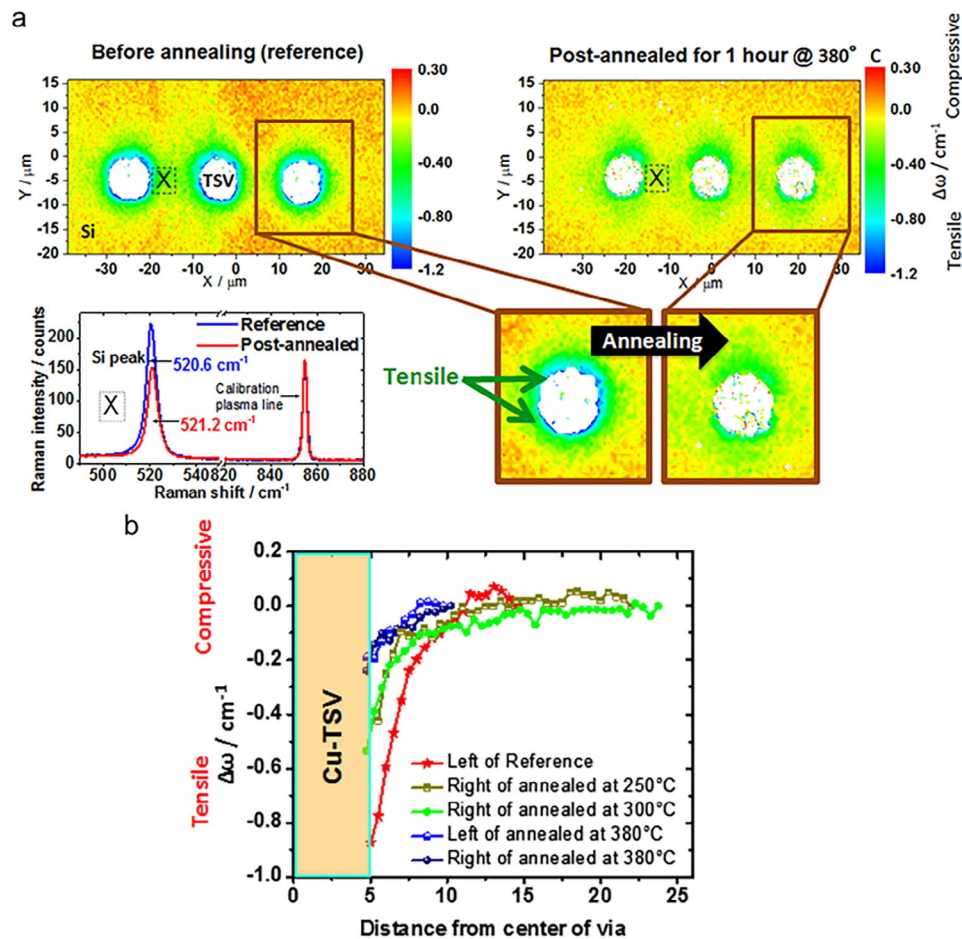


Figure 24. Effect of annealing temperature on Raman shift and stress evolution of TSV. a) Raman frequency shift map of Cu-TSVs population on the Si surface. The left picture is the sample before annealing, and the right picture is the sample after annealing. The spectrum in the lower left corner corresponds to the “X” point. b) Comparison of Raman shifts near TSVs at different annealing temperatures. Reproduced with permission.^[149] Copyright 2015, Elsevier.

today. These components are interrelated and collectively influence the accuracy of data in actual testing. Among these, the Raman-stress relations serve as the theoretical foundation for Raman micro-scale mechanics, while the selection of Raman measurement parameters is crucial in actual experimental research. Presently, Raman technology introduces new research directions and testing methodologies for exploring the micro-scale mechanics of electronic devices, aiding in the development of precise stress prediction models and providing insights into the stress behaviors across different crystal planes and within new semiconductor materials. As Raman technology progresses, it has become a key tool for detecting residual stress and exploring the reliability of microelectronic devices. Nonetheless, it is imperative to acknowledge its inherent limitations, notably its constrained depth detection capabilities, typically restricted to surface-level information acquisition. Furthermore, given its reliance on the light scattering mechanism, Raman technology necessitates sample uniformity for optimal performance.

7.2. Outlook

Looking forward, Raman technology holds promise and excitement in the realm of micro-scale mechanics for its natural advantages. For future endeavors in Raman-stress detection, the following recommendations are proposed:

- 1) Refinement and expansion of Raman-stress relation models: Raman-stress relation models facilitate quick stress detection. However, to address the intricacies of real-world stress conditions and enhance detection accuracy, optimizing and expanding these models is paramount.
- 2) Detection of stress evolution in the operating environment of microelectronic devices: Considering that internal residual stress in these devices may rise during actual operation, employing in-situ/ex-situ Raman spectroscopy to examine how stress evolution correlates with electrical performance under genuine operating conditions will be a pivotal area of subsequent research. The investigation will enhance the application

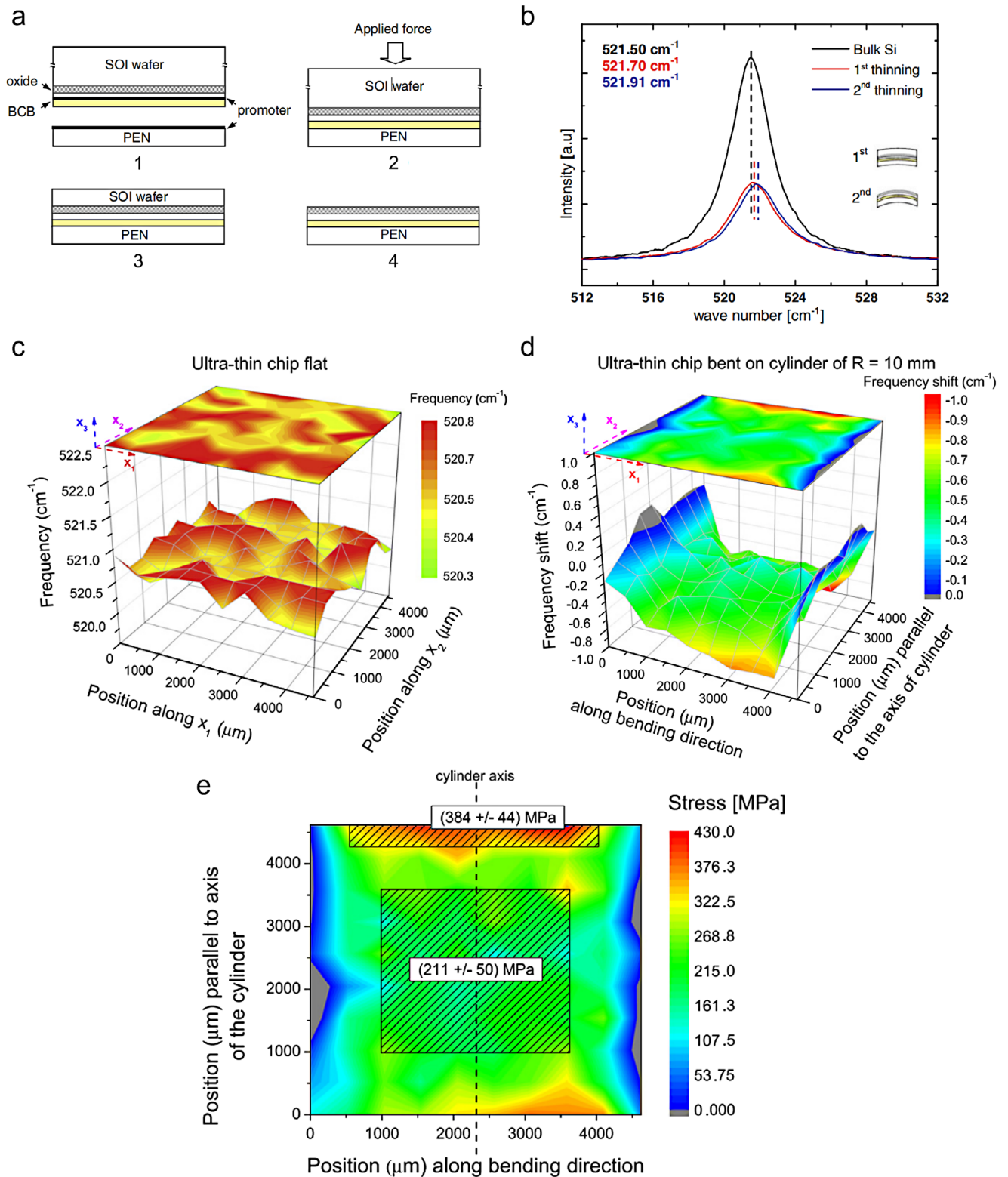


Figure 25. Stress distribution of silicon-based flexible devices. a) Schematic of transfer process of single-crystalline Si to flexible substrates. b) Raman spectroscopy of transferred Si sample based on 514.5 nm Laser. c) Raman spectroscopy of Si chip surface under flat conditions for flexible device. Raman-position d) and stress-position e) mapping of Si surface after bending an ultrathin Si wafer on cylinder. (a,b) Reproduced with permission.^[150] Copyright 2010, Springer Nature. (c-e) Reproduced with permission.^[151] Copyright 2014, Springer Nature.

and development of Raman spectroscopy in microelectronic stress detection.

- 3) Integration with advanced packaging and structural design: The progression toward highly integrated, densely packed electronic devices necessitates innovations in structural designs and advanced packaging techniques. Understanding the influence of these new designs and processes on residual stress is important. Raman spectroscopy offers an effective evaluation of the feasibility of these innovations with stress considerations.
- 4) Flexible electronic stress research: Unlike traditional microelectronic devices, the electrical properties of flexible electronics are significantly influenced by the intricate internal stress distribution resulting from various bending and folding states. Investigating the relationship between stress patterns and the real-world performance of flexible electronic devices through Raman spectroscopy is pivotal in promoting flexible electronic technology for practical applications.

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Conflict of Interest

The authors declare no conflict of interest.

Keywords

microelectronic manufacturing, micro-scale mechanics, Raman-stress relation, residual stress

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