Multi-Anode Sawtooth SDD for X-Ray Spectroscopy Fabricated on NTD Wafers

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*Abstract—***We are developing a multi-anode sawtooth silicon** drift detector (MSSDD) with an anode pitch of $250 \mu m$ for **one-dimensional position-sensitive detection of low-energy X-rays down to 200 eV. The detector is intended to be used in X-ray diffraction analysis. In this paper, we present new results of X-ray spectroscopy measurements with detectors fabricated on neutron transmutation doped (NTD) wafers with a thickness of 290** μ **m.** Using an MSSDD with an anode pitch of 250 μ m and having p⁺ **strips on both sides, we have measured an energy resolution of 191-eV full-width half-maximum (FWHM) per anode pixel for the 5.89 keV line of** ⁵⁵**Fe at 213 K. At room temperature the energy resolution is 375 eV FWHM. Split events are almost completely** eliminated due to the sawtooth-shaped p^+ strips.

*Index Terms—***Charge sharing, multi-anode linear drift detectors, sawtooth design, X-ray spectroscopy.**

I. INTRODUCTION

ONE-DIMENSIONAL position-sensitive spectroscopy of low-energy X-rays in applications such as X-ray diffraction analysis can be realized by a multi-anode linear silicon drift detector (MLSDD). The position information is obtained only from the $n⁺$ anode pixel signals. The necessity to detect low-energy X-rays (down to 200 eV) sets strict limits for the level of electronic noise, which should not exceed 10 rms electrons. Broadening of the electron cloud during its drift toward the anode pixels is a major problem that one has to face when developing an MLSDD. The resulting split events (only part of the signal electrons is collected at one anode pixel) will decrease the achievable energy resolution. This cannot be tolerated for low-energy X-ray detection. We have solved this problem by a nonconventional detector design with sawtooth-shaped p strips, referred to as a multi-anode sawtooth silicon drift detector (MSSDD). The sawtooth strips induce potential gutters, forcing electrons to drift towards a single anode pixel without additional lateral broadening [1].

Recently [2], we reported an energy resolution of 450 eV full-width half-maximum (FWHM) for the 5.89 keV line of 55 Fe at 233 K with an MSSDD having an anode pitch of 500 μ m. The energy resolution was dominated by the heating of our detector causing a continuous increase of the leakage current. The heating was caused by a high power consumption in the resistor

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chains (mainly in the external ones). The bulk doping nonuniformities of the wafers were another problem. These local variations of bulk doping concentrations cause perturbations of the applied electric field. Consequently, the distortions of the confining potential gutters resulting in a leak of guided signal electrons [3] and the deviations of the electron trajectories from straight lines [4] are observed. So far, we have used Czochralski wafers (thickness of 500 μ m) for detector fabrication. In this type of wafer, the doping nonuniformity was measured to be as high as 15%. Neutron transmutation doped (NTD) wafers having a doping nonuniformity as low as a few percent should be preferentially used to fabricate linear silicon drift detectors.

In this paper, we present X-ray spectroscopy measurements obtained with MSSDDs fabricated on NTD wafers of a thickness of 290 μ m. The problem of the detector heating is solved by a new biasing approach minimizing the number of external connections. We will present results of the performance of an MSSDD with p^+ strips on both sides of the detector. Eventually, we plan to use an MSSDD with a semicontinuous backplane. This detector with an anode pitch of 500 μ m was designed and fabricated, but results are not yet available.

II. DETECTOR DESIGN AND ELECTRON CONFINEMENT

MSSDDs with a total active area of 2.5×1.3 cm² have been fabricated on 4-in wafers of NTD silicon with a resistivity of 3.5 k Ω cm and a thickness of 290 μ m. The detector is a bidirectional MSSDD with a maximum drift distance of 1.25 cm in both directions. The anode pitch is 250 μ m, and the p⁺ strip pitch is 200 μ m. The detector is subdivided into four sections with sawtooth angles $\alpha = 0^{\circ}$ (straight strips), 30°, 45°, and 60°. A resistor of 200 k Ω was implanted between adjacent p⁺ strips. The layout of MSSDDs together with detailed description of the design can be found in [1].

To achieve confinement of sufficient electron capacity, it is preferred to design the sawtooth strips on both sides of the detector. In this way, the confining effects, which propagate from the surface, sum and result in a potential minimum at the center of the wafer (see Fig. 1). We have used the SEMISIM package [5] to calculate these potential distributions. We assumed electric fields of 390 V/cm (a potential difference of 7.8 V between adjacent strips) and 280 V/cm (a potential difference of 5.6 V) on the anode side and p-side of the detector, respectively. These fields correspond to the measurement conditions.

The disadvantage of strips on the p-side (radiation entrance side) of the detector is the positive oxide charge accumulated in the oxide between strips. The positive charge affects the potential distribution beneath the oxide regions close to the surface and creates potential "pocket"-like distributions between

Fig. 1. The maximal depth of potential gutters as a function of the wafer depth supposing that sawtooth strips are placed on both sides (two-side driven confinement, anode pitch of 250 μ m) or only on one side (one-side driven, anode pitch of 250 and 500 μ m). The other parameters used for calculation are strip pitch of 200 μ m and $\alpha = 60^{\circ}$. The drift fields are 390 V/cm (n-side) and 280 V/cm (p-side). The horizontal dashed line shows the depth of potential gutters necessary to confine a hypothetical repulsion-free amount of charge.

the strips. The signal electrons generated by low-energy X-ray photons absorbed near the surface will therefore be partially captured, and consequently split events will occur. For this purpose, a shallow continuous p^+ implantation on the p-side is the ideal solution. However, using continuous implantation on one side of a large detector limits the maximum drift field applicable on the other side. If the potential difference between opposite electrodes across the wafer exceeds the bulk depletion voltage, the drifting potential minimum is set close to the surface and the signal electrons will be lost. As a tradeoff between strips and continuous implantation, we have designed a set of eight wide straight strips on the p-side of the detector. A stair-like potential distribution is applied on these strips. In this way, a reasonably large drift field can be applied on the n-side of the detector. The total area covered with oxide is also minimized. For this type of detector, the confinement is only one-side driven. Nevertheless, the potential gutters (for a drift field of 390 V/cm and $\alpha = 60^{\circ}$) are still deep enough to guarantee the electron confinement supposing that the signal electrons are not drifting closer than about 15 μ m from the p-side of the detector (see Fig. 1).

III. EXPERIMENTAL RESULTS

The detectors were mounted on ceramic boards that allow cooling down to \sim 200 K. The front-end p-JFETs of the preamplifiers are mounted close to the readout anode pixels to minimize the stray capacitance. The rest of the low-noise preamplifier [6] is placed on the board. The presented results were obtained from sections of the MSSDD with $\alpha = 0^{\circ}$ and 60° with strips on both sides.

A vacuum setup with liquid nitrogen cooling was used to carry out the room- and low-temperature X-ray spectroscopy measurements. We have used an uncollimated 55 Fe radioactive source for these measurements. The signals are processed by an ORTEC 672 spectroscopy amplifier with a semi-Gaussian filter and fed into an ORTEC AD114. For the light-generated electrons, a pulsed laser beam ($\lambda = 675$ nm) was used. The intensity of the laser beam was adjusted to produce \sim 25 000 signal

Fig. 2. Measured values of the voltages on certain strips on both sides of the tested MSSDD. We have used a 1-G Ω probe to measure these voltages.

electrons. An XY micrometric stage allows generation of the signal electrons along the entire active area of the detector. The voltages on p^+ strips were measured using a 1-GQ probe.

A. Biasing

To minimize the number of external connections to the biasing strips and external voltage dividers, we have used a self-biasing approach [7]. Only a few $p⁺$ strips are externally biased on the n-side, so a negative potential decreases in approximately uniform steps from the outermost p^+ strip (n₆₃) to the first p^+ strip (n_1) . The other strips are biased via the implanted resistors. On the opposite p-side, we bias externally only one strip (p_0) beneath the array of anode pixels. The potential on this strip has to be about -80 V in order to focus the drifting electrons toward readout anode pixels. The high-voltage (p_{63}) strip is biased by means of the reach-through effect between this strip and the exactly opposite strip n_{63} . When the voltage applied on strip n_{63} reaches the bulk depletion voltage, a hole current starts to flow between these two strips. From this point, the voltage on strip p_{63} follows the applied voltage on strip n_{63} with a 90-V lag. In Fig. 2, we have plotted voltages measured on different strips on both sides of the detector. We will refer to the biasing described above as bias A.

The disadvantage of this biasing method is a large potential difference between the opposite sides starting from strip number 43. In this region, the drifting potential minimum is positioned very close to the surface and one can expect losses of signal electrons. To improve this, we have set the voltage on strip n_{54} to a value approaching the one measured on the opposite strip p_{54} . In this way, the drifting potential minimum will be located sufficiently far away from the surface up to strip number 54 (see Fig. 2). The rest of the biasing (referred as bias B) remains unchanged. The voltage distribution on the p-side is not influenced by this modification. The average drift field is about 390 V/cm for both types of biasing.

B. Laser-Light Response

The experiment with laser-light-generated electrons is used to verify the confinement effect of the sawtooth strips and the uniformity of the response over the whole active area. Fig. 3 shows a plot of normalized charge collection along the drift length at

Fig. 3. Normalized charge collection at one anode pixel for $\alpha = 0^{\circ}$ and 60° measured with different biasing. The laser spot was always positioned in the center of the active area belonging to the tested anode pixel.

one anode pixel for $\alpha = 0^{\circ}$ and 60° for biasing A and B, and charge injection at the center of the active area belonging to the readout anode pixel. It is clearly visible that the signal can be read out only from about 75% of the total active area when using bias A. The electrons generated in the remaining \sim 25% of the active area are lost in the surface. The situation dramatically improves when switching to bias B. For $\alpha = 60^{\circ}$, we collect more than 93% of injected charge along the whole drift length, which indicates that also the confinement is well under control. The problems with bulk doping nonuniformities were also solved by using NTD wafers. We have measured straight electron trajectories as expected.

C. X-Ray Spectroscopy Measurements

The ⁵⁵Fe spectra obtained with an MSSDD for $\alpha = 0^{\circ}$ and 60° at low temperature are shown in Fig. 4(a) and (b). During the measurements, the ⁵⁵Fe source (\sim 1 \times 1 cm²) placed above the center of the detector irradiates approximately the whole active area of the detector. In the case of $\alpha = 60^{\circ}$, we have measured an energy resolution of 191 eV FWHM (18.1 rms electrons) for a shaping time of 6 μ s at 213 K. The split events are almost completely eliminated by using the sawtooth-shaped p^+ strips. The peak-to-valley ratio is 50 : 1. The observed small background is expected to be due to the edge effects. The energy resolution slightly deteriorates at higher temperatures, and $FWHM = 375$ eV (42 rms electrons) was measured at room temperature. In the case of $\alpha = 0^{\circ}$, despite of a small electronic noise, the spectroscopic quality worsens tremendously due to the high amount of split events.

The results presented above were obtained using a low-noise preamplifier [6], where the feedback capacitor is discharged by a slightly forward-biased gate-to-source junction of a p-channel JFET. A module including a low-noise p-channel JFET and a feedback capacitor (50 fF) was designed and fabricated. The input capacitance of the p-JFET is about 0.5 pF. To highlight the influence of major noise components at low temperatures, we have plotted measured equivalent noise charge (ENC) as a function of the shaping time in Fig. 5. We can determine the overall capacitance, leakage current, and $1/f$ noise by fitting the experimental points [8]. As one can see in Fig. 5, the ENC

Fig. 4. Pulse height spectra of 55 Fe measured with one anode pixel of MSSDD for (a) $\alpha = 60^{\circ}$ and (b) $\alpha = 0^{\circ}$ at a temperature of 213 K with a shaping time of 6 μ s. The detector was operated using bias B.

is dominated by the $1/f$ noise corresponding to about 15 rms electrons. The $1/f$ noise of the front-end p-JFET contributes less than one electron to the total ENC [9]. We suspect that a poor n-implantation of the readout anode pixels is responsible for this high level of $1/f$ noise. For this implantation, we use a relatively high dose of arsenic $(5 \times 10^{15} \text{ ions/cm}^2)$, and the implanted ions are activated at a temperature of 600 °C. Under these condition, only a small part of the implanted ions is activated and the damaged lattice is probably not entirely recovered. The quality of this junction should be improved by realizing this implantation with phosphorus instead.

The influence of the biasing methods A and B on the spectroscopic quality of the detector is negligible. A small worsening of the energy resolution is observed at room temperature, where the leakage current slightly increases for bias B due to the larger active area. For $\alpha = 0^{\circ}$, a small improvement of the spectroscopic quality is observed when using bias B due to improved charge collection (see Fig. 3).

IV. CONCLUSION

We have presented new results of X-ray spectroscopy measurements on MSSDD with strips on both sides and an anode pitch of 250 μ m fabricated on NTD wafers. Until now, circular SDD with one central anode was used for high-resolution X-ray

Fig. 5. ENC of the MSSDD ($\alpha = 60^{\circ}$) readout system as a function of the shaping time. The ENC values were measured at a temperature of 213 K. The most important contribution to the noise is the $1/f$ series noise and the white series noise coming from the total capacitance.

spectroscopy. We have proven that the spectroscopic quality of our position-sensitive MSSDDs is equal to the quality of a circular SDD. Naturally, the next step is to test an MSSDD with a semicontinuous backplane, which is necessary for detection of low-energy X-ray photons. The performed simulations verified that the charge confinement is satisfactory also in this case. The energy resolution of the MSSDD ($\alpha = 60^{\circ}$) is 191-eV FWHM per anode pixel at low temperatures. This is practically identical to the best energy resolution measured with a circular SDD fabricated by the same technology and using the same readout. Fur-

ther improvement of the energy resolution can be obtained by integrating the front-end JFET into the readout anode pixel and also by improving the quality of the n-implantations.

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