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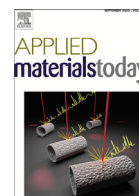
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Integrating van der Waals materials on paper substrates for electrical and optical applications

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ABSTRACT

Paper holds the promise to replace silicon substrates in applications like internet of things or disposable electronics that require ultra-low-cost electronic components and an environmentally friendly electronic waste management. In the last years, spurred by the abovementioned properties of paper as a substrate and the exceptional electronic, mechanical and optical properties of van der Waals (vdW) materials, many research groups have worked towards the integration of vdW materials-based devices on paper. Recently, a method to deposit a continuous film of densely packed interconnects of vdW materials on paper by simply rubbing the vdW crystals against the rough surface of paper has been presented. This method utilizes the weak interlayer vdW interactions and allows cleaving of the crystals into micro platelets through the abrasion against the paper. Here, we aim to illustrate the general character and the potential of this technique by fabricating films of 39 different vdW materials (including superconductors, semi-metals, semiconductors, and insulators) on standard copy paper. We have thoroughly characterized their optical properties showing their high optical quality: one can easily resolve the absorption band edge of semiconducting vdW materials and even the excitonic features present in some vdW materials with high exciton binding energy. We also measured the electrical resistivity for several vdW materials films on paper finding exceptionally low values, which are in some cases, orders of magnitude lower than those reported for analogous films produced by inkjet printing. We finally demonstrate the fabrication of field-effect devices with vdW materials on paper using the paper substrate as an ionic gate.

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While the cost of silicon substrates ($\sim 1000 \text{ €/m}^2$) constitutes a big portion of the electronic components price tag, standard copy paper is incomparably inexpensive ($\sim 0.1 \text{ €/m}^2$). [1,2] Moreover, unlike silicon, paper is biodegradable and thus its use can relieve some of the urgent issues and challenges of the electronic waste management. [1,3–6] Indeed, although paper electronics cannot

compete with silicon-based electronics in highly integrated circuits, simpler electronic components and sensors could be fabricated on paper substrates at a sizeable lower cost and with lower electronic waste footprint. Paper flexibility also makes it particularly suitable for flexible electronics applications. [5,7–10].

Given the fibrous structure of paper (that yields high surface roughness) the most widespread device fabrication approaches for van der Waals devices, based on mechanical exfoliation followed by lithographic and metal deposition steps, cannot be directly used. The scientific community, however, has developed new

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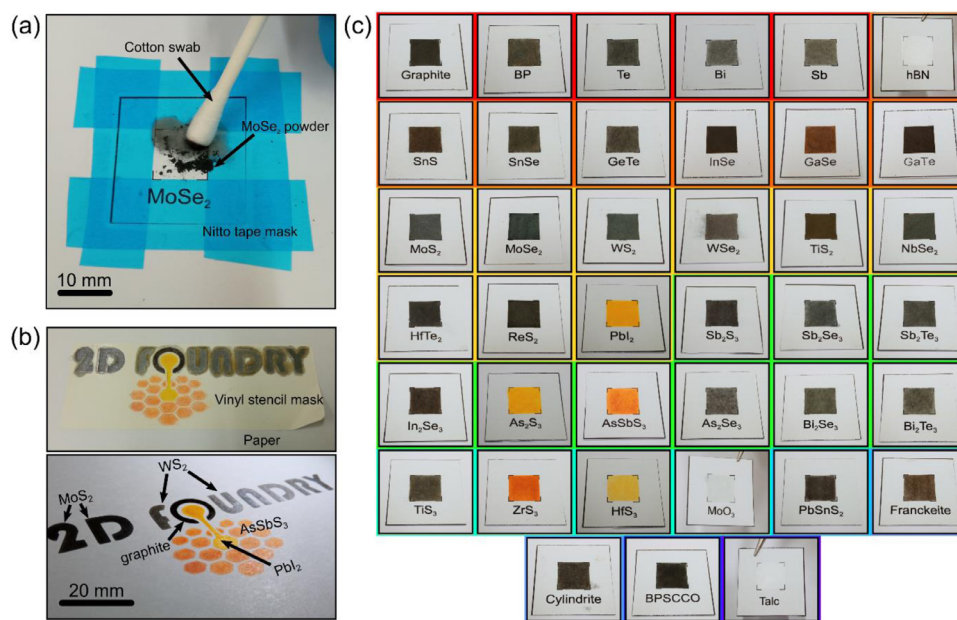


Fig. 1. Deposition of vdW materials on paper by the abrasion-based method. (a) Picture of the process employed to deposit a MoSe₂ film on standard paper by mechanically rubbing MoSe₂ powder against the surface of paper with a cotton swab. (b) Example of the use of a custom-designed vinyl stencil mask to deposit the vdW materials on paper following a desired geometry and even allowing the deposition of different vdW materials on different areas of the paper. (c) Pictures of the catalog of vdW materials deposited on paper by the abrasion-based method to illustrate the general character of this technique.

strategies to facilitate the fabrication of devices based on vdW materials onto paper substrates. [7,11,12] Up to now inkjet printing of inks prepared by liquid-phase exfoliation of vdW materials is probably the most common method and it has already demonstrated that it can be used to pattern complex devices, with high spatial resolution while combining different vdW materials. [13–16] Inkjet printing vdW materials on paper, however, is not exempt of open challenges. [17] First, this technique produces optimal results on the surface of specially engineered paper substrates, whose cost is substantially higher (5–15 €/m²) than standard copy paper, [5] and it needs non-standard printers. Another open challenge is that the drop-by-drop nature of inkjet printing makes it non-trivial to find the right parameters to obtain smooth and continuous films, avoiding the so-called “coffee ring” effect. Lastly, achieving highly conductive films using ink suspensions containing vdW materials is still challenging. [14,18–20] This is attributed to the presence of insulating adsorbates, coming from the solvent of the ink suspension, on the surface of the deposited vdW materials thus preventing an intimate contact between platelets. These open challenges further motivated the development of alternative deposition methods to fabricate vdW materials on standard paper like the all-dry abrasion-induced deposition method. This method is based on the erosion of a vdW material while it is being rubbed against the rough surface of paper, similarly to the action of drawing/writing with pencil on paper. These abrasion forces break the weak vdW interlayer bonds, yielding a film formed by a network of interconnected platelets. This method has been used almost exclusively to fabricate sensors and devices based on graphite [21–29] but recently it has been extended to a handful of other vdW materials. [30–33] In this work we aim to demonstrate the general character of this technique, showing that it can be applied to fabricate dense films of many different vdW materials on standard paper. We have characterized the basic morphology, structural, optical and electrical properties of films of different vdW materials on paper to provide a basic playground for future works on paper-based electronics integrating dry-deposited vdW materials.

The deposition of the vdW materials is carried out by simply rubbing the desired material, in fine powder form, against the

standard copy paper substrate with a cotton swab. Fig. 1a shows a picture acquired during the deposition of a MoSe₂ film. Nitto tape (SPV 224) has been used to form a square shaped mask to control the geometry of the resulting film. Note that more complex mask designs can be realized by using a desktop vinyl cutter machine (Silhouette Portrait). Fig. 1b shows an example of a paper substrate with a vinyl stencil mask, representing the logo of the ‘2D Foundry’ research group, adhered on its surface. Different parts of the mask have been used to deposit different vdW materials: graphite, MoS₂, WS₂, PbI₂ and AsSbS₃. The resulting deposited vdW film, after peeling off the mask, is shown in the bottom panel of Fig. 1b. Note that the desktop vinyl cutter machine allows for the patterning of features as small as 1 × 1 mm² and a minimum separation between features of 200 μm (see the Figure S1 in the Supporting Information).

We have found that this method to deposit vdW materials on paper is universal and it can be used to achieve densely packed films of a large variety of layered materials. Fig. 1c shows a summary of 39 different vdW materials on paper. This catalog contains diverse materials ranging from simple elemental materials (e.g. graphite, black phosphorus, tellurium...) to materials with very complex chemical and crystal structure (e.g. talc), and from superconductors (e.g. NbSe₂) to wide-gap insulators (e.g. h-BN, MoO₃). We address the reader to the Supporting Information (Figures S2 to S8) for a combined scanning electron microscopy (SEM) and energy-dispersive X-ray spectroscopy (EDX) analysis of the morphology and the chemical composition of the as-deposited films on paper. We observed that the vdW materials are homogeneously deposited over the paper surface except for few spots of uncoated paper that show up in the low magnification SEM images as brighter spots due to electrostatic charging. From the SEM images we also infer that abrasion-induced deposition process crushes the vdW materials flakes forming compact films onto the cellulose fibers while the gaps between fibers, where the pressure and friction forces are lower, are filled in with crystallites and it is sometimes possible to resolve loose flakes. Therefore, this deposition process leads to films with non-uniform thickness. This can be a handicap for applications, like high performance electronics or

the fabrication of micro/nano devices, but it can be acceptable for other applications where the ultra-low cost of the substrate and its biodegradability are more important than the performance (e.g. smart tags and patches). The EDX spectra (Figures S2 to S8) also demonstrate that the abrasion-induced deposition does not modify the chemical composition of the vdW materials. Furthermore, Figures S9 and S10 of the Supporting Information show a Raman spectroscopy characterization of the as-deposited films on paper demonstrating that the layered materials are not structurally modified during the deposition process except for some laser-induced oxidation in few cases (e.g. HfTe₂). Characteristics phonon modes for the different films are resolved, and their identification performed based on previously reported data (See the Supporting Information).

The optical properties of the as-prepared vdW films on paper were characterized by measuring their transmittance spectra in the 1.3 eV–2.7 eV range (see Materials and Methods section for details about the measurement). Fig. 2a shows a comparison of the spectra collected for different vdW materials on paper ranging from a semi-metal (graphite) to a wide-gap insulator (h-BN). The absorption band edge of the semiconducting materials with a gap value within the visible spectrum (e.g. In₂Se₃, Sb₂S₃, As₂Se₃, AsSbS₃, As₂S₃) is clearly visible as a rather abrupt increase in transmittance for energies lower than the band gap. The increase in transmittance matches very well with the band gap reported for these vdW materials in bulk and thin film. [34–37] Remarkably, some semiconducting vdW material films even show dips in the transmittance spectra associated with the generation of excitons. In fact, some transition metal dichalcogenides present high exciton binding energy that allow the observation of excitonic features in their reflectance and transmittance spectra even at room temperature. [38–43] Fig. 2b illustrates how for WS₂, MoS₂, WSe₂, MoSe₂ and ReS₂ on paper one can still resolve those excitonic features (highlighted with an arrow), which are in good agreement with those observed for multilayer flakes. [38–43] These observations demonstrate the high optical quality of these vdW films on paper and opens the door to apply them in ultra-low-cost optical component as wavelength selective coating or filters. We address the Supporting Information (Figures S11 and S12) for the optical characterization of other 25 vdW materials on paper. Fig. 2c shows an example where a As₂S₃ film on paper has been illuminated with monochromatic light with energy above and below its band gap showing a clear transition from opaque to transparent.

In order to study the electrical properties of the vdW material films we have used the transfer length method that consists of patterning the vdW material film under study into an elongated rectangular shape and depositing electrodes at increasing distance to extract the contact resistance and the resistivity. The electrodes are deposited by drawing with a graphite pencil (4B type, graphite content ~ 80%) rectangular contacts onto the vdW material film. Fig. 3a shows a picture of a WS₂ film on paper with graphite electrodes separated from 1 mm to 7 mm. Note that, once the electrical measurements are accomplished, one can quantitatively measure the thickness of the film, which is needed to determine the resistivity, by slicing the device with a sharp razor blade and imaging the cross-section under an optical microscope (see Fig. 3b). Fig. 3c shows current vs. voltage characteristics (*I*Vs hereafter) measured between different pair of electrodes with spacing ranging from 1 mm to 7 mm on a WS₂ film. The *I*Vs are very linear in the ±1 V voltage range and even for much higher voltages (see Figure S13 in the Supporting Information for *I*Vs in ±30 V range). Fig. 3d shows the resistance values (extracted from the slopes of the *I*Vs in Fig. 3c) as a function of the electrode distance that follows a linear trend. The intercept of that line with the vertical axis gives twice the contact resistance (*R*_c) value and the slope of the trend (*R*/*L*) allows determining the resistivity (ρ) as it is defined

as:

$$\rho = R \cdot W \cdot th / L$$

where *R* is the resistance and *L*, *W* and *th* are the length, width and thickness of the channel respectively.

From the transfer length measurement for WS₂ on paper, shown in Fig. 3d, we obtain a contact resistance *R*_c ~ 150 kΩ and a resistivity $\rho = 440 \pm 10 \Omega \cdot m$. We have carried out similar transfer length measurements on other 4 WS₂ devices, finding resistivity values between ~360 Ω·m and ~530 Ω·m (see the Supporting Information, Figure S16). These resistivity values are remarkably low as compared to the best resistivity values reported in the literature for films prepared with inks of liquid phase exfoliated WS₂ (10⁴ Ω·m). [44] Note that we have also measured six other transfer length devices with WS₂ from other two material sources, finding resistivity values in the ~ 70–370 Ω·m range. See the Supporting Information (Figure S16) for the datasets corresponding to the other two WS₂ sources.

We attribute these low resistivity values to the all-dry nature of the deposition method which avoids the presence of inter-platelet adsorbates that can be difficult to remove and may hamper the electrical transport. To get an insight about the device reproducibility we have tested 118 WS₂ devices with 2 mm of channel length and 10 mm of width. Fig. 3e shows a histogram with the resistance values measured on the 118 WS₂ devices. The histogram follows a skewed normal distribution with a peak at ~3 MΩ. Interestingly ~ 95% of the devices have resistances in the 1.0–6.7 MΩ, a low dispersion taking into account the nature of the films: a random network of interconnected platelets where percolation transport is expected. In fact, we have simulated the percolative electronic transport of the van der Waals devices on paper through a random resistor network model finding very similar statistical (a lognormal) distribution of the resistance (see the Figure S19 in the Supporting Information for details about the simulation). See Figure S20 in the Supporting Information for a calculation of the resistance dispersion as a function of channel length in a percolative film. The inset in Fig. 3e shows a histogram of the average thickness values measured on 35 WS₂ films finding that the thickness of the deposited vdW films have a median value of ~ 20 ± 5 μm. (see the Supporting Information Figure S21 for a thickness histogram including the thickness of other vdW materials films).

Fig. 3f summarizes the resistivity values measured in transfer length devices on paper based on 15 different vdW materials: NbSe₂, graphite (from 4 different sources), GeTe, TiS₃, TiS₂, SnSe, SnS, BP, WS₂ (from 3 different sources), MoSe₂, WSe₂, MoS₂ (from 3 different sources), ReS₂, In₂Se₃ and GaTe. These materials range from metals/semimetals to semiconductors. We obtain resistivity values as low as (2.1–5.6)·10⁻³ Ω·m for NbSe₂, 1.7·10⁻³–4.7·10⁻² Ω·m for graphite (depending on the graphite source) and 8.2·10⁻²–1.2·10⁻¹ Ω·m for GeTe, metal or semimetal vdW materials. For narrow gap semiconducting materials (TiS₃, TiS₂, SnSe, SnS and BP) we obtain values spanning from 4.1 Ω·m for TiS₃ to 3.0·10⁴ Ω·m (for BP). For the semiconducting materials with a band gap in the visible part of the spectrum (MoSe₂, WSe₂, MoS₂, ReS₂, In₂Se₃ and GaTe) all the resistivity values obtained are between 2.5·10⁴ and 3.9·10⁵ Ω·m, in the same range of the lowest resistivity values reported for inkjet-printed MoS₂ films (~ 1.1·10⁴–1.7·10⁶ Ω·m). [14,18–20] For WS₂, as discussed above, we obtain lower resistivity values (in the 72.6–528.2 Ω·m). These measurements further demonstrate how the all-dry nature of the abrasion-based deposition method allows the fabrication of films with high electrical performance. We address the reader to the Supporting Information for the datasets of the transfer length devices, measured for the different vdW materials (Figures S14, S15, S16, S17 and S18). The WS₂ resistance histogram, shown in Fig. 3e, could be used as an estimation for the standard variation expected for the

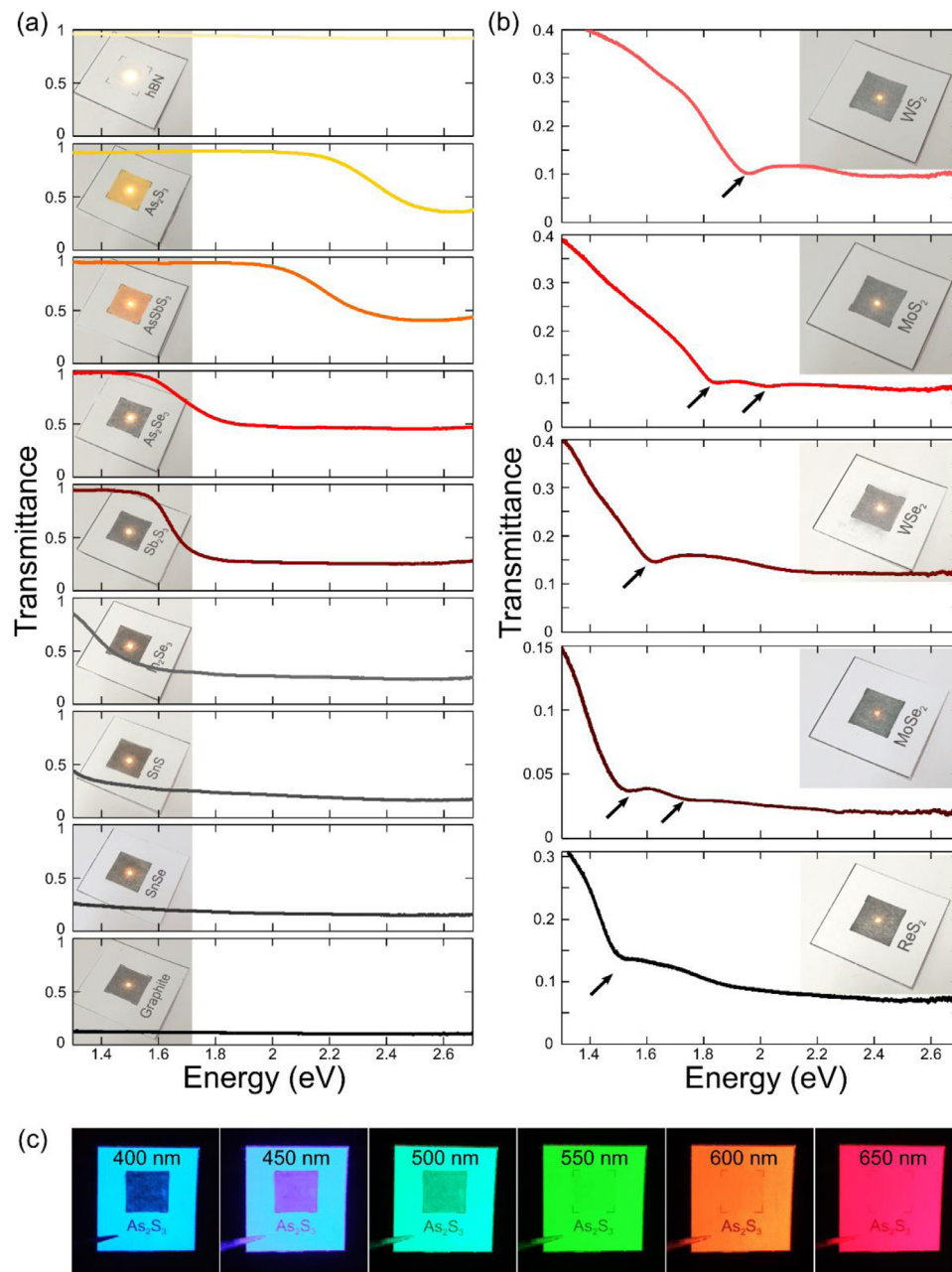


Fig. 2. Optical properties of vdW materials on paper. (a) Transmittance spectra of 9 different vdW materials films on paper with band gaps ranging from 0 eV (graphite) to -6 eV (hBN). For In_2Se_3 , Sb_2S_3 , As_2Se_3 , AsSbS_3 and As_2S_3 the absorption band edge can be resolved in the measured spectra as an abrupt change of the transmittance. (b) Transmittance spectra of 5 vdW materials on paper with a high exciton binding energy that present dips (highlighted with arrows) in the spectra associated to the photogeneration of excitons. The energy of the dips matches with the literature values of the excitons obtained on multilayered flakes through photoluminescence, reflectance and absorption spectroscopies. (c) Pictures of a As_2S_3 film illuminated with monochromatic light with energy higher than the band gap ($\lambda < 520$ nm) and lower than the band gap ($\lambda > 520$ nm) where a sudden change in opacity of the film is observed.

resistivity of these films: $\sim \pm 33$ –50%. Note that one would expect that the source of powdered vdW material could have a strong impact in the properties of the deposited films. This is still an open problem in the community working on van der Waals materials. There are only few works devoted to systematically correlate the properties of exfoliated materials produced from different sources of van der Waals materials. [45–47] Here we compared the resistivity values obtained for graphite, WS_2 and MoS_2 from different sources finding that sources with smaller particle size led to lower electrical resistivity. But a more comprehensive study in a single vdW material should be done in the future to draw more robust conclusions.

We further explore the possibility of fabricating more complex devices such as field-effect devices with the films of vdW materials on paper. To do so we employ a back-gate configuration (see Fig. 4a) [48] where the fillers (e.g. CaCO_3) and hydroxyl groups present on the paper lead to the formation of cation-anion pairs upon electrical field bias. [49–51] Therefore, the paper substrate acts as a solid ionic gate that can be used to modulate the electrical properties of the as deposited vdW materials. Fig. 4b shows the conductance of 4 paper-supported field-effect devices based on different vdW materials (graphite, TiS_2 , WS_2 and TiS_3) as a function of the voltage applied to the gate electrode. The insets show the current vs. voltage characteristics acquired at different gate volt-

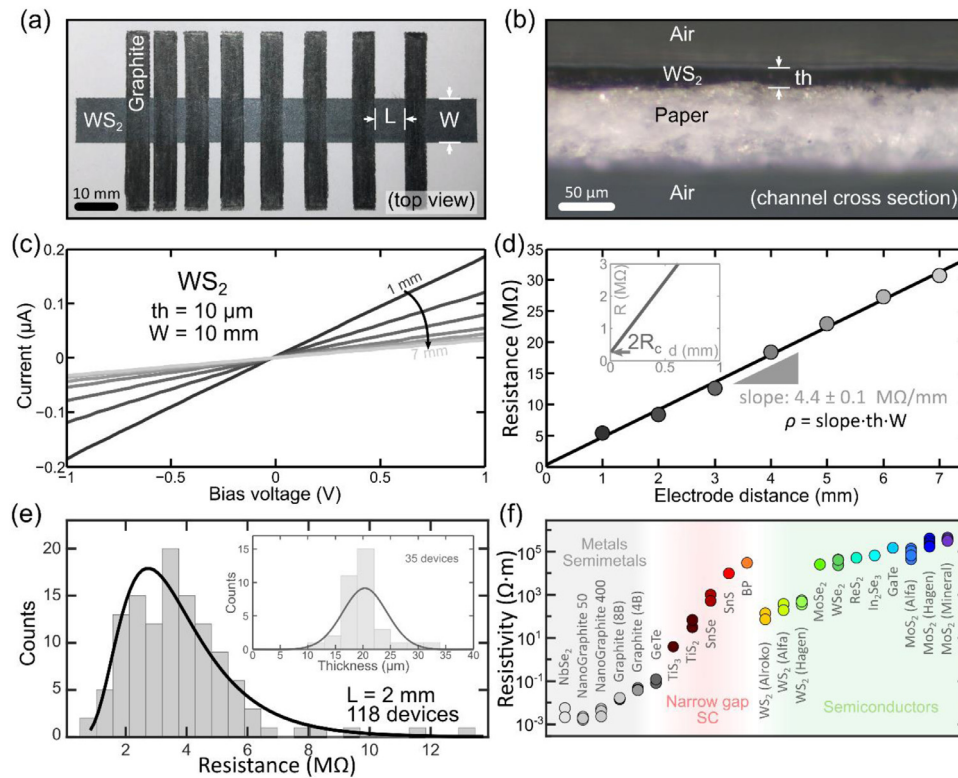


Fig. 3. Electrical properties of vdW materials on paper. (a) Picture of a transfer length device on standard paper with a WS₂ channel and graphite electrodes. (b) Optical microscopy image of the cross-section of the WS₂ film on paper where the average thickness of the WS₂ film can be measured. (c) Current vs. voltage characteristics (*IV*s) of WS₂ channels with different length from 1 mm to 7 mm. (d) Resistance vs. channel length, extracted from the *IV*s in (c). The slope of the resistance vs. length dataset can be used to extract the resistivity and the intercept with the vertical axis allows the extraction of the contact resistance R_c (see inset). (e) Histogram constructed with the resistance values measured on 118 WS₂ devices ($L = 2$ mm, $W = 10$ mm) to get an estimation of the device-to-device variation. A well-defined peak at ~ 2 – 4 M Ω is obtained. The inset in (e) shows a histogram of the thickness values measured on 35 WS₂ films on paper where a well-defined average thickness of $\sim 20 \pm 5$ μ m is obtained. (f) Comparison of the resistivity values measured for films of 15 different vdW materials.

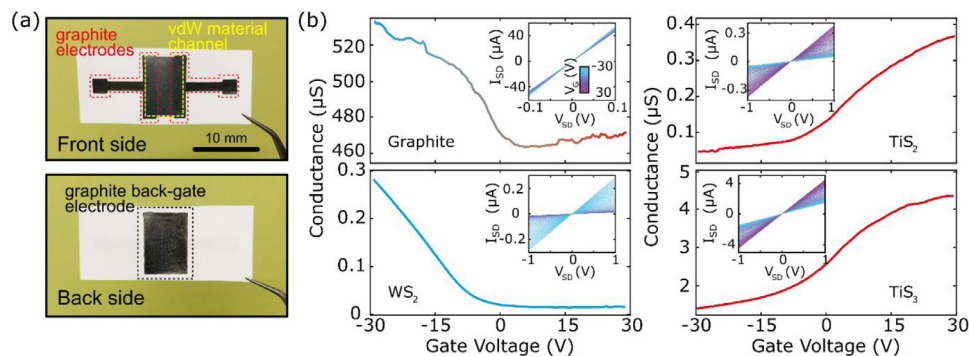


Fig. 4. Electric field-effect in vdW materials on paper. (a) Pictures of a device employed in electric field-effect gating of van der Waals films on paper. A graphite back-gate electrode is deposited on the back side of the paper substrate. (b) Conductance of graphite (Nanographite 400), TiS₂, TiS₃ and WS₂ (Hagen Automation) as a function of the voltage applied to the back-gate electrode. The insets show the current vs. voltage characteristics of the devices at different gates.

ages. While graphite shows a weak ambipolar (although stronger for hole) modulation of the conductance, the other materials show a sizeable change in the conductance upon gating. This is expected due to the semi-metallic nature of graphite which leads to a more effective electric field screening than for semiconducting materials. TiS₂ and TiS₃ are n-type and show modulations up to a factor of ~ 8 . WS₂, on the other hand, shows a p-type behavior with a modulation of the conductance up to a factor of ~ 19 . These results illustrate the feasibility of fabricating more complex electronic devices on paper with vdW materials.

Conclusions

In summary, we demonstrate the potential of abrasion-induced deposition method to fabricate films of a large variety of vdW materials, with different structural, optical and electrical properties, on standard copy paper. We characterize the crystallinity, morphology and chemical composition of 39 vdW material films on paper by a combination of Raman spectroscopy, scanning electron microscopy and energy-dispersive X-ray spectroscopy. Their optical properties were tested by measuring their transmittance spectra,

finding that the semiconducting films show well-defined absorption band edges and, in some cases, even excitonic features. The electrical properties of WS₂ films have been thoroughly characterized by studying more than 100 devices finding resistivities in the 70–530 Ω·m range, which is about 20–150 times lower than the lowest reported resistivity values for films based on WS₂ inks prepared by liquid phase exfoliation. We have also measured the resistivities of 14 additional vdW materials with different properties ranging from metal/semimetals to semiconductors with a sizeable gap. A resistivity as low as $2.1 \cdot 10^{-3}$ Ω·m has been measured for metallic NbSe₂. We also demonstrate the possibility of fabricating field-effect devices with vdW materials on paper, with a gate dependent modulation of the conductance up to a factor of ~19, using the paper substrate as an ionic gate. Our results show how the abrasion-induced deposition method yields vdW material films on paper capable of high performance for optical and electrical applications and open the door to further work exploring the intrinsic properties and applications of vdW materials on standard paper substrates.

Materials and methods

Materials sources

Graphite was extracted from 4B and 8B Faber Castell pencils. Nano graphite powder with 400 nm (PN: MKN-CG-400) and 50 nm (PN: MKN-CG-50) average particle sizes were purchased at Lowerfriction Lubricants.

Antimony (Smart-elements), bismuth (Novaelements) and tellurium (Novaelements) high purity (>99.99%) chunks were crushed and manually ground before abrasion-induced deposition on paper.

BP (black phosphorus) was synthesized using a high-pressure procedure in a piston-cylinder press (Rockland Research Co.), and a pressure of 2 GPa at high temperature of 1073 K for 1 h. Initially, small pieces of amorphous red phosphorus (Alfa-Aesar) were ground, in an agate mortar inside a nitrogen-filled glove box; the material was sealed in a niobium capsule, and then introduced inside a cylindrical graphite heater. After quenching and releasing pressure, pieces of BP (6–7 mm in diameter; 5 mm in thickness) were recovered. [52] The as-synthesized BP chunks were crushed and manually ground before abrasion-induced deposition on paper.

hBN (PN: 11,078.18), SnS (PN: 14,051.06), SnSe (PN: 18,781.06), GeTe (PN: 45,461.03), WSe₂ (PN: 13,084.09), TiS₂ (PN: 12,826.06), NbSe₂ (PN: 13,101.09), MoSe₂ (PN: 13,112.06), HfTe₂ (PN: 39,223.03), ReS₂ (PN: 89,482.04), Sb₂Se₃ (PN: 13,130.06), Sb₂Te₃ (PN: 45,922.09), In₂Se₃ (PN: 88,280.06), As₂Se₃ (PN: 13,130.06), Bi₂Se₃ (PN: 47,198.06), Bi₂Te₃ (PN: 44,077.06), MoO₃ (PN: A11159.18) high purity materials were purchased from Alfa Aesar. Most materials came in fine powder form, but some materials had to be manually ground before using.

High purity PbI₂ was purchased at Sigma Aldrich (PN: 203,602–50 G) and re-crystallized into micro-platelets shape. [53]

BPSCCO (Bi_{1.6}Pb_{0.4}Sr_{1.6}Ca_{2.0}Cu_{2.8}O_{9.2+x}) fine powder was purchased from Sigma Aldrich (PN: 378,720–10 G).

MoS₂ of three different sources were used in this work: Alfa Aesar (PN: 41,827.14), Hagen Automation Ltd and a natural molybdenite mineral.

WS₂ of three different suppliers were used in this work: Hagen Automation Ltd, Alroko GmbH & Co KG and Alfa Aesar (PN: 11,829.18).

InSe, GaSe, GaTe single crystals were grown by the Bridgman method [54–57]. High purity In (7 N), Ga (6 N), Se (6 N) and Te (7 N) was used as the raw material. Gallium was baked at 673 K for 4 h under high vacuum to remove the oxide layer. Synthesis were performed before crystal growth. For GaTe, direct synthesis

was used in a rocking furnace where Ga and Te were mixed in a stoichiometric ratio and sealed in a quartz crucible at 10⁻⁵ Pa. For InSe and GaSe, Physical Vapor Transfer (PVT) method was used. In/Ga and Se source were placed at both ends of a horizontal crucible in a nine-zone furnace. The source temperature was optimized separately during the synthesis and cooling process to ensure the stoichiometry of the compound. After that, a six-temperature zone furnace was used to grow the InSe/GaSe/GaTe single crystal by vertical Bridgman method. The temperature gradient is usually 5–10 K/cm, the growth rate is among 0.5–1 mm/h. T

Sb₂S₃, As₂S₃, AsSbS₃, PbSnS₂, franckeite, cylindrite and talc materials were extracted from natural minerals directly purchased in mineral collector's shops. The minerals were manually ground before using.

TiS₃, ZrS₃, HfS₃: Powders of TiS₃, ZrS₃ and HfS₃ were synthesized by a solid-gas reaction of metal powders of Ti (Goodfellow, 99.5%), Zr (Johnson Matthey, 99.9%) and Hf (Alfa Aesar, 99.6%) with sulfur (Merck, 99.75%) at molar ratios of M/S = 3 in a vacuum sealed ampoule annealed at 550 °C during 60 h. To obtain TiS₂ powder the annealing temperature used was 600 °C and the Ti/S molar ratio = 2. [58].

Scanning electron microscopy and energy-dispersive X ray spectroscopy characterization. The topography and the chemical composition of the films of vdW materials on paper were characterized using a FEI Helios G4 CX system. A thin film of sputtered gold or a spin coated layer of electrically conductive resist (Electra 92) were used to image films of insulating vdW materials (e.g. hBN, MoO₃ or talc).

Raman characterization. Raman spectra of the vdW materials were acquired under ambient conditions using a confocal Raman microscope (MonoVista CRS+, Spectroscopy & Imaging GmbH, Germany) with a 1500 lines/mm grating in the backscattering geometry. A 532 nm line of a CW solid-state laser at 0.5 mW power was used for excitation through a 100 × magnification microscope objective (NA = 0.9). The 300 lines/mm grating was used to acquire a reference spectrum of the paper substrate, covering the full Raman spectrum (50 to 4600 cm⁻¹).

Optical characterization. The transmittance spectroscopy measurements were carried out by illuminating the samples with fiber coupled halogen lamp (OSL2, Thorlabs) and collecting the transmitted light with a fiber coupled CCD spectrometer (CCS200/M, Thorlabs). The transmittance spectra (T) were obtained by making the quotient of the transmission spectrum acquired on the blank paper (I₀) and that obtained on the paper covered by the vdW film (I): $T = I/I_0$.

Electronic characterization. The electrical transport measurements were carried out in a homebuilt probe station operated at atmospheric conditions. A Keithley 2450 source-measure unit was used to acquire the current vs. voltage characteristics. For the field effect measurements, a pair of programmable benchtop power supply (TENMA 72–2545) were connected to the back-gate electrode.

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Data availability

The raw/processed data required to reproduce these findings cannot be shared at this time due to technical or time limitations.

Data will be made available on request.

Declaration of Competing Interest

The authors declare no competing financial interests.

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Supplementary materials

Supplementary material associated with this article can be found, in the online version, at doi:[10.1016/j.apmt.2021.101012](https://doi.org/10.1016/j.apmt.2021.101012).

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