

Niobium titanium nitride-based superconductor-insulator-superconductor mixers for low-noise terahertz receivers

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Integrating NbTiN-based microstrip tuning circuits with traditional Nb superconductor-insulator-superconductor (SIS) junctions enables the low-noise operation regime of SIS mixers to be extended from below 0.7 to 1.15 THz. In particular, mixers incorporating a NbTiN/SiO₂/NbTiN microstrip tuning circuit offer low-noise performance below 0.8–0.85 THz, although their sensitivities drop significantly at higher frequencies. Furthermore, a microstrip geometry in which NbTiN is used as the ground plane material only (NbTiN/SiO₂/Al) yields significant improvements in the sensitivities of SIS mixers operating up to 1.15 THz, with an upper operating frequency that depends upon the quality of the NbTiN layer, and thus its deposition process. Films deposited at room temperature have $T_c = 14.4$ K and $\rho_{n,20\text{ K}} \sim 60 \mu\Omega \text{ cm}$, and offer low-noise performance up to 1 THz, whereas films deposited at 400 °C have $T_c = 16$ K and $\rho_{n,20\text{ K}} \sim 110 \mu\Omega \text{ cm}$, and offer low-noise performance up to 1.15 THz. Taken together, these results demonstrate that the high-frequency surface resistance of a NbTiN layer depends upon the film's structural properties. Most significantly, the drop in performance that is seen at $F > 1$ THz in mixers incorporating NbTiN ground planes deposited at room temperature is attributed to nonhomogeneities in the structural and electrical properties of these films, as is the poor performance of mixers that incorporate NbTiN wiring layers at $F > 0.85$ THz. The development of these NbTiN-based microstrip tuning circuits will enable the production of low-noise SIS mixers for the 0.8–0.96- and 0.96–1.12-THz frequency bands of the Heterodyne Instrument for the Far Infrared on board the European Space Agency's Herschel Space Observatory. © 2005 American Institute of Physics. [DOI: 10.1063/1.1927281]

INTRODUCTION

Low-noise, broadband terahertz mixers will enable the Heterodyne Instrument for the Far Infrared (HIFI) on board the European Space Agency's Herschel Space Observatory to provide full coverage of the 0.48–1.25- and 1.41–1.91-THz spectral ranges with high sensitivities.¹ Combined with the absence of absorption by atmospheric water vapor (the atmosphere is virtually opaque over the majority of this spectral range), this broad spectral coverage and high sensitivity will provide astronomers with a unique opportunity to observe spectral lines of a range of chemically and physically important molecules, atoms, and ions. However, in order to realize this potential, it has been necessary to develop superconductor-insulator-superconductor (SIS) mixer technologies to extend the low-noise performance of Nb-based SIS mixers from millimeter and low submillimeter frequencies ($F < 0.7$ THz) (Ref. 2) to as high as 1.25 THz.

Past work has shown that a drop in the sensitivities of traditional Nb SIS mixers at high frequencies³ can be largely attributed to increasing losses in Nb-based microstrip tuning

circuits at $F > F_{\text{gap}} = 2\Delta/h \sim 0.7$ THz,⁴ where 2Δ is the superconductor's energy gap and F_{gap} is termed the “gap frequency” of the material. This drop in sensitivity was partially addressed by the integration of Al-based microstrip tuning circuits with high current-density (high- J_c) Nb/Al–AlO_x/Nb SIS junctions, which yielded a double sideband receiver noise temperature of $T_{N,\text{rec}} = 840$ K at 1042 GHz.⁵ However, it was concluded that the ultimate performance of these mixers was limited by a combination of shot noise in their high current-density AlO_x-based tunnel junctions and ~ 5 –6 dB of coupling losses in their Al-based tuning circuits.⁶ Thus, one of the goals in the development of low-noise terahertz SIS mixers has been the development of microstrip tuning circuits based upon superconductors with higher superconducting transition temperatures than the $T_c = 9.2$ K that is typical for Nb.

The first candidate to fill this role was NbN, as its $T_c \sim 16$ K was expected to offer low-loss performance below its superconducting gap frequency $F_{\text{gap}} = 2\Delta/h \sim 1.2$ THz, where $\Delta_{\text{NbN}} = (3.56\text{--}4.16)k_B T_{c,\text{NbN}}$ is taken from Ref. 7. However, the development of NbN-based SIS mixers had only limited success, partly due to the excess shot noise that is observed in some NbN SIS junctions,⁸ and partly due to the anomalously high surface resistance of NbN films depos-

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ited at room temperature on silicon or fused quartz.⁹ In contrast, NbTiN-based SIS mixers have yielded record low-noise receiver sensitivities at frequencies between 0.75 and 1 THz.^{10–12} (Note that recent results have demonstrated a significant improvement in the performance of NbN-based SIS mixers,¹³ with a mixer incorporating a NbN/SiO₂/Al tuning circuit yielding a sensitivity at 0.8 THz that is similar to those demonstrated previously in NbTiN-based mixers.)

This paper summarizes the design and performance of two waveguide SIS mixers incorporating NbTiN-based microstrip tuning circuits: one with a NbTiN/SiO₂/NbTiN microstrip in which the NbTiN layers are deposited at room temperature and one with a NbTiN/SiO₂/Al microstrip in which the NbTiN ground plane is deposited at 400 °C. Together with previously reported results for quasioptical¹¹ and waveguide¹² mixers containing NbTiN/SiO₂/Al tuning circuits in which the NbTiN ground plane is deposited at room temperature, these results demonstrate that the use of NbTiN-based tuning circuits enables the production of low-noise SIS receivers for frequencies between 0.7 and 1.15 THz. These results are also compared with knowledge of the electrical and structural properties of NbTiN and NbN films deposited at different substrate temperatures and on different substrate materials to yield qualitative conclusions about the high-frequency surface resistances of the NbTiN films that are used here.

MIXER DESIGN AND SIS DEVICE FABRICATION

As is summarized in Table I, the results are presented and discussed here for four basic mixer geometries that com-

bine different rf geometries and microstrip tuning circuit material combinations. Of the four mixers, three are waveguide mixers—mixers A and B, and that described in Ref. 12. The fourth mixer is the quasioptical mixer described in Ref. 11. Mixer A incorporates a full-height 1-THz waveguide geometry that is identical to that used in Ref. 12, while mixer B incorporates a half-height 1.04-THz waveguide geometry that is based upon that of a 650-GHz mixer that was developed for the James Clerk Maxwell Telescope.¹⁴ (As is discussed in detail in Ref. 15, the 1.04-THz mixer design is a scaled version of the 650-GHz design, but with the substrate suspended in the substrate channel to reduce the magnitude and reactance of the waveguide probe impedance.)

Waveguide mixers A and B described here make use of twin-junction tuning circuits that are similar to that used in Ref. 12 (see Fig. 1), but with their dimensions optimized to accommodate different tuning circuit material combinations; mixer A includes a NbTiN/SiO₂/NbTiN tuning circuit in which the NbTiN layers are deposited at room temperature, while mixer B includes a NbTiN/SiO₂/Al microstrip tuning circuit in which the NbTiN ground plane is deposited at NASA's Jet Propulsion Laboratory at 400 °C on an AlN buffer layer. In comparison, the previously reported waveguide¹² and quasioptical¹¹ mixers incorporated NbTiN/SiO₂/Al tuning circuits in which the ground planes were deposited at room temperature. As described in Ref. 16, the NbTiN films deposited at room temperature have $T_c = 14.4$ K and $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$, with moderate amounts of compressive stress. In contrast, deposition at 400 °C on an

TABLE I. Mixer embedding geometries, SIS device layer parameters, and SIS tuning circuit geometries for the mixers described here, including the deposition parameters used for their NbTiN ground planes.

	Mixer A	Mixer B	Ref. 11	Ref. 12
Mixer embedding geometry	1-THz full-height waveguide with diagonal horn	1.04-THz half-height waveguide with corrugated horn	0.95-THz twin-slot planar antenna with elliptical silicon lens	1-THz full-height waveguide with diagonal horn
Substrate	fused quartz	fused quartz	silicon	fused quartz
Ground plane	300-nm NbTiN 20 °C, Al ₂ O ₃ buffer $T_c = 14.4$ K $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$	300-nm NbTiN 400 °C, AlN buffer $T_c = 16$ K $\rho_{n,20\text{ K}} = 60 \mu\Omega \text{ cm}$	300-nm NbTiN 20 °C, Al ₂ O ₃ buffer $T_c = 14.4$ K $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$	300-nm NbTiN 20 °C, Al ₂ O ₃ buffer $T_c = 14.4$ K $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$
Dielectric layer	250-nm SiO ₂ $\epsilon_r = 3.8$ (assumed)	250-nm SiO ₂ $\epsilon_r = 3.8$ (assumed)	250-nm SiO ₂ $\epsilon_r = 3.8$ (assumed)	250-nm SiO ₂ $\epsilon_r = 3.8$ (assumed)
Wiring layer	400-nm NbTiN $T_c = 14.4$ K $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$	400-nm Al $\sigma_4\text{ K} \sim 2.5 \times 10^8 \Omega^{-1} \text{ m}^{-1}$	400-nm Al $\sigma_4\text{ K} \sim 2.5 \times 10^8 \Omega^{-1} \text{ m}^{-1}$	400-nm Al (+100-nm Nb) $\sigma_4\text{ K} \sim 2.5 \times 10^8 \Omega^{-1} \text{ m}^{-1}$
SIS junction	Nb/Al–AlO _x /Nb $J_c = 7\text{--}8 \text{ kA/cm}^2$ $A = 1 \mu\text{m}^2$ $R_{2.0\text{ mV}}/R_N \sim 10$	Nb/Al–AlO _x /Nb $J_c = 6\text{--}7 \text{ kA/cm}^2$ $A = 1\text{--}1.2 \mu\text{m}^2$ $R_{2.0\text{ mV}}/R_N \sim 30\text{--}50$	Nb/Al–AlO _x /Nb $J_c = 7\text{--}8 \text{ kA/cm}^2$ $A = 0.7\text{--}1.05 \mu\text{m}^2$ $R_{2.0\text{ mV}}/R_N = 30\text{--}50$	Nb/Al–AlO _x /Nb $J_c = 7\text{--}8 \text{ kA/cm}^2$ $A = 0.58\text{--}0.67 \mu\text{m}^2$ $R_{2.0\text{ mV}}/R_N = 10\text{--}17$
Transformer dimensions	$W_{\text{tr}} = 6 \mu\text{m}$ $L_{\text{tr}} = 20\text{--}26 \mu\text{m}$	$W_{\text{tr}} = 4\text{--}5 \mu\text{m}$ $L_{\text{tr}} = 20\text{--}22 \mu\text{m}$	$W_{\text{tr}} = 5 \mu\text{m}$ $L_{\text{tr}} = 23 \mu\text{m}$	$W_{\text{tr}} = 7 \mu\text{m}$ $L_{\text{tr}} = 18\text{--}23 \mu\text{m}$
Junction separation	$W_s = 10 \mu\text{m}$ $L_s = 4\text{--}7 \mu\text{m}$	$W_s = 6\text{--}7 \mu\text{m}$ $L_s = 3.5\text{--}5.5 \mu\text{m}$	$W_s = 5 \mu\text{m}$ $L_s = 4 \mu\text{m}$	$W_s = 11 \mu\text{m}$ $L_s = 4\text{--}7 \mu\text{m}$

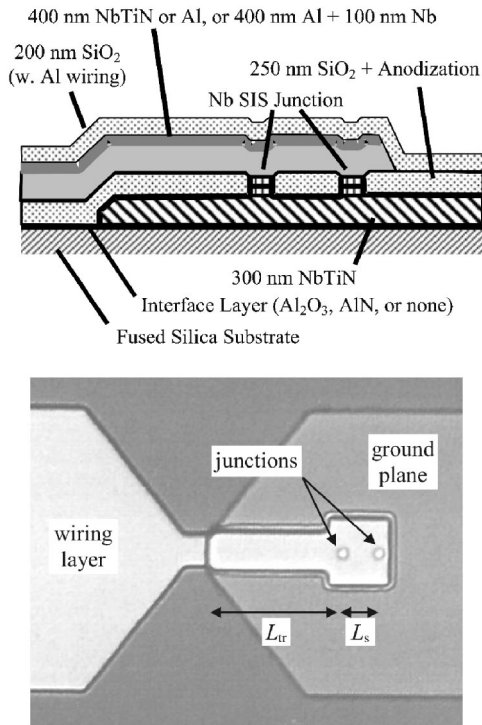


FIG. 1. Twin-junction tuning circuit geometry. (top) A cross section of the microstrip geometry. (bottom) A photograph of a 1-THz SIS device incorporating two $1\text{-}\mu\text{m}^2$ SIS junctions and a NbTiN/SiO₂/Al microstrip (modified from Ref. 12). All of the waveguide devices presented here have this basic geometry, but with lengths and widths of the transformer and the junction separation (identified by L_{tr} and L_s) that are optimized to suit the surface impedances of the microstrip's ground plane and wiring layer (see Table I). As is described in Ref. 11, the quasi-optical devices presented here also include a two-junction tuning circuit.

AlN buffer layer yields NbTiN ground planes with $T_c = 16\text{ K}$ and $\rho_{n,20\text{ K}} = 60\ \mu\Omega\text{ cm}$ (also with moderate amounts of compressive stress).¹⁷

In all cases, the mixers described here include Nb/Al–AlO_x/Nb SIS junctions with areas of $0.6\text{--}1.2\ \mu\text{m}^2$ and current densities ranging between 6 and 8 kA/cm². The junctions were defined by contact optical lithography and a combination of reactive ion etching of the two Nb electrodes and Ar sputter etching of the Al–AlO_x barrier layer. In the devices in mixer A and in the previously reported results, the Nb layers were etched in a CF₄+O₂ plasma, while in the devices in mixer B the Nb layers were etched in a SF₆-based plasma to improve the anisotropy of the etch (and thus, the control of the junction size). In addition, the junction etch in the devices in mixer B included an additional O₂ reactive ion etch of the resist pattern following the etch of the top Nb electrode and prior to sputter etching the Al–AlO_x barrier. Hereafter referred to as a “resist-recessing” step, this oxygen etch shrinks the resist pattern so that the reactive ion etch of the bottom Nb electrode also etches the exposed edges of the top Nb electrode. The end result is thus a stepped junction profile in which the size of the top Nb electrode, which defines the active area of the Al–AlO_x tunnel barrier, is defined by the resist pattern after the O₂ plasma etch, while the size of the bottom Nb electrode is defined by the resist size before the O₂ etch. This resist-recessing step has been introduced because it ensures that the edges of the Al–AlO_x tunnel bar-

rier are not exposed to the Ar sputter etch of the barrier, as the final junction size is only reached at the completion of the second SF₆-based reactive ion etch. This, in turn, is expected to improve the quality of the junctions by reducing the risk of damage to the edges of the tunnel barrier. Note that the term “junction quality” is defined here as the junction's subgap to normal-state resistance ratio $R_{2.0\text{ mV}}/R_N$, such that high-quality junctions are those with low subgap leakage currents.

Other than the junction definition step (and the material of their wiring layers, which must be patterned in different ways), the other significant difference between the processes used for the devices in mixers A and B is that the use of an elevated-temperature deposition process for the ground planes in mixer B requires that the ground plane be patterned by reactive ion etching instead of lift-off. The sharp edge that this etch produces along the perimeter of the ground plane pattern can lead to problems with step coverage, as evidenced by short circuits between the wiring layer and the ground plane. This problem has been partially mitigated by “planarizing” the wafer after the NbTiN etch with a SiO₂ layer of approximately the same thickness as the NbTiN ground plane that is patterned to form the mirror image of the ground plane. However, this solution is not ideal, the buildup of the three SiO₂ layers in the devices (this planarization layer, the dielectric layer in the microstrip tuning circuit, and a passivation layer that protects the Al wiring layer from chemical attack) can lead to adhesion problems that reduce the process yield.

In all cases, the SiO₂ layers are deposited by rf magnetron sputtering of a SiO₂ target, while the Al and Nb layers are deposited by dc magnetron sputtering. A combination of a reasonable vacuum quality ($P_{\text{background}} < 5 \times 10^{-7}$ mbars) and a relatively high deposition rate (> 130 nm/min) yields 400-nm-thick aluminum wiring layers with a typical “residual resistance ratio” of $RRR = (R_{300\text{ K}} - R_{4\text{ K}})/R_{4\text{ K}} \geq 10$ at 4 K (yielding $\sigma_{\text{Al},4\text{ K}} > 3 \times 10^8\ \Omega^{-1}\text{ m}^{-1}$. Note that $\sigma_{\text{Al},4\text{ K}} \sim (2\text{--}2.5) \times 10^8\ \Omega^{-1}\text{ m}^{-1}$ is typically used when modeling the performance of these devices, to account for the fact that the rf radiation in the microstrip probes the bottom part of the Al layer, which is likely to have a local resistivity that is slightly higher than that of the bulk of the film. This is because the SiO₂ dielectric layer is not expected to be a good substrate for the initial growth of the Al film. The device performance is not expected to be strongly dependent upon the resistivity of the Al layer.

DC CURRENT–VOLTAGE CHARACTERISTICS

Figure 2 presents typical low-temperature current–voltage characteristics for SIS junctions from mixers A and B, with NbTiN/SiO₂/NbTiN and NbTiN/SiO₂/Al microstrip tuning circuits, respectively. As seen in Refs. 11 and 12, the use of a NbTiN/SiO₂/Al tuning circuit in mixer B yields a current–voltage characteristic that is typical of a Nb SIS junction, with a gap voltage of $V_{\text{gap}} = 2.7\text{--}2.8$ mV. When compared to the previous results of waveguide mixers in Ref. 12, the qualities of the junctions in mixer B (as measured by their subgap to normal-state resistance ratios) are

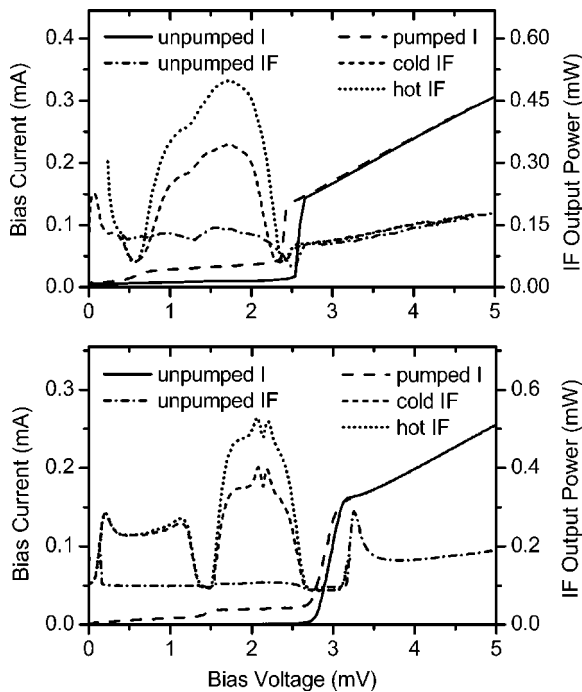


FIG. 2. Bias current and intermediate frequency (IF) output power as a function of bias voltage for mixer A (top, with a NbTiN/SiO₂/NbTiN tuning circuit) and mixer B (bottom, with a NbTiN/SiO₂/Al tuning circuit). The current–voltage characteristic of mixer B is typical for a Nb SIS junction (with $\sim 1 \Omega$ of series resistance in the Al wiring layer). However, that of mixer A shows evidence of gap voltage suppression due to the trapping of heat in the Nb junction electrodes, as demonstrated by the suppression of its gap voltage by the application of LO pumping power.

excellent, with $R_{2.0 \text{ mV}}/R_N \sim 30\text{--}50$ for this junction with $J_c = 6.5 \text{ kA/cm}^2$. This high quality has been repeated in several batches of devices produced using the resist-recessing step during the junction definition process and is taken as evidence that the incorporation of this extra process step is having a positive impact on junction quality. Note that previous work also yielded high junction qualities, including $R_{2.0 \text{ mV}}/R_N$ as high as 60 in the batch of quasioptical mixers described in Ref. 11. However, previously characterized waveguide mixers were typically characterized by poorer junction qualities, such as the value of $R_{2.0 \text{ mV}}/R_N = 10$ seen in Ref. 12.

In contrast with the results of mixer B, the current–voltage characteristic of mixer A, with an all-NbTiN tuning circuit, is characterized by a suppressed gap voltage of $V_{\text{gap}} \sim 2.6 \text{ mV}$. Additionally, although not shown here, the current–voltage characteristic of mixer A is characterized by hysteresis at the gap voltage, with $V_{\text{gap}} \approx 2.65 \text{ mV}$ if the gap is approached from the subgap region, and $V_{\text{gap}} \approx 2.55 \text{ mV}$ if the gap is approached from the normal-state region. As is discussed in Ref. 18, this gap voltage suppression is attributed to heat being trapped in the junction electrodes due to the fact that they are flanked by NbTiN layers that have larger superconducting energy gaps than that of Nb. As is also seen in Fig. 2, this heat trapping results in a further suppression of the gap voltage when local oscillator power is introduced. The net effect of this gap voltage suppression is to significantly reduce the bias voltage range that is available for mixer operation, which could effectively limit the maxi-

mum operating frequency of this mixer geometry—Nb SIS junctions combined with an all-NbTiN microstrip.

RF MEASUREMENT SETUP

All receiver sensitivities reported here are double-side band receiver noise temperatures determined from Y -factor measurements with a room-temperature “hot” blackbody and a 77-K “cold” blackbody. The effective input power levels from these loads, plus the noise contributions from optical losses, are determined from the Callen–Welton formulation for the thermal radiation of a blackbody.¹⁹

The heterodyne mixer measurements reported here were performed in two test cryostat configurations. Backward wave oscillators and carcinotron local oscillators (LO’s) combine to provide coverage of a large fraction of the 0.75–1.15-THz range. In both test configurations, the mixer is mounted on the helium-cooled 4-K stage of a HD8 Infrared Labs cryostat, with the LO and signal beams being combined with a dielectric beamsplitter and coupled into the cryostat via an optical window and infrared heat filters. The intermediate frequency (IF) output from the mixer is coupled to a cryogenic low-noise amplifier via an isolator that suppresses the formation of standing waves between the mixer and the amplifier. After cryogenic amplification, the mixer’s IF output signal is coupled out of the cryostat, further amplified, filtered, and then detected with a power meter. Unless specifically stated otherwise, the results presented here are obtained with the mixer at an operating temperature of 2–3 K. (This is achieved by pumping on the cryostat’s helium bath.) The improvement in the receiver noise temperature that is obtained by reducing the operating temperature from 4.5 K to less than 3 K is typically 25%–30%.

As is summarized in Table II, the primary differences between the measurement configurations used for the four sets of results considered here are the IF band and the optical elements in the path between the mixer and the hot/cold load. In particular, mixer A and those in Refs. 11 and 12 were tested at $F_{\text{IF}} = 1.5 \text{ GHz}$ with a conventional hot/cold configuration that included a beamsplitter for LO injection located between the hot/cold load and the cryostat, a dielectric film as a vacuum window, and Zitex G104 sheets as infrared heat filters. In contrast, mixer B was tested at $F_{\text{IF}} = 4\text{--}8 \text{ GHz}$ with a vacuum hot/cold system in which the beamsplitter is located inside the vacuum, which allows the vacuum window to be removed from the signal path. Additionally, while early tests of mixer A and the mixer described in Ref. 12 made use of a dielectric lens to focus the incident optical beam into the mixer’s horn antenna, mixer B was tested with a cold mirror as a focusing element, to further reduce the optical losses in the system. A focusing element was not needed in testing the quasioptical mixer reported in Ref. 11 because the elliptical lens used in that mixer produces a relatively collimated rf beam.

Note that the use of a broadband, higher-frequency IF system for testing mixer B increases the IF system’s noise contribution to the total receiver noise slightly, since the input noise temperature of the 4–8-GHz IF system is $T_{\text{N,IF}} = 8\text{--}10 \text{ K}$, while the 1.5-GHz IF system is characterized by

TABLE II. Receiver optics, intermediate frequency (IF) system configurations, and mixer operating temperatures used for testing mixers A and B and in Refs. 11 and 12.

	Mixer A	Mixer B	Ref. 11	Ref. 12
Beamsplitter (BS)	14-, 49- μm Mylar	14-, 49- μm Mylar	6-, 14-, 49- μm Mylar	14-, 49- μm Mylar
Dewar window	100- μm Mylar	not in signal path	12- μm Kapton	100- μm Mylar
Infrared heat filter(s)	Zitex G104 at 77 K	Zitex G104 at 77 K	Zitex G104 at 77 and 4.2 K	Zitex G104 at 77 K
Focusing element	high-density polyethylene lens	Au-coated mirror	silicon lens is part of mixer	high-density polyethylene lens
Receiver optics input noise and insertion loss	100 K, 2.0 dB at 830 GHz with 14- μm BS	60 K, 1.0 dB at 1040 GHz with 14- μm BS	35 K, 1.2 dB at 970 GHz with 6- μm BS	146 K, 2.5 dB at 970 GHz with 14- μm BS
IF bandwidth, center frequency, and input noise	$\Delta F_{\text{IF}}=80$ MHz $F_{\text{IF}}=1.5$ GHz $T_{\text{N,IF}}\sim 4-5$ K	$\Delta F_{\text{IF}}=4$ GHz $F_{\text{IF}}=4-8$ GHz $T_{\text{N,IF}}\sim 8-10$ K	$\Delta F_{\text{IF}}=80$ MHz $F_{\text{IF}}=1.5$ GHz $T_{\text{N,IF}}\sim 4-5$ K	$\Delta F_{\text{IF}}=80$ MHz $F_{\text{IF}}=1.5$ GHz $T_{\text{N,IF}}\sim 4-5$ K
Operating temperature	$T_{\text{mixer}}=4.5$ K	$T_{\text{mixer}}=2.5$ K	$T_{\text{mixer}}=2.5$ K	$T_{\text{mixer}}=2.5$ K

$T_{\text{N,IF}}=4-5$ K. Given a typical single sideband conversion gain of -10 dB for the mixers described here and optics losses of $1-2$ dB, this results in ~ 30 K of additional double sideband receiver noise in the measurements of mixer B relative to those of mixer A and the mixers in Refs. 11 and 12. However, this effect is much less than the differences in the optical losses in the different systems—these are summa-

rized in Table II. For this reason, the results obtained with different mixers are best compared after correcting the measured receiver sensitivities for the optical losses in the receiver cryostat, which yields $T_{\text{N,mixer+IF}}$.

Direct detection measurements of the mixers' response bandwidths are also presented here. These were obtained with a Fourier transform spectrometer in which a Michelson

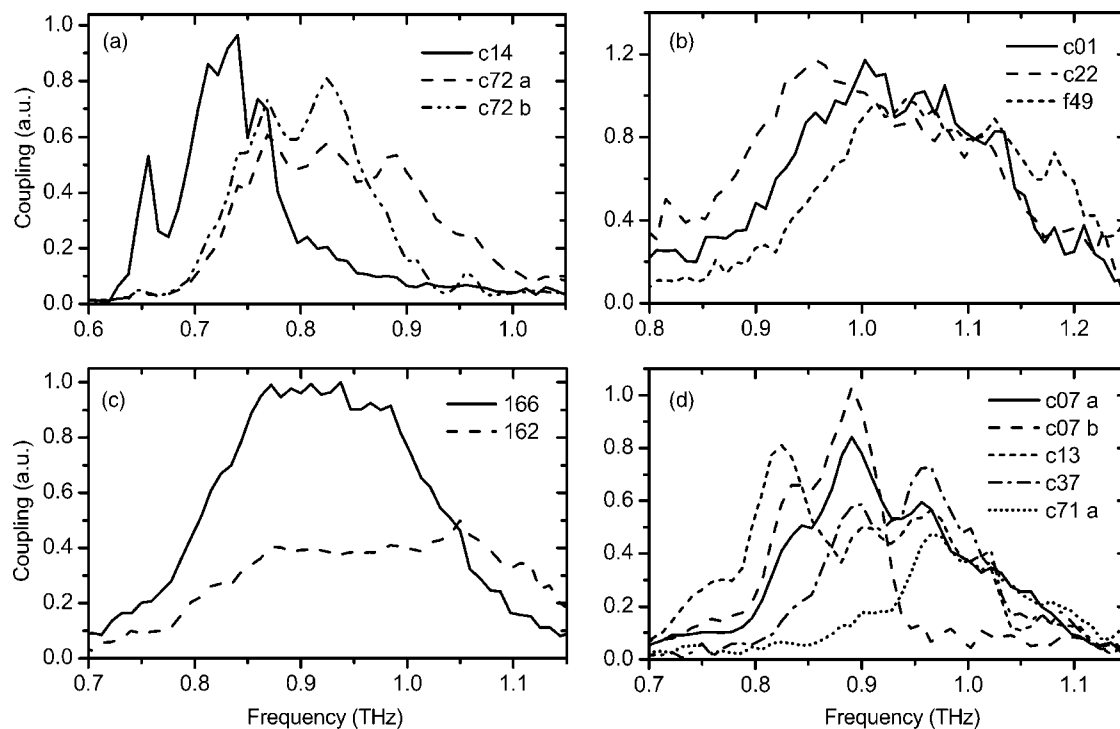


FIG. 3. Direct detection responses of several devices in: (a) mixer A with NbTiN/SiO₂/NbTiN tuning circuits; (b) mixer B with NbTiN/SiO₂/Al tuning circuits in which the NbTiN ground plane is deposited at 400 °C; (c) a quasi-optical mixer with a NbTiN/SiO₂/Al tuning circuit in which the NbTiN is deposited at room temperature (modified from Ref. 11); and (d) a waveguide mixer with a NbTiN/SiO₂/Al tuning circuit in which the NbTiN is deposited at room temperature (modified from Ref. 12). Note that the response of device c72 in (a) is the strongest high-frequency response that has been obtained with a NbTiN/SiO₂/NbTiN microstrip tuning circuit, with the curves marked c72a and c72b corresponding to measurements with different backshort depths in a mixer block that includes a sliding contacting backshort. Devices with a NbTiN/SiO₂/Al tuning circuit in which the NbTiN ground plane is deposited at 400 °C [in (b)] offer strong coupling above 1 THz, in notable contrast with those in which the NbTiN is deposited at room temperature [in (c) and (d)].

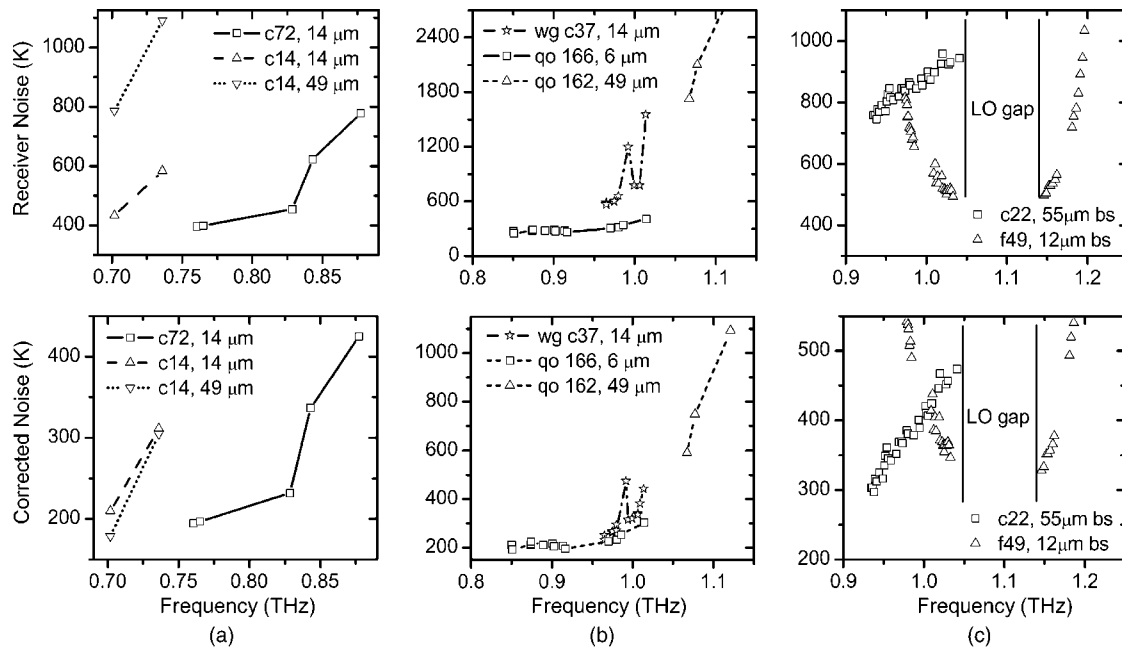


FIG. 4. Measured double sideband receiver noise temperatures of several devices in: (a) mixer A with NbTiN/SiO₂/NbTiN tuning circuits; (b) mixers from Refs. 11 and 12 with NbTiN/SiO₂/Al tuning circuits in which the NbTiN ground plane is deposited at room temperature; and (c) mixer B with NbTiN/SiO₂/Al tuning circuits in which the NbTiN ground plane is deposited at 400 °C. All results were obtained at $T_{\text{mixer}}=2.5$ K, except for those in (a), which were obtained at 4.5 K. All results were obtained at $F_{\text{IF}}=1.5$ GHz, with $T_{\text{N,IF}}\sim 4\text{--}5$ K, except for those in (c), which were obtained at $F_{\text{IF}}=4\text{--}8$ GHz, with $T_{\text{N,IF}}\sim 8\text{--}10$ K. In addition to the individual device labels, the legends also identify the beamsplitter thickness that was used for each measurement—a thick beamsplitter was used to compensate for a lack of LO power in some measurements. Because this significantly degrades these receiver sensitivities, mixers are best compared using their their effective input noise temperatures at the input to the mixer $T_{\text{N,mixer+IF}}$, which do not include the noise contributions of the receiver optics.

interferometer is used as a tunable signal source, the beam path of which can be evacuated to eliminate absorption lines of atmospheric water vapor from the measured spectra.

RF MEASUREMENT RESULTS

Figure 3 presents the measured direct detection spectra of several devices in each of mixers A and B, plus spectra from the mixers presented in Refs. 11 and 12. Two particularly notable features are seen in these spectra. First, it is noted that while mixer A shows strong responses at lower frequencies, none of the devices with NbTiN/SiO₂/NbTiN tuning circuits that were tested have strong responses above ~ 0.85 THz. Indeed, device c72 in Fig. 3(a) offered the best high-frequency performance of the devices tested in mixer A. However, measurements with different backshort depths—c72a and c72b in Fig. 3(a)—show that its highest sensitivities are obtained below 0.85 THz. This is in sharp contrast with the previously reported results obtained in the same mixer block using devices with NbTiN/SiO₂/Al tuning circuits [see Fig. 3(d) and Ref. 12], in which strong responses were seen up to 1 THz. The second point that is noted here is that mixer B (with a NbTiN/SiO₂/Al tuning circuit incorporating a NbTiN ground plane that was deposited at 400 °C) yields strong responses at frequencies well above the 1-THz upper frequency limit that was seen in the performance of the devices in Refs. 11 and 12 [see Figs. 3(b) and 3(c)], which incorporated NbTiN ground planes that were deposited at room temperature.

These observations of the frequency dependence of the mixers' direct detection responses are mirrored in their mea-

sured heterodyne sensitivities, which are summarized in Fig. 4. In particular, Fig. 4(a) clearly demonstrates that mixer A, with a NbTiN/SiO₂/NbTiN tuning circuit, offers low-noise performance at frequencies below 0.85 THz, with the input noise temperatures of these mixers increasing sharply at higher frequencies. (As noted above, device c72 has the best high-frequency sensitivity of all of the devices of this type that were measured.) In contrast, as is seen in Fig. 4(b), mixers with NbTiN/SiO₂/Al tuning circuits in which the NbTiN ground plane is deposited at room temperature offer low-noise performance at frequencies up to 1 THz. However, the receiver and mixer sensitivities from Refs. 11 and 12 are characterized by a sharp increase in their noise temperatures at frequencies higher than 1 THz. Fortunately, the results seen in Fig. 4(c) clearly demonstrate that the use (in mixer B) of NbTiN films deposited at 400 °C allows this upper frequency limit to be pushed to at least 1.1–1.15 THz.

Comparing the sensitivities of these receivers and mixers, it is seen that the mixers with NbTiN/SiO₂/NbTiN tuning circuits yield $T_{\text{N,mixer+IF}}\approx 200$ K at 0.75 THz (at an operating temperature of $T_{\text{mixer}}=4.5$ K), while mixers with a room-temperature NbTiN/SiO₂/Al tuning circuit yield $T_{\text{N,mixer+IF}}\approx 200$ K at 0.85–0.9 THz (at $T_{\text{mixer}}=2.5$ K). Both of these results are obtained at $F_{\text{IF}}=1.5$ GHz, with $T_{\text{N,IF}}=4\text{--}5$ K. Finally, the mixers with an elevated-temperature NbTiN ground plane and an Al wiring layer yield $T_{\text{N,mixer+IF}}=300\text{--}350$ K at 1.0–1.1 THz and $F_{\text{IF}}=4\text{--}8$ GHz (with $T_{\text{N,IF}}=8\text{--}10$ K at $T_{\text{mixer}}=2.5$ K).

DISCUSSION

As mentioned previously, mixers in which Nb SIS junctions are integrated into a NbTiN/SiO₂/NbTiN microstrip tuning circuit are characterized by heat trapping in the Nb junction electrodes. Following the discussion in Ref. 18, it is noted that this heat trapping is sufficient to raise the temperature of the junction electrodes by ~ 1 K due to the power dissipated by the dc bias voltage alone, plus another 1 K when the junctions are pumped by local oscillator power at a level that is similar to that which is used in heterodyne mixer measurements. Operationally, one effect of this heating is to suppress the junctions' gap voltages, which could limit their upper operating frequency, if the high-frequency sensitivity of the mixers was not limited by other factors. Specifically, the observed 0.8–0.85-THz upper frequency limit in these mixers is lower than the 0.9–1.0-THz operating frequency limit that would be expected from their junctions' 2.4-mV gap voltages when LO power is applied. Furthermore, the junctions' gap voltages are ~ 2.6 mV in the absence of LO power, so an even larger upper frequency limit would be expected in their direct detection responses, which are measured without a LO.

The heating effect observed in mixer A does complicate the operation of these mixers, due the fact that the width of the first photon step below the gap voltage is dependent upon the LO pumping level. Thus, if a NbTiN/SiO₂/NbTiN tuning circuit was to be used at frequencies below 0.8 THz, it would be desirable to reduce this heating effect. For example, the use of a Nb/Al–AlN_x/NbTiN junction²⁰ would allow heat to escape into the top NbTiN wiring layer via the NbTiN junction electrode. (Note that the first photon step below the gap voltage is the region in which the mixer's optimum bias voltage is found.)

The drop in sensitivity above ~ 0.85 THz in mixers incorporating NbTiN/SiO₂/NbTiN tuning circuits is attributed to losses in the NbTiN wiring layer that are caused by a variation in the superconducting properties of the NbTiN as a function of the vertical position within the film. In particular, it is noted that measurements of the T_c of NbTiN films of varying thickness showed that the T_c of these films drops significantly for film thicknesses less than 100 nm.²¹ Indeed, if the thickness dependence of the T_c of NbN films deposited at room temperature on silicon⁷ is combined with $T_c = 14.4$ K for bulk NbTiN, a value of $T_c \sim 11$ K is estimated for a 25-nm-thick layer of NbTiN. (This value is consistent with the trend seen in Ref. 21 for NbTiN films.) If this value is then used as the effective T_c of a NbTiN wiring layer, the effective gap frequency of this film is determined to be 0.8–0.9 THz [using $F_{\text{gap,NbTiN}} = (3.56 - 4.16)k_B T_c / h$, from previously determined relationships for similar NbN films⁷], which is consistent with the observed upper frequency limit in these devices. Note that because the observed drop in T_c with decreasing film thickness occurs even though the thicknesses in question (tens of nanometer) are much larger than the expected coherence length for these types of films (it is ~ 7 nm in single-crystal NbN films²²), this drop is not attributed to the superconducting proximity effect.

The fact that the NbTiN ground plane in a

NbTiN/SiO₂/Al microstrip can be used to higher frequencies than the 0.85-THz upper frequency limit seen in the performance of mixers with a NbTiN wiring layer is attributable to the fact that the fields in a microstrip are concentrated between the ground plane and wiring layer. Thus, the microstrip's performance is strongly influenced by the quality of the bottom tens of nanometer of a NbTiN wiring layer, which is precisely the region in which the T_c of the film is seen to drop. In contrast, the surface resistance of a NbTiN ground plane is dominated by the bulk properties of the film, since only a small fraction of the energy in the microstrip reaches the bottom tens of nanometer of the film.

As is reported in Refs. 11 and 12, the high-frequency drop in the sensitivity of mixers with NbTiN/SiO₂/Al tuning circuits in which the NbTiN ground plane is deposited at room temperature is attributed to losses in the NbTiN ground plane. However, this drop is seen at $F = 1$ THz, instead of the 1.05–1.1 THz that would be expected from the measured superconducting transition temperature of the film ($T_c = 14.4$ K) and the previously observed relationship between T_c and the superconducting energy gap in NbN films. This unexpectedly low upper frequency limit in the performance of these mixers is believed to be due to inhomogeneities in the NbTiN ground plane. In particular, the observed rf losses are attributed to the presence of grains in the NbTiN film with local T_c 's that are lower than that which is measured for the bulk film. This argument is supported by the measurements of the crystal texture of these films, in which grains with two different crystal orientations are seen in films deposited at room temperature.²³ In contrast, only one of these crystal orientations is seen in the films deposited on a lattice-matched MgO substrate, which are characterized by T_c 's and low-temperature dc resistivities that are similar to those of NbTiN films deposited at 400 °C on fused quartz— $T_c = 16$ K and $\rho_{n,20\text{ K}} = 60 \mu\Omega \text{ cm}$ versus the $T_c = 14.4$ K and $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$ that characterize NbTiN films deposited on fused quartz at room temperature.

The observed differences in the dc electrical and structural properties of NbTiN ground planes deposited at room temperature and 400 °C are also consistent with the observed improvement in high-frequency mixer performance that is obtained by the use of films deposited at elevated temperature. In particular, the results seen here show that the previously reported drop in mixer sensitivities at ~ 1 THz is pushed to at least 1.15 THz by the use of films with $T_c = 16$ K and $\rho_{n,20\text{ K}} = 60 \mu\Omega \text{ cm}$, in place of the films with $T_c = 14.4$ K and $\rho_{n,20\text{ K}} = 110 \mu\Omega \text{ cm}$ that were used previously.

CONCLUSIONS

The integration of Nb/Al–AlO_x/Nb SIS junctions with NbTiN-based microstrip tuning circuits yields low-noise mixers in the 0.75–1.15-THz range, with NbTiN/SiO₂/NbTiN microstrip tuning circuits yielding high sensitivities below 0.8–0.85 THz, and NbTiN/SiO₂/Al tuning circuits yielding low-noise performance at frequencies up to 1.15 THz. The maximum frequency at which mixers incorporating NbTiN/SiO₂/Al tuning circuits offer high sensitivity

ties depends upon the conditions under which the NbTiN ground plane is deposited, with room-temperature deposition yielding high sensitivities for $F < 1$ THz and deposition at 400 °C yielding high sensitivities up to 1.15 THz. These results are consistent with both the observed dc electrical characteristics of the NbTiN films (room-temperature deposition yields $T_c = 14.4$ K and $\rho_{n,20\text{ K}} \sim 110 \mu\Omega \text{ cm}$, while deposition at 400 °C yields $T_c = 16$ K and $\rho_{n,20\text{ K}} \sim 60 \mu\Omega \text{ cm}$), and previously reported measurements of the crystalline structure of NbTiN films with similar dc electrical characteristics. The observed drop in performance at $F > 0.8\text{--}0.85$ THz in mixers incorporating NbTiN wiring layers is also consistent with the previously reported measurements of the T_c of NbTiN layers as a function of film thickness, which indicate that the first few tens of nanometer in the film have significantly poorer electrical characteristics (especially T_c) than a 300-nm-thick film.

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