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EuroSys 2025

We are delighted to welcome you to EuroSys 2025, the 20th edition of the European Conference on Computer Systems! We are excited to host EuroSys 2025 in the modern and dynamic city of Rotterdam, Netherlands. This year's EuroSys is very special as it is co-located (for the first time) with the 30th ACM International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2025). We hope you will enjoy an excellent technical program, engaging discussions, and networking opportunities in this vibrant city known for its innovative architecture, bustling port, and rich cultural scene.

Main Conference Program: This year's program features papers that advance the state of the art across a range of systems research topics -- operating systems, virtualization, ML systems, cloud and serverless computing, LLM serving, GPU systems, bug finding, networking, distributed systems, graph and stream processing, fault tolerance, and storage.

EuroSys 2025 followed the example of EuroSys 2024 and implemented a dual-deadline format with separate submission cycles in the spring and in the fall. Across the two deadlines, we received 696 submissions, a 42% increase in the number of submissions over last year. The submissions were reviewed by a program committee of 122 members (chairs excluded). Each valid submission received 3-6 reviews across two rounds of reviewing for a total of 2,571 reviews. Overall, we accepted 85 papers for an acceptance rate of 12.4%. While the acceptance rate is somewhat lower than in previous years, we found that the difference primarily stems from fewer papers passing to the second round of reviews. The acceptance rate in the second round is comparable to prior installments of EuroSys. All accepted papers were assigned a shepherd from the program committee, with acceptance of the final version for publication being conditional on shepherd's approval. Like prior editions, EuroSys 2025 also provided one-shot revisions. Twenty-five papers across the two deadlines (corresponding to 3.6% of all submissions) were offered the opportunity to submit a revised version of the paper to the following EuroSys deadline. Nine of the papers submitted to the spring deadline were revised papers from the EuroSys 2024 fall deadline, and 14 papers submitted to the fall deadline were revised papers

from the EuroSys 2025 spring deadline. All such revised papers embodied significant work that successfully addressed the reviewers' requirements, and thus were finally accepted. EuroSys 2025 offers two parallel sessions (going multi-track for the first time) and ASPLOS 2025 offers four parallel sessions. Attendees can freely attend any of the sessions.

EuroSys 2025 also features a poster session, hosted during the welcome reception. ASPLOS 2025 offers poster sessions across the three main conference days, where attendees can have in-depth discussions with the leading authors of the ASPLOS 2025 papers. To foster social interactions between the ASPLOS and EuroSys communities, the joint program also features a banquet at the SS Rotterdam, a former ocean liner and cruise ship.

Artifact Evaluation (AE): EuroSys 2025 includes an Artifact Evaluation (AE) process, organized by Thaleia Dimitra Doudali (IMDEA Software Institute, Spain), Christian Pinto (IBM Research Europe, Ireland), and Georgios Portokalidis (IMDEA Software Institute, Spain). The AE process was supported by a dedicated committee of 98 volunteers. Authors of accepted papers were invited to submit artifacts related to their research, including source code, systems, datasets, models, test suites, benchmarks, and other materials supporting their contributions. Each submission was rigorously evaluated by two to four AE committee members to assess its availability, functionality, and reproducibility. This year, the AE committee received submissions from 45 accepted EUROSYS papers (15 in the spring round and 30 in the fall round), with a total of 44 submissions to the Artifacts Available badge, 43 submissions to the Artifacts Functional badge and 28 to the Results Reproduced Badges. The evaluation process resulted in an acceptance rate of 100% for Artifacts Available badges (44 granted out of 44 submissions), 98% for Artifacts Functional badges (42 granted out of 43 submissions) and 75% for Results Reproduced badges (21 granted out of 28 submissions).

Shadow PC: In parallel to the main PC, we also organized a Shadow PC. The Shadow PC was chaired by Antonio Barbalace (The University of Edinburgh, Scotland) and Baptiste Lepers (Inria, France). The Shadow PC is an integral part of the EuroSys community-building exercise: serving on a Shadow PC is an excellent opportunity for young systems researchers (PhD students, postdocs, and new faculty members) to gain experience in program committee practices. The 2025 Shadow PC included 71 members evenly split across three continents: a third from Europe, a third from the US and Canada, and a third from Asia. The Shadow PC received 63 papers from the main track. All received papers were reviewed extensively by the Shadow PC members and got between 4 and 5 reviews each. The Shadow PC reached decisions with online discussions on HotCRP (no virtual PC meeting was needed). The Shadow PC finally decided to accept 21 papers, and to revise 7 papers (~44% acceptance ratio including the revisions).

Workshops: In addition to the main program, EuroSys 2025 and ASPLOS 2025 jointly feature 34 workshops and tutorials, coordinated by ASPLOS 2025 workshop chairs Magnus Jahre

(Norwegian University of Science and Technology, Norway) and Alexandra Jimborean (University of Murcia, Italy), and EuroSys 2025 workshop chairs Asterios Katsifodimos (TU Delft, Netherlands) and Soham Chakraborty (TU Delft, Netherlands). Among these, 9 workshops are specific to EuroSys 2025, and 5 workshops are relevant for both EuroSys and ASPLOS communities. These workshops cover a large range of topics reflecting the breadth of the EuroSys community and include well-established workshops as well as one workshop that is being offered for the first time. Continuing from previous years, we have the 5th Workshop on Challenges and Opportunities of Efficient and Performant Storage Systems (CHEOPS), the 8th International Workshop on Edge Systems, Analytics and Networking (EdgeSys), the 20th EuroSys Doctoral Workshop (EuroDW), the 5th Workshop on Machine Learning and Systems (EuroMLSys), the 18th European Workshop on System Security (EuroSec), the 12th Workshop on Principles and Practice of Consistency for Distributed Data (PaPoC), and the 3rd Workshop on SErverless Systems, Applications and MEthodologies (SESAME). New this year is the 1st Workshop on Systems and Architectures for Encrypted AI (SAFE-AI). We look forward to the engaging discussions and innovative ideas these workshops will foster.

The EuroDW Doctoral Workshop holds a special place at EuroSys, serving as an essential platform to foster the growth of young systems researchers. This year's EuroSys Doctoral Workshop is chaired by Haitham Al Hassanieh (EPFL, Switzerland) and Sanidhya Kashyap (EPFL, Switzerland). The 20th EuroDW aims to provide a supportive environment for PhD students to present their work and receive valuable feedback from seasoned professionals and fellow students alike. In addition to technical presentations, the workshop will offer insights and discussions about pursuing a PhD, conducting research, and exploring various career paths. PhD students at any stage of their doctoral journey are welcome to participate and benefit from this enriching experience.

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Hassanieh (EPFL, Switzerland) and Sanidhya Kashyap (EPFL, Switzerland); the travel grant chairs Asia Slowinska (Vrije Universiteit Amsterdam, The Netherlands) and Daniele Cono D'Elia (Sapienza University of Rome, Italy); the publicity chairs Manolis Stamatogiannakis (Vrije Universiteit Amsterdam, The Netherlands) and Vijay Chidambaram (UT Austin, USA); the sponsorship chairs Minsoo Rhu (KAIST, South Korea), Rakesh Kumar (Norwegian University of Science and Technology, Norway), and Benjamin Lee (University of Pennsylvania, USA); the registration chair Enrico Bassetti (Delft University of Technology, The Netherlands); and the AV chair Aleksandar Markovic (Vrije Universiteit Amsterdam, The Netherlands). Additionally, we would like to extend our thanks to the EuroSys Officers and the Steering Committee for their continued support throughout this process.

We would also like to thank the ASPLOS steering committee and organizers for the excellent collaboration that made it possible to co-locate the two conferences for the first time and bring together researchers who work on similar topics.

We also want to acknowledge the generous support by the EuroSys 2025 sponsors: Huawei (diamond sponsor); Amazon Web Services and Microsoft (silver sponsors); AMD, Arm, Google, ING, Intel, MangoBoost, Qualcomm, Red Hat, Rotterdam Partners, Samsung, SK Hynix, VMWare, Western Digital (bronze sponsors).

Finally, we would like to thank the Vrije Universiteit Amsterdam and the Delft University of Technology for their generous support and the Postillion Hotel & Convention Centre WTC Rotterdam for hosting the conference.

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