

Wireless Indoor Climate Sensor

Implementing the Control Unit at Ultra Low Power

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Bachelor of Science Thesis

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For the degree of Bachelor of Science in Electrical Engineering at Delft
University of Technology

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Faculty of Electrical Engineering, Mathematics and Computer Science (EEMCS) · Delft
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The undersigned hereby certify that they have read and recommend to the Faculty of
Electrical Engineering, Mathematics and Computer Science (EEMCS) for acceptance
a thesis entitled

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by

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in partial fulfillment of the requirements for the degree of
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Preface

The Electronic Instrumentation Laboratory department at Delft University of Technology has designed the sensor parts of a multi-sensor IC called the MIST1431[1], which is manufactured by NXP. This is an ultra-low power sensor chip that measures relative humidity, temperature and ambient light intensity. Because of the low power characteristic the chip is extremely suitable for applications where information about the ambient climate is needed, but energy is scarce.

One specific application for the MIST chip is to be part of wireless climate sensor modules that could be used to implement a smart climate system in buildings. Such modules can provide additional information about the climate in a room.

Now that the MIST chip is being finished at NXP, the need arises for a demonstrator for the smart climate control application. We, a total of six students, have been asked to design and build a Wireless Indoor Climate Sensor demonstrator with the MIST chip. This demonstrator should be fully wireless; furthermore, it should be able to take measurements in a timely fashion and send it to a computer. The demonstrator should also be able to work for at least a year without intervention. The complete set of requirements is found in appendix A.

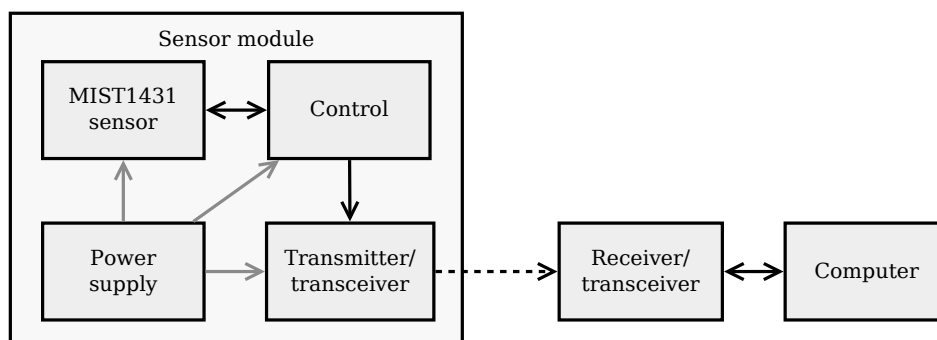


Figure 1: Block diagram of the to be designed system

As the system is to be designed by a group of six students and time is limited, a proper subdivision of design tasks was necessary. An additional constraint to the project was that three theses were to be written. The team was split into three groups of two students each.

We have divided the design into subparts (see figure 1), in order to have smaller and better solvable problems and be able to work together. We have divided the students into three

groups of 2 students each. Wireless communication is presented in [2], energy management and harvesting in [3] and third, control of the demonstrator, is done by us. The reasoning behind this subdivision was that the three tasks were expected to take approximately equal amounts of time to complete. Additionally, they could be completed in parallel as their design is largely independent of one another.

Abstract

With the completion of the MIST1431 multi sensor IC a demonstrator developed by the Electronic Instrumentation Laboratory of Delft University of Technology and NXP, a demonstrator that shows the capabilities of the MIST chip was needed. This thesis describes the implementation of the control unit, which has to control the MIST chip and wireless communication module residing in the demonstrator. Temperature and relative humidity had to be transmitted wirelessly at low power consumption.

A method of analysis and assessment of the different methods and hardware was developed. Then a suitable controller was chosen: the LPC1114. After choosing the controller, a study about the functionalities of the controller was done. Software was designed and written. Finally measurements were taken proving the functionalities of the demonstrator.

Testing showed that the demonstrator is capable of sending its measurements once per second to a computer while using a mean of $50.807\mu A$. The result is that the demonstrator meets its requirements.

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Chapter 1

Introduction

The MIST1431 is an ultra-low-power sensor chip partly made by the Electronic Instrumentation Laboratory, which has recently been finished. For the development of a wireless indoor climate sensor demonstrator, based on the MIST1431 chip and the XBee Series 2 ZigBee module, a control unit is needed. This bachelor thesis presents the process of methodologically selecting a way to control the demonstrator, and the implementation of the subsystem.

The control unit has to communicate with the MIST chip to retrieve the measurement data. The XBee Series 2 module has to be issued to send the measurements to a computer. A power budget of $100\mu\text{A}$ is available for the entire demonstrator.

In chapter 2 the requirements for the subsystem are presented and the resulting research statements. When the requirements are clear, two ways of controlling the demonstrator are evaluated in chapter 3. Chapter 4 will look into methods for power savings. Subsequently, the implementation of the demonstrator's control unit is treated in chapter 5. Finally, measurements are done to validate the system against the program of requirements. This is presented in chapter 6.

Project Description

In this chapter, the requirements for the project are enumerated in section 2-1. Then, in section 2-2 the research statement, which forms the basis for the literature study is composed.

2-1 Requirements

For wireless communication, the XBee Series 2 module[4] was selected[2]. This module interfaces through UART with the control unit. The MIST chip communicates via Serial Peripheral Interface(SPI). The MIST also needs an external clock at 1 MHz. The mean power budget for the entire demonstrator was rated at $100 \mu A$. This was derived from the requirement that the demonstrator should last at least one year without changing a battery. This section, together with the program of requirements (Appendix A) establishes the requirements for the control unit:

- UART interface
- SPI Master interface
- 1 MHz external PWM signal (for MIST clock)
- Two operation modes: normal mode and demo mode. In normal mode, the sample and transmission rate must be at least once per minute. In demo mode, the sample and transmission rate must be at least once per second.
- Deliver the measured data, while retaining the accuracy of the MIST chip used.
- Measure and transmit relative humidity and temperature.
- Power budget of $100 \mu A$

2-2 Research Statement

The requirements lead to the main research statement that is evaluated in this thesis:

Provide a means to send data, collected from the MIST chip, and receive commands, through the wireless communication module, at ultra low power.

This statement is treated in three parts by the following sub statements:

1. *Compare ways to control the wireless communication module and the sensors at ultra low power.*
2. *Find methods to decrease power consumption of the wireless communication module, while maintaining the specified data-rate (see appendix A).*
3. *Find methods to decrease power consumption of the sensors, while maintaining full accuracy.*

Statement 1 is treated in chapter 3, statement 2 and 3 in chapter 4.

Selecting the Control Unit for the Demonstrator

To serve the first research statement, this chapter evaluates two possibilities of controlling the MIST chip via SPI and the XBee module through UART. In section 3-1, FPGAs (field programmable gate arrays) are analyzed. Subsequently, microcontrollers are evaluated in section 3-2. Finally, a method to control the demonstrator is selected.

3-1 FPGA Analysis

FPGAs are generally not considered for low power applications, since FPGAs are less energy efficient than dedicated logic due to the overhead of reconfiguration[5]. They are especially suited for complex algorithms and digital signal processing as they can do a lot of calculations concurrently.

FPGAs usually do not have low power modes as contemporary microcontrollers do. However, efforts (e.g. Xilinx Pika Technology[5]) are made by FPGA producers to catch up with the microcontrollers by adding low power features into FPGA cores.

In regular FPGAs, all transistor blocks are always on and running at full speed, consuming a lot of power. On average, only 20% of the transistors in a FPGA are timing critical[6]. So the power usage can be significantly reduced by putting the non-critical 80% of the transistors blocks in low power mode. Power usage improvements such as these and the vast performance and flexibility of FPGAs make them more and more interesting for mobile applications. However, this flexibility comes with its drawbacks. FPGAs do not offer dedicated SPI and UART peripherals. These functionalities have to be built in separately, bringing in more complexity and increased development time.

FPGAs are still less energy efficient than microcontrollers, and they do not ship with dedicated support for SPI and UART. So custom hardware has to be written in order to use these

protocols. However, time for this bachelor project is limited. Thus, FPGAs are not suitable for this project, since rapid development and low power consumption are essential.

3-2 Microcontroller Analysis

Microcontrollers are fast, cheap and very dynamic. They are dynamic in the sense that the functionality of the microcontroller can be changed with little effort, and without any modification to the physical circuit. This makes microcontrollers an ideal solution to the research statement introduced in section 2-2.

This section describes the comparison of different microcontrollers. First, in section 3-2-1, the method used to compare the microcontrollers is presented. Then in section 3-2-2 microcontrollers are found and compared, using the method of comparison described in section 3-2-1.

3-2-1 Method of Comparison

Section 2-1 defined the requirements for the subsystem: a SPI interface is needed for communication with the MIST chip and UART is needed for the XBee module. Most modern microcontrollers implement an UART and SPI peripheral. Therefore, the low-power criterion together with ease of development will be the most important criteria.

This subsection starts with making a few assumptions on the microcontrollers to be compared. Then it gives the method of power consumption estimation. Finally it will list the other criteria, including ease of development.

No benchmarks (a test to determine the calculation power of a CPU) have been found with the calculation power of all the CPU's. In fact, only ARM states a Dhrystone [7, 8] result for its microcontrollers. Therefore all CPU's are assumed to be of equal calculation power.

Assuming that all CPU's are equal in processing power makes it easier to do a power consumption prediction. The microcontroller has to wait for either the ZigBee module or the MIST chip most of the time. In between a lot of data copying, bitwise operations and branches have to be done.

With the assumption of equal processing power, these operations can be modeled as a series of clock cycles. Also, the waiting for either the ZigBee module or the MIST chip can be modeled as a series of clock cycles. Note that all operations can only be modeled as clock cycles because the clock frequency is set to 1 Mhz. Taking the highest clock frequency available will result in more power consumption on behalf of the CPU, since it has to wait most of the time.

Measurements have to be taken each second or each minute, depending on the operation mode of the demonstrator (see appendix A). Also the measurements have to be sent at the same rate for both modes. This means that the mode of operation is periodic, thus in order to predict the power consumption a single period of power consumption is needed. Each period consists of 4 stages:

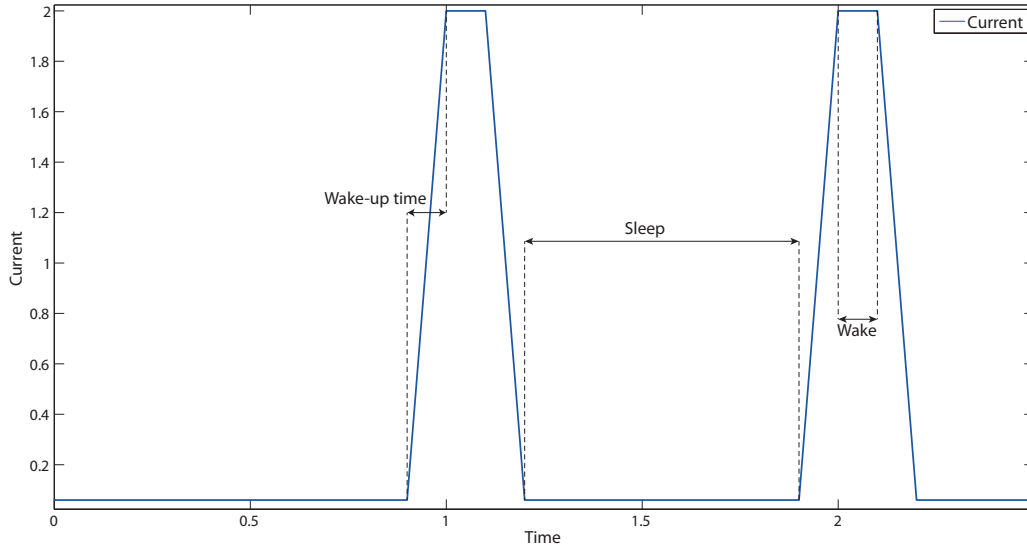


Figure 3-1: Power Modes Consumption Estimation

- Waking up
- Taking measurement and sending
- Going to sleep
- Sleeping until 60, or 1, seconds have passed

A graphic display of these stages can be found in figure 3-1. With this model, formulas to calculate the power consumption of each period can now be made:

$$C_{on_period} = (T_{measurement} + T_{send}) * A_{on} * 1Mhz / F_{on} = T_{on} * A_{on} * 1Mhz / F_{on} \quad (3-1)$$

Where C_{on_period} is the power consumption of one period while the microcontroller is active, $T_{measurement} + T_{send} = T_{on}$ is the time duration the microcontroller is turned on each period, A_{on} is the on-current given by the datasheet and F_{on} is the frequency associated with the on current. The multiplication with 1Mhz divided by the frequency is done to calculate the power consumption of the microcontroller with a 1 Mhz clock.

$$C_{wake-up} = T_{wake-up} * 1/2 * (A_{on} * 1Mhz / F_{on} + A_{sleep}) \quad (3-2)$$

Where $T_{wake-up}$ is the time required to get out of the deep sleep mode and A_{sleep} is the power consumption of the microcontroller in deep sleep mode.

$$C_{go-to-sleep} = A_{wake-up} \quad (3-3)$$

$C_{wake-up}$ and $C_{go-to-sleep}$ is the power needed to either to get out of deep sleep or into deep sleep mode, both are assumed equal.

$$C_{sleep} = (T_{period} - T_{on} - 2 * T_{wake-up}) * A_{sleep} \quad (3-4)$$

Where C_{sleep} is the deep sleep power consumption per period and T_{period} is the period time, which is 60s.

$$C_{period} = (T_{on} + T_{wake-up}) * (A_{on} * 1Mhz / F_{on} - A_{sleep}) + T_{period} * A_{sleep} \quad (3-5)$$

Where C_{period} is the power consumption per period.

During the measurements and transmitting phase the microcontroller is turned on, thus the power consumption of the microcontroller during this phase is the on state power consumption given by the datasheet. Since the microcontroller will operate at 1Mhz the on-current is multiplied by 1 Mhz and divided by the associated frequency (see equation 3-1).

Now that a model for the power consumption estimation of the microcontroller is found, other criteria can be assessed. Although low power consumption is the most important criterion it is also important that the time to development does not exceed the time restrictions (Time to development includes properties like: good compiler availability, good documentation a development board and available on-hands experience.). If the Electronic Instrumentation Laboratory wants to reuse the demonstrator some time in the future it is also preferred that they have some familiarity with the microcontroller. This will make it easier for them to work with the demonstrator and it also means that they already have the tools needed. The last factor included in the comparison is support (specifically in the form of fora and helpdesk). Support is a handy tool to help solve problems, which is what makes it different from time to development. These last criteria (the criteria excluding the power consumption) are not measurable, that is why a relative weight factor is attributed to them.

3-2-2 Microcontroller Comparison

In this section the microcontrollers are compared. The microcontrollers were selected by searching for microcontrollers which the manufacturer has labeled as ultra low power. This search has led to the microcontrollers listed in table 3-1. The data required for the power consumption estimation are found in the datasheets [9, 10, 11, 8, 12, 13, 14, 15, 16, 17] see table 3-1). It must be noted that some wake-up times were missing in the datasheets, these have been set to zero.

Figure 3-2 shows that the power consumption per clock cycle of the microcontrollers is the most significant property. The wake-up time has no major influence on the average power consumption, which was expected since it only has to wake-up once every minute. The sleep current is also negligible because it is small compared to the on state power consumption.

Applying the other criteria was done for the MSP430, the LPC1114, the c8051F9806 and the cc430 (see table 3-2). These microcontrollers have a similar power consumption, while the rest of the microcontroller have a higher power consumption. Since the power consumption criterion is still the most important one, the choice will go to one of these more energy efficient

Table 3-1: Microcontroller Power Consumption

	on current [mA]	sleep current [uA]	wake up time [us]	frequency [MHz]
PIC16LF1823[9]	2.2	0.03	0	8
MSP430[10]	3.6	1.3	150	25
ATtiny1634[11]	0.23	40	4	1
LCP11AXX[8]	7	2	0	50
ATiny2313A[12]	0.2	4	6	1
Si1004[13]	0.18	0.3	2	1
c8051F980[14]	0.15	0.3	2	1
cc430[15]	3.1	1.3	150	25
ATmega128RFA1[16]	3.7	1.2	0.38	16
efm32g210[17]	5.67	0.59	2	32

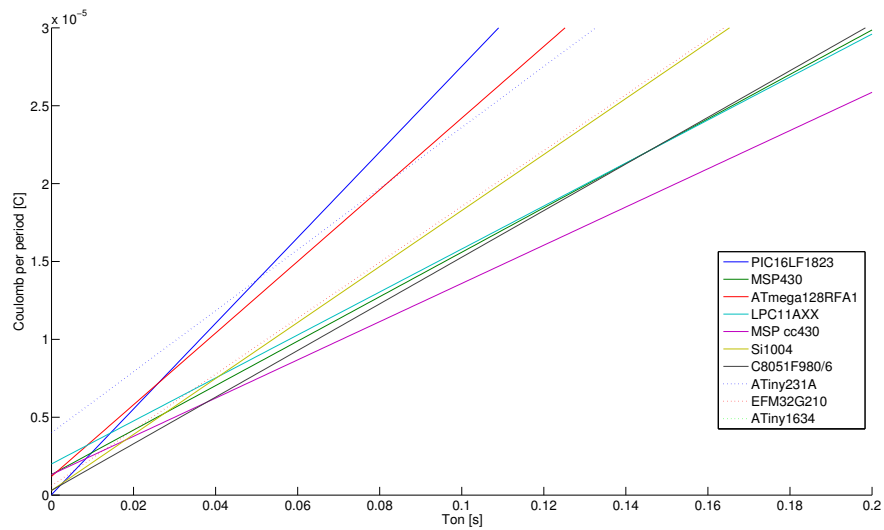


Figure 3-2: Microcontroller Power Estimation

Table 3-2: Microcontroller Comparison Table

	PIC16LF1823	MSP430	ATiny1634	LPC11AXX	ATiny2313A	Si1004	c8051F9806	cc430	ATmega128RFA1	efm32g210
Power Consumption	-	+	-	+	-	-	++	++	-	-
Development Time		-		++			+	-		
Support		+		++			+-	+-		
Familiarity		+		++			+-	+		

microcontrollers.

The MSP430 and cc430 do not come with a freely available compiler, that is why development time has been judged negatively. A support site for the MSP430 has been set up, however the cc430 (which is an MSP430 with a wireless communication module) does not have much support in the form of libraries or example code. The LPC11AXX has an ARM cortex-M0 core, therefore a lot of coding examples and free compilers are available. Also, the LPC11AXX has a cheap development board which comes with IDE, compiler and a support site. The c8051F9806 has a 8051 core, which is old and long-lasting core and also has a free compiler. Because of it's long life there is a lot to be found on different internet fora.

From table 3-2 follows that the LPC11AX chip is the most favorable. The MSP430 is slightly more power efficient, but decisive was the fact that the LPC1114 is already used on the Electronic Instrumentation Laboratory department and has a development board with free unrestricted compiler, IDE and support site. So LPC1114 was chosen to be the heart of the demonstrator.

Power Saving Techniques

This chapter evaluates different methods for saving power and treats the second and third research statement given in section 2-2. The program of requirements states that the demonstrator should last at least one year on a battery in normal mode. The sensors should do one measurement per minute and transmit the data at least once per minute over the wireless link.

Since a measurement and transmitting combined takes about a 100 milliseconds, the demonstrator will be idle most of the time. Thus, there is a lot to gain in minimizing power consumption when the demonstrator is waiting. This can be achieved by literally turning off certain parts of the device.

The LPC1114 microcontroller has several low power modes built in that provide this functionality. In section 4-1 these low power modes are investigated. Then the sleep modes supported by the XBee module are described in section 4-2. Section 4-3 evaluates the power saving by the MIST chip. Efficient package handling is described in section 4-4 and buffer optimization in section 4-5.

4-1 LPC1114 Sleep Modes

The LPC1114 supports four different levels of power modes[18]:

Active Mode is the normal, non-power-saving mode. The Cortex-M0 core and the memory are on and are clocked by the system clock. Peripherals are clocked by the system clock or a dedicated peripheral clock.

Sleep Mode is the first level power saving mode. The system clock is stopped, so the Cortex-M0 core is off as well as the memory, the related controllers and the internal buses. This diminishes the dynamic power consumption. Peripherals however can still be used if selected in the SYSAHBCLKCTRL register. Also, the processor state and registers are maintained. Waking up can be achieved by interrupts generated by peripherals.

Deep Sleep Mode powers down all analog blocks in addition to the Sleep Mode power savings. Only the brown out detector and the watchdog can be selected to remain active. Waking up from deep sleep mode can be done either by an external signal, brown out reset or by a watchdog timer reset.

Deep Power Down Mode shuts off the entire chip. Only the data in five general purpose registers is saved. Waking up from deep power down mode can only be achieved by pulling the WAKEUP pin low.

The demonstrator has to transmit the measurements periodically. Also there is no external device to wake up the LPC1114. That means Deep Power Down Mode cannot be used. This leaves Sleep Mode and Deep Sleep Mode. The latter can be used if no peripherals are needed, which is the case when measurements are done and sent over the ZigBee connection. Then the LPC has to wait until it can start the measurement cycle again. While waiting nothing has to be done, so the peripherals can be powered off.

Sleep Mode can be used if peripherals need to be powered. This is the case when the LPC1114 has issued the MIST chip to do a measurement. The MIST chip needs a tenth of a second (see equation 4-1) to finish a measurement. In the meantime, the LPC can go into Sleep Mode while maintaining the clock signal for the MIST chip. Sleep Mode can also be used while sending packets through UART. Sending a character at 9600 BAUD takes equals 1250 CPU instructions at a clock frequency of 12 MHz. That is 1250 instructions of potential sleep time.

Note that it is required to implement asynchronous functionality for the UART and MIST(SPI) (i.e. using interrupts instead of polling) to be able to go into Sleep Mode while the demonstrator is idle.

4-2 XBee Sleep Modes

The XBee series 2 module also has two sleep modes for power saving[4].

Pin/Host Controlled Sleep is the first low power mode. In this configuration, sleep mode is entered by asserting (logical high) the Sleep_RQ pin. When the XBee is signaled to go to sleep it will finish any transmit or receive operation before entering a low power state. When the same pin is de-asserted (pulled to ground) the module will wake up again.

Cyclic Sleep sets an interval for the XBee module to wake up periodically to check for or to send RF data.

The LPC will be controlling the state of the demonstrator. In order for the LPC to have more control, the XBee module will be configured in Pin/Host Controlled Sleep. The LPC will issue the XBee module when it is time to transmit or sleep.

At least each half minute the ZigBee module needs to poll the coordinator in order to not lose its connection. This means that in demonstrator mode at least the LPC and the ZigBee module need to wake up. If the program of requirements permits it, it will be interesting to see if in demonstrator mode the demonstrator can send three measurements per minute.

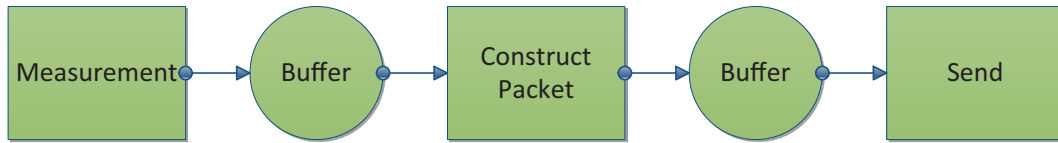


Figure 4-1: Circular Buffer Implementation

4-3 MIST power saving

So far power saving features for the control part and the wireless communication part of the demonstrator have been assessed. Contributing for the sensor part, the MIST chip offers a deep power down mode to save power[1]. This mode is entered by pulling the RESETn pin to ground. When in deep power down mode the MIST consumes $< 0.3nA$. De-asserting the RESETn pin results in rebooting of the MIST. After $1.5ms$ the chip is ready to accept commands again. A temperature measurement takes, at full resolution (12 bit), 100 milliseconds. Humidity measurements take about 18 milliseconds, which results in:

$$T_m = T_{wake_up} + T_{temperature} + T_{humidity} = 1.5 + 100 + 18.0 = 119.5ms \quad (4-1)$$

These waiting times are ideal for sleep mode, since deep sleep mode cannot sustain the peripherals. Making the MIST code perform asynchronously will enable the microcontroller to also perform other tasks.

4-4 Efficient Package Handling

The demonstrator would perform optimally if 10 measurements are sent in each packet. However the program of requirements states that the demonstrator has to send at least one sample each minute. In order to keep the power consumption to a minimum it was decided that the demonstrator sends one measurement each minute in normal mode.

The program of requirements requires the demonstrator to send at least one measurement each second in demo mode. Since measurements take a little over 119.5 milliseconds, accounting for microcontroller times also, it is technically possible to send 4 measurements each second. However this would not increase functionality while decreasing performance by consuming more power. Therefore it is decided that in demonstrator mode one measurement is sent each second.

4-5 Buffering Optimization

Circular buffers are often used in low level applications. Circular buffers are fast first in first out streams. Therefore they seem ideal for use in the demonstrator.

However it was decided that the demonstrator is not going to use circular buffers. Streaming would be very nice to have during the sending of packets or the storing and translating



Figure 4-2: Buffer Handle Implementation

of sensor data. But as can be seen in figure 4-1 it would require more data copying than necessarily needed. Since sending data is only done once every time the controller is awake no stream is needed.

It was decided that the demonstrator needs to have a pool of buffers. If some part of the code is in need of a buffer it can request a buffer from the pool. Passing data from one point to another would be done by passing the buffer handle (a pointer to the first address of the buffer along with the size of data in the buffer). Passing along the buffer handle also means passing along ownership of the buffer. Since the buffer pool has an endless amount of buffers it is also required that buffers are released (returned to the buffer pool) after usage.

The buffer pool with buffer handles are an excellent solution for this application, however when large data streams have to be handled, it is recommendable to use circular buffers.

Control Unit Implementation

This chapter describes how the control unit of the demonstrator has been implemented. Chapter 3 described the reasons to go with the LPC1114 as control unit. Chapter 4 explained how energy can be saved by the microcontroller, the MIST chip and the XBee module. The software designed for the LPC will be explained top-down. First the high level software design will be explained in section 5-1. Then the lower level software is described in sections 5-2 to 5-4. Section 5-5 describes the data protocol used for transmitting the measurement data. All corresponding source code can be found in Appendix B

5-1 High Level Software Design

On the highest level, the software design consists of four parts: WICS Main Loop, Power, ZigBee and MIST. This is illustrated in figure 5-1. The main loop controls the MIST driver to acquire the measurement data, it uses the ZigBee driver to make the XBee module transmit data and it uses the Power functions to manage sleep and deep sleep mode in order to save power. The main loop also makes it possible to switch between normal mode and demonstrator mode. Switching can be done via a wireless message over ZigBee. In addition to the two modes, the sample rate and transmitting rate are configurable individually as well.

The main loop cycle is illustrated in figure 5-2. It starts with waking up from deep sleep and turning on the XBee module, which checks for incoming messages. If there are any, they will be processed. Then, the measurement process starts, and concurrently any available data will be transmitted (data from earlier cycles). After that, XBee will be turned off and the LPC goes into deep sleep.

5-2 MIST Implementation

The MIST code is responsible for dealing with the MIST chip. The MIST chip communicates through SPI with the LPC(see figure 5-3). In addition, the MIST chip also needs an external

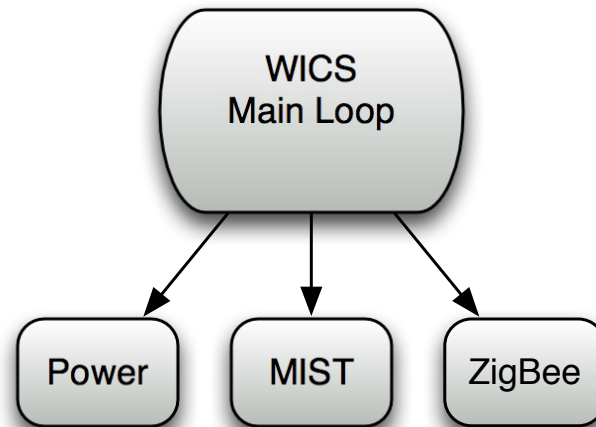


Figure 5-1: High level software design

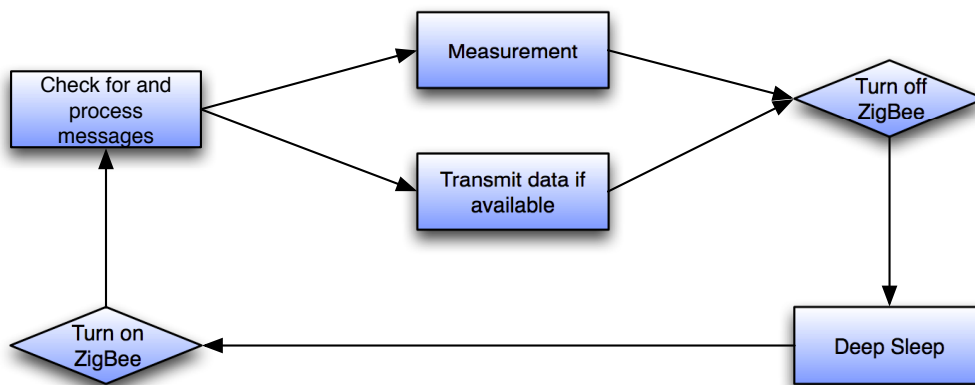


Figure 5-2: High level flow chart

1MHz clock, which is also generated by the LPC. When measurement data is needed, MIST follows the steps as illustrated in figure 5-4. First the MIST is enabled. This means the clock is generated on a PWM pin. Then the temperature measurement is started. Since this measurements takes over 100ms the LPC goes into sleep mode during this measurement. The 1MHz clock is still generated in sleep mode. When the temperature measurement is finished, the data is retrieved and the humidity measurement is started. During this measurement sleep mode is invoked as well. After retrieving the humidity data the MIST is disabled.

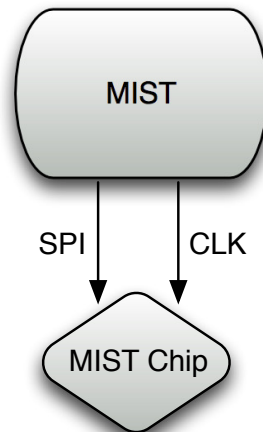


Figure 5-3: MIST topology

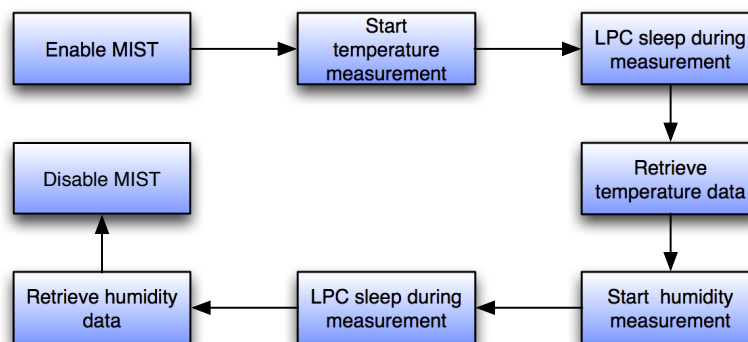


Figure 5-4: MIST Driver flow chart

5-3 ZigBee Implementation

ZigBee is the driver for the XBee module. It realizes the communication with the XBee module via the LPC UART peripheral, see figure 5-5. All processes in the ZigBee driver run asynchronously. So they work with interrupt requests in stead of polling. When ZigBee receives data to send, first a buffer is allocated. Then the ZigBee packet is constructed and stored in the buffer. Then a transmit is requested for the data in the buffer, see figure 5-6.

The ZigBee packet (see figure 5-7) is constructed around every data set to be sent. Every packet starts with 0x7E as start byte, followed by two bytes that indicate the packet size. Then, an arbitrary ID byte can be assigned just before the actual data is inserted. Finally a checksum is calculated over the entire packet except the start byte. Also all bytes that happen to match a XBee control byte are escaped[4].

The transmit routine waits for a transmit request. At a request it will issue the UART to send

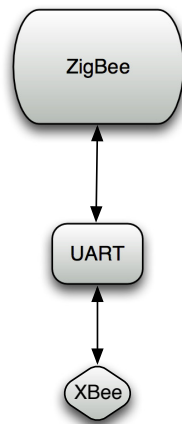


Figure 5-5: ZigBee Topology

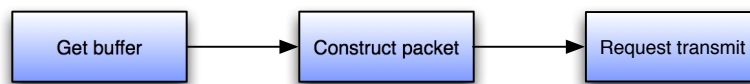


Figure 5-6: ZigBee preparation for transmit

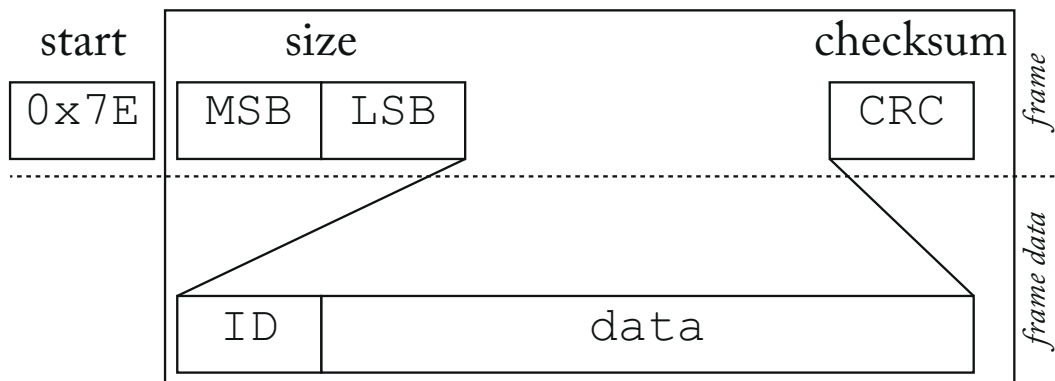


Figure 5-7: ZigBee Packet

the first byte. Then it checks if there are any bytes left to send. This is illustrated in figure 5-8.

The XBee module can also receive data. If data is received, UART triggers an interrupt in ZigBee, which will store the data byte for byte in a buffer. When the entire packet is received, the buffer is sent to the handler, which will decode the packet. See figure 5-9 for the corresponding flow chart.

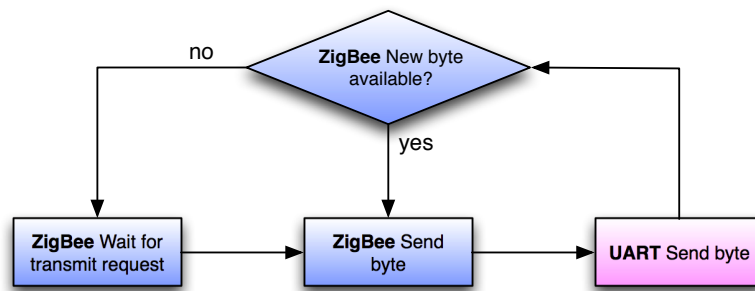


Figure 5-8: ZigBee transmit flow

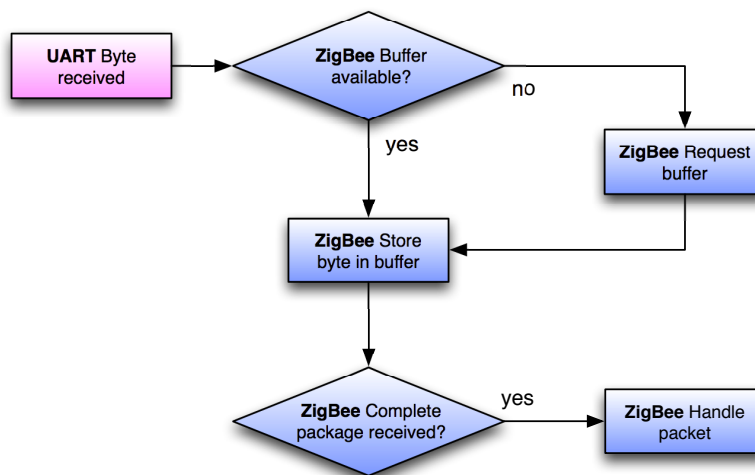


Figure 5-9: ZigBee receive flow

5-4 Power Implementation

As explained in chapter 4 the LPC needs to be able to enter Sleep Mode and Deep Sleep Mode. The Power part of the microcontroller source code has three functions: One function to initialize the watchdog oscillator and the counter needed to wake up from sleep and two functions to enter either sleep or deep sleep mode.

The watchdog oscillator runs at the lowest possible frequency, 0.5/64MHz in which 64 is the frequency divider and 0.5 is the lowest possible RC oscillator frequency.

Waking up from sleep mode is done through either a timer interrupt or any other interrupt that occurs during sleep. For the timer interrupt, timer 32B1 is used driven by the watchdog oscillator. The timer generates this interrupt when the timer value matches the value stored in the timer match register. This register is used to control the time the LPC is in sleep mode.

Deep sleep mode uses timer 32B0 to drive an external pin to invoke the start logic which in its turn wakes up the LPC. The timer is driven by the watchdog oscillator, which is the only oscillator running in deep sleep mode.

5-5 WICS data protocol

The measurements are sent to a pc in a WICS data frame. The data frame can hold a maximum of 10 measurements. This is because a XBee packet holds a maximum of 72 bytes of user data and one packet of measurement data is 7 bytes. Figure 5-10 shows the structure of the data frame. Sequence ID indicates the packets number in the packet sequence. Interval is the measurement interval in seconds. The status flags indicate MIST sensor errors and harvesting errors and battery status.

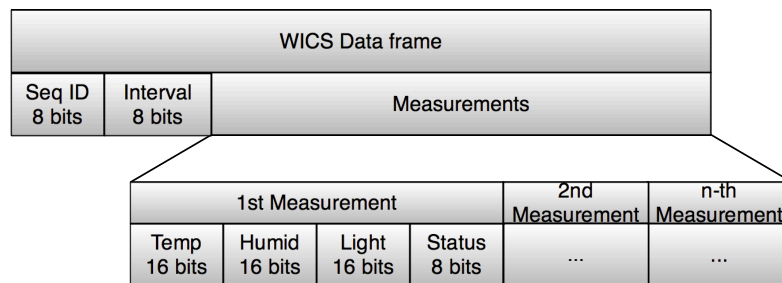


Figure 5-10: WICS Data Frame

Chapter 6

Measurements

This chapter explains how testing of the demonstrator and subsystems is done in section 6-1. Then section 6-2 presents and evaluates the test data. First power consumption will be looked into. Then the data rate will be checked against the specifications in the program of requirements (see appendix A) in section 6-3.

The demonstrator supports 2 different operation modes: Normal mode and Demo mode. In Normal mode measurements and data transmissions should be done once per minute. In Demo mode once per second.

6-1 Measurement method

The measurements were done with the Agilent 6613C power supply [19], Agilent 34401A digital multimeter [20] and the DSO6034A scope [21]. The digital multimeter was used for measuring the deep sleep mode current of the circuit (see figure 6-1). This was done because the scopes have an offset, which had to be compensated for. The measurements taken with the scope (see figure 6-2) start and end with the demonstrator in sleep mode, these parts are

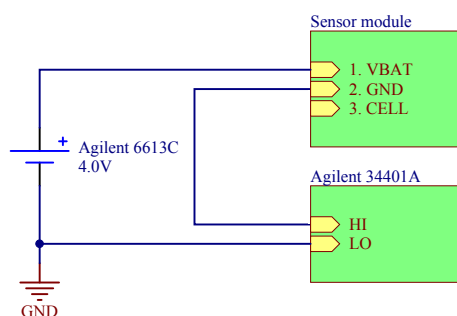


Figure 6-1: Average Sleep Mode Current Measurement Setup

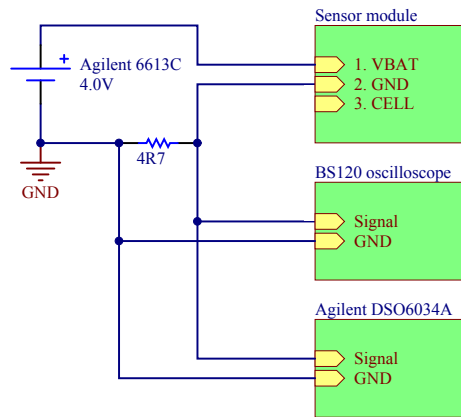


Figure 6-2: Average On Mode Current Measurement Setup

calibrated to zero. Then the measurements are transformed to a current, using Ohm's law: $I = U/R$, and finally the sleep current is added.

The measurements are taken this way because the digital multimeter is very accurate at measuring the demonstrator current, but not at high sample frequencies. The scope on the other hand can sample at high enough frequencies but has an offset. Also the scope has to measure the current by measuring the voltage over a shunt resistor, and thus loses some accuracy.

6-2 Power Consumption

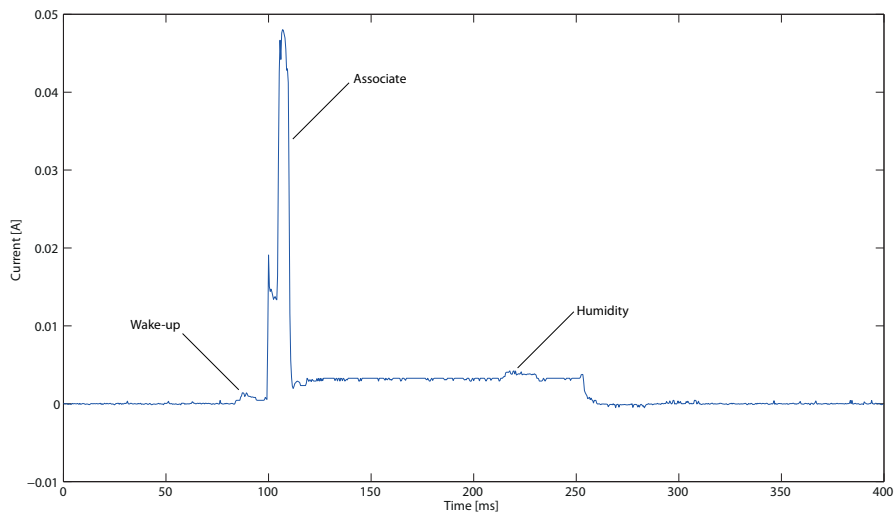


Figure 6-3: Association Period Current Consumption Graph

In figure 6-3 a measurement period can be seen. During these measurement periods the XBee module is only turned on to keep its association (connection) with the coordinator. This is done because after about 28 seconds of sleep time the ZigBee module will lose its association. Since the microcontroller is turned on it might as well gather temperature and humidity samples. Note that just before the ZigBee module power consumption peak occurs, a small peak can be seen, which is the microcontroller waking up. Also at the end of the period the MIST is done with it's temperature measurement, a small peak can be seen which is the microcontroller waking up to setup a humidity measurement.

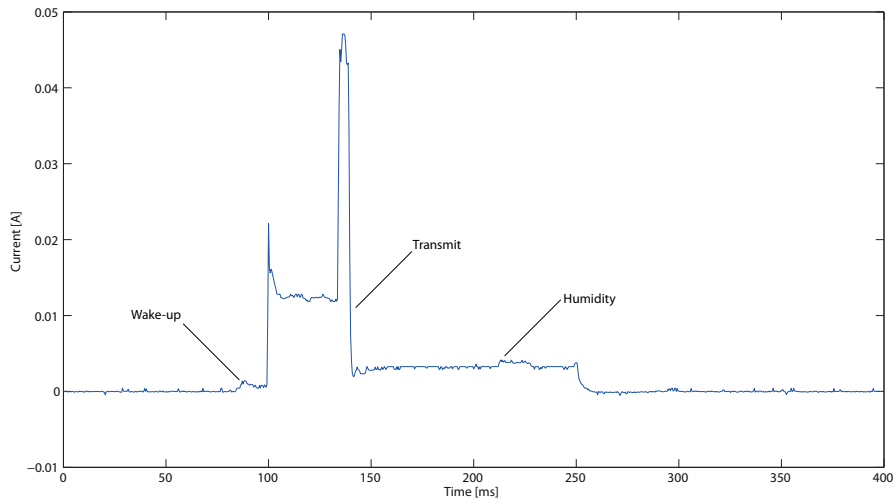


Figure 6-4: Send Period Current Consumption Graph

Figure 6-4 depicts a period in which the ZigBee module sends its data and the MIST takes a measurement. As can be seen the ZigBee is turned on longer than the duration of a measurement period.

Figure 6-5 shows the power consumption when one or more resends are necessary. The demonstrator uses considerably more power than without packet resending, because the ZigBee module is on for a longer time.

Finally all the sampled periods are averaged out and presented in figure 6-6. A lot of noise has been canceled by averaging the samples. But it is still possible to see the peak when the XBee module tries to keep associated, the transmit peak and the humidity measurement bump.

$$A_{average} = A_{sleep} + T_{measurement} / N_{samples} * k_{periods/minute} \sum_{i=1}^{N_{samples}} A_i = 6.027\mu + 44.780\mu = 50.807\mu A \quad (6-1)$$

Equation 6-1 states the average power consumption. A_{sleep} is the average deep sleep current as measured with fig 6-1. $T_{measurement}$ is the measurement duration which is 400ms. $N_{samples}$ is the amount of samples taken and A_i is the i^{th} sample. $k_{periods/minute}$ is the amount of mea-

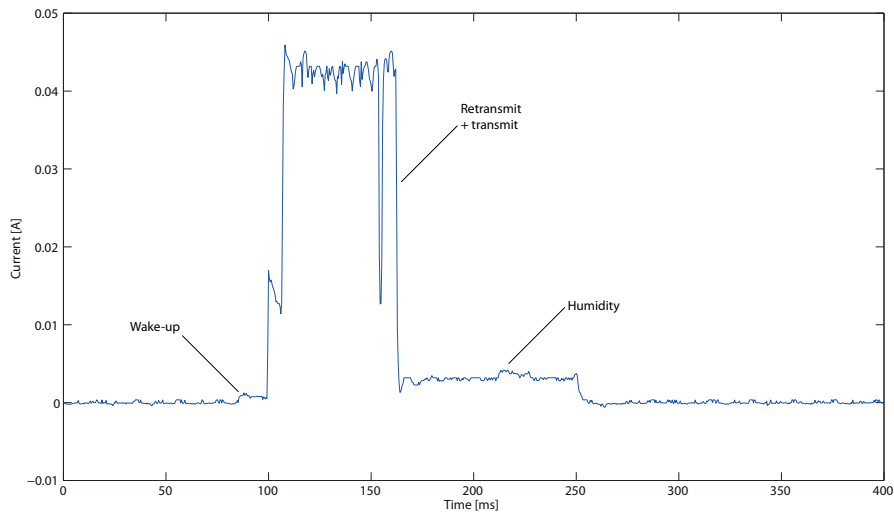


Figure 6-5: Resend Period Current Consumption Graph

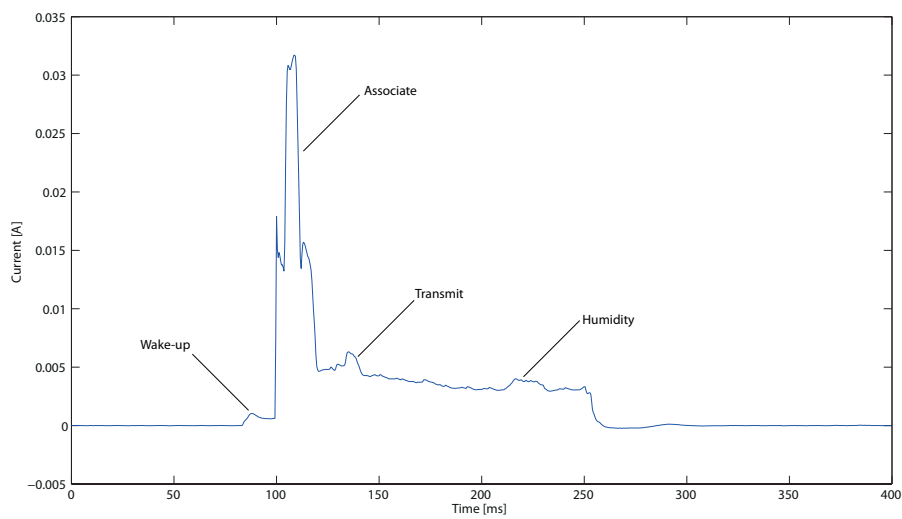


Figure 6-6: Average Period Current Consumption Graph

measurements taken per minute which is: $3/60$. The average current is lower than the maximum power budget of the entire demonstrator, which was $100\mu A$. Even with 3 measurements a minute the average power consumption is only $52\mu A$, which is slightly more than half the power budget.

6-3 Data Rate

After extensive measuring it appeared that sometimes the ZigBee module is not able to deliver the packet in one try and on the next send period it has to send both the old data and the new. This means that the ZigBee module does not have the time during the measurement periods to both keep its association and send its data. This can be solved by allowing the ZigBee to be awake for just a little longer during a measurement period. However, because the ZigBee is not able to send the data in one go means that there is something wrong. The problem seems to be caused by a collapsing voltage supply. After extensive measuring it appears that in 5.2% of all cases a retransmit is necessary. The packet loss was 0%

Receiving data on the demonstrator(sent from a computer) is very unreliable. Using the demonstrator with the energy harvester a packet loss of 100% was measured. Using the demonstrator without the energy harvester resulted in very unreliable receiving. Packets could arrive minutes later, not at all, and even multiple times. The reason for the unreliability is probably the supply voltage drop, due to the power consumption of the ZigBee in combination with the voltage regulator.

When watching the data sent from the ZigBee module to the LPC it became apparent that the ZigBee resets itself due to the voltage drop out. Since the ZigBee module polls its coordinator for packets it is sometimes so that the ZigBee already has the packet but hasn't successfully send and acknowledgement back to the coordinator. This is why sometimes a single packet is received multiple times.

The data rate is configurable in the code. However, packets losses may affect the data rate negatively because of retransmits. The one transmit per second requirement for the demo mode was achieved. It is possible to transmit more frequently by adjusting the time the LPC goes into deep sleep mode every cycle.

Chapter 7

Conclusion

This chapter evaluates the measurement results in section 7-1. Then it continues to present recommendations on microcontroller comparison in section 7-2.

7-1 Conclusion

The purpose of this project was to find a method to control a wireless indoor climate sensor with a maximum mean power usage of $100 \mu A$. First a way had to be found to control the MIST1431 multi sensor IC and the XBee Series 2 wireless communication module chosen in [2]. FPGAs proved not appropriate because of the high energy usage and long development time needed. Subsequently microcontrollers were compared on power consumption, performance and usability. The NXP LPC1114 microcontroller turned out the most suitable. Its low power characteristic and availability of on-hand experience were decisive. To reach the low power goal, the LPC1114 can be put into Sleep Mode and Deep Sleep Mode when it is idle. The LPC also controls the operation mode of the XBee module and the MIST chip by putting them in low power modes or by waking them up.

These low power modes were used to keep the control unit of the demonstrator within its power budget of $100 \mu A$. The measurement results show that the power consumption of the demonstrator has been kept well within its power budget, even when packets had to be resend.

7-2 Recommendations

Comparing the microcontrollers was not a straightforward task. A lot of problems came up, most of them had something to do with the power consumption estimation and subsequently with determining the CPU calculation power.

The modeling of power consumption of embedded systems is an upcoming problem. With

the need for low power microcontrollers comes the need for accurate power consumption prediction. A set of cycle-accurate tools have been developed to accurately predict the power consumption of microcontrollers. These tools simulate the processor with the code running on the processor and can predict the power consumption within 3-8%, depending on the simulator [22, 23]. The downside of these simulators is that they need a very accurate model of the processor and the code which will be running on the processor. Extensive testing will be needed. Therefore the usage of cycle-accurate simulators was not an option within a narrow time restriction. However no other tool has been found which predicts the power consumption of the CPU or microcontroller.

A new method to predict/compare the power consumption of microcontrollers is needed. Ideally at this moment a weight factor is found specifying the calculation power of the different CPUs. With these weight factors a CPU specific clock cycle can be found and the power consumption can be estimated. Although there are more than enough benchmarks [24] no benchmark could be found for the CPUs, only ARM gives the Dhrystone benchmark.

The datasheets of the manufacturers do not seem reliable, not only do they not state in what condition measurements are taken, measurements sometimes contradict each other. So a solution is needed for microcontroller benchmark.

Some recommendations on behalf of the ZigBee must also be made. Sometimes the ZigBee module needs to resend a packet to the coordinator. However, it is not able to find the time during an measurement period, in which the ZigBee is turned on only to keep its association. The ZigBee is probably turned off too soon, waiting much longer is not really desirable since the microcontroller is waiting. Therefore it is better to include a time-out in the ZigBee. Then the ZigBee could also be turned off asynchronously after a time-out has occurred.

One requirement was that our implementation could not affect the accuracy of the MIST chip measurements. This has not been tested. Since the XBee module is transmitting at the same time the MIST chip takes samples, the electromagnetic waves due to the ZigBee could affect the samples. To be sure that this requirement is met, this should be tested.

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Appendix A

Program of Requirements

Note: the following appendix applies to the complete sensor system, not just the energy system presented in this thesis. See Preface for more information.

The required product is a wireless indoor climate sensor. It is an autonomous sensor that transmits several parameters about its environment wirelessly. The product will be used to demonstrate a set of energy efficient sensors, which are developed at the Electronic Instrumentation department at Delft University of Technology. This document lists all requirements and wishes as stated by the client.

In the requirements below, the following definitions apply.

1. *Sensor* - the sensor module to be designed.
2. *Host* - the system which the sensor communicates its data to.
3. *Sampling rate* - the rate at which the sensor takes sensor value samples.
4. *Transmission rate* - the rate at which the sensor transmits (previously recorded) sensor data.

A-1 Usage Requirements

- [1.1] The product must measure at least temperature and humidity. More measured quantities are encouraged.
- [1.2] All communications between the sensor and the host must be done wirelessly.
- [1.3] The product must function autonomously in terms of energy supply.
- [1.4] If a battery is used, the user should be notified when the battery needs to be replaced.
- [1.5] The measured quantities should be visible on a computer.

A-2 Requirements according to the ecological situation of the system's environment

- [2.1] The product must function indoors.
- [2.2] The transmitter frequency, bandwidth and output power must fall within Dutch regulations.
- [2.3] The product must be non-intrusive within its operating environment, i.e. it should not draw attention to itself.

A-3 System Requirements

- [3.1] If a battery is used, the sensor must operate without battery replacement for at least a year. This requirement assumes the sensor is run in normal (not demo) mode.
- [3.2] The range for wireless communication must be at least 5 meters.
- [3.3] The sensor must have at least two operating modes in terms of sampling rate and transmission rate: a demo mode and a normal mode. In demo mode, the sample and transmission rate must be at least once per second. In normal mode, the sample and transmission rate must be at least once per minute.
- [3.4] The operating mode must at least be selectable using a jumper or switch on the sensor. Being able to set the operating mode wirelessly is a nice to have. Being able to set more sampling and transmission rates is also a nice to have.
- [3.5] Having the possibility to set minima and maxima for the measured quantities is a nice to have. If such a limit were to be exceeded, the sensor should wirelessly transmit the current sensor data regardless of transmission rate.
- [3.6] To measure the temperature and humidity, the sensor chip developed by the Electronic Instrumentation department at Delft University of Technology and produced by NXP must be used.
- [3.7] The chip mentioned above must be visible and influenceable during a demonstration. For instance, it must be possible to breathe on or touch the sensor to demonstrate that the measured quantities indeed change on the screen in such a case.
- [3.8] The system must deliver the measured data in such a way that does not reduce the accuracy of the sensor chip(s) used.

A-4 Installation Requirements

- [4.1] It must be possible to install the product without changes to the environment.

[4.2] The installation must be as simple as inserting a battery and installing some software on a computer. In other words, it should be "Plug & Play". It is acceptable if something like a USB dongle is required for communications.

[4.3] Replacing a battery must be possible within a minute.

A-5 Project Requirements

[5.1] All software written for this product must be well documented.

[5.2] All hardware designed for this product (circuits and circuit board layout) must be well documented.

[5.3] Writing platform independent software is encouraged. The "platform" is defined here as being the operating system for PC based software and the microcontroller (architecture) used for hardware based software/firmware.

Appendix B

Source Code

B-1 ZigBee

Listing B.1: ZigBee.h

```
1 /*
2  * ZigBee.h
3  *
4  * Created on: 4 mei 2012
5  * Author: Jan Angevare
6  */
7
8 #ifndef ZIGBEE_H_INCLUDED
9 #define ZIGBEE_H_INCLUDED
10
11 #include "UART.h"
12 #include "ZigBee_buffer.h"
13 #include "ZigBee_constructor.h"
14 #include "ZigBee_receiver.h"
15 #include "ZigBee_sender.h"
16 #include "ZigBee_translator.h"
17 #include "power_modes.h"
18
19 // ZigBee initialize will initialize the ZigBee
20 // And all it's subparts plus the UART
21 void ZigBee_init(void);
22 // Send a piece of data to the Coordinator
23 // This sending will be done asynchronously
24 void ZigBee_send(char* data, int length);
25
26 // This function returns true if a message was ready
27 // and if a message was returned. If the return value
28 // is false no message was returned, otherwise a subsequent
29 // call must be made to ZigBee_done_reading_new_message
30 // to free up the buffer
31 int ZigBee_check_for_new_message(char** data, int* size);
```

```

32 // Must be called after ZigBee_check_for_new_message returns
33 // a message. This function free's up the buffer
34 void ZigBee_done_reading_new_message(void);
35
36 // Wait's for the ZigBee code to stop receiving and
37 // sending and then set's the ZigBee module to sleep
38 void ZigBee_set_sleep(void);
39 // Wake's the ZigBee module up, the ZigBee module must
40 // be awake in order for ZigBee to be able to send and
41 // Receive
42 void ZigBee_wake_up(void);
43
44 // Explicit request for connection status
45 // After the ZigBee module sends a reply
46 // The ZigBee_state will be updated
47 void ZigBee_request_connection_status(void);
48
49 // Returns the State the ZigBee is in, which is either
50 // associated (false) or Disassociated (true)
51 int ZigBee_get_state(void);
52
53 enum ZIGBEE_STATE {
54     ZIGBEE_ASSOCIATED = 0,
55     ZIGBEE_DISASSOCIATED = 1
56 };
57
58 #endif /* ZIGBEE_H_ */

```

Listing B.2: ZigBee.c

```

1 /*
2  * ZigBee.c
3  *
4  * Created on: 4 mei 2012
5  * Author: Jan Angevare
6  */
7 #include "ZigBee.h"
8
9 // To keep apart status of sending frames
10 static char* _frames[4];
11 static int _frames_size[4];
12 static int _frame_id;
13
14 // For keeping incomming receives
15 // will be filled on interrupt
16 // Should be emptied by main loop
17 static char* volatile _data;
18 volatile static int _size;
19
20 volatile static int _state;
21
22 static void ZigBee_sleep(void) {
23     LPC_GPIO2->DATA |= 0x040;
24 }
25
26 static int ZigBee_is_sending(void) {
27     return ZigBee_sender_get_state();

```

```

28 }
29
30 static int ZigBee_is_receiving(void) {
31     return (ZigBee_receiver_get_state() & 1);
32 }
33
34 // Initialize
35 void ZigBee_init() {
36     _frame_id = 0;
37
38     _data = 0;
39     _size = 0;
40
41     _state = ZIGBEE_DISASSOCIATED;
42
43     // Initialize sleep pin
44     LPC_IOCON->PIO2_6 = 0xC0; // set GPIO, no pullup
45     LPC_GPIO2->DIR |= 0x040; // set output
46
47     ZigBee_buffer_init();
48     ZigBee_receiver_init();
49     ZigBee_sender_init();
50     UART_init();
51 }
52
53 // This handle gets called when ZigBee_sender completes a send request
54 void ZigBee_sender_send_complete_handle(char* data) {
55     ZigBee_buffer_release_buffer(data);
56
57     if (!ZigBee_is_receiving())
58         ZigBee_sleep();
59 }
60
61 // Send an amount of data to the coordinator
62 void ZigBee_send(char* data, int length) {
63     char* buffer;
64     int size;
65
66     // Just to be sure, wake the ZigBee up
67     ZigBee_wake_up();
68
69     // Try to get a buffer for filling
70     while(!ZigBee_buffer_get_buffer(&buffer));
71
72     // Construct message and set the frames ID etc, for resend if necessary
73     _frames[_frame_id++] = buffer;
74
75     size = ZigBee_constructor_construct_message(data, length, buffer, _frame_id);
76     _frames_size[_frame_id - 1] = size;
77     _frame_id %= 4;
78
79     // Loop until we get an ok for sending
80     while(!ZigBee_sender_send_frame(buffer, size));
81 }
82
83 // Is called whenever there has been received an whole frame on UART
84 void ZigBee_receiver_new_message_handle() {

```

```
85 char* buffer;
86 int size;
87
88 // Get the frame
89 ZigBee_receiver_get_message(&buffer, &size);
90
91 // And translate it
92 if(ZigBee_translator_translate(buffer, size))
93     ZigBee_buffer_release_buffer(buffer);
94 }
95
96 // If an transmit status: successful is received
97 // put the ZigBee module to sleep
98 void ZigBee_translator_send_success_handle(int data_frame) {
99     if (!ZigBee_is_receiving() && !ZigBee_is_sending())
100         ZigBee_sleep();
101 }
102
103 // If an transmit status: unsuccessful is received
104 // do the same as an successful send, this means
105 // the data is lost
106 void ZigBee_translator_send_failure_handle(int data_frame) {
107     ZigBee_translator_send_success_handle(data_frame);
108 }
109
110 // Gets called if the ZigBee sends an associated status
111 void ZigBee_translator_associated() {
112     _state = ZIGBEE_ASSOCIATED;
113     if (!ZigBee_is_receiving() && !ZigBee_is_sending())
114         ZigBee_sleep();
115 }
116
117 // If the ZigBee is dissacociated
118 void ZigBee_translator_disassociated() {
119     _state = ZIGBEE_DISASSOCIATED;
120 }
121
122 // If an received message has been received
123 // put it in the queue
124 // The main loop should poll with check for new message
125 void ZigBee_translator_receive_data_handle(char* data, int size) {
126     if (_data)
127         ZigBee_buffer_release_buffer(_data);
128
129     _size = size;
130     _data = data;
131
132     if (!ZigBee_is_receiving() && !ZigBee_is_sending())
133         ZigBee_sleep();
134 }
135
136 // If a new message is ready, get it
137 // this function returns true if a new message has been received
138 // else this function returns false
139 // if this function returns true a subsequent call to
140 // ZigBee_done_reading_new_message should be done
141 int ZigBee_check_for_new_message(char** data, int* size) {
```



```
142     if (_data) {
143         *data = _data;
144         *size = _size;
145
146         return 1;
147     } else return 0;
148 }
149
150 // This function empties the message queue and releases the buffer
151 void ZigBee_done_reading_new_message() {
152     if (_data) ZigBee_buffer_release_buffer(_data);
153
154     _data = 0;
155     _size = 0;
156 }
157
158 // Wait until no more sending or receiving is done
159 // then put the ZigBee module to sleep
160 void ZigBee_set_sleep() {
161     while (ZigBee_is_receiving() || ZigBee_is_sending()) {
162         ZigBee_wake_up();
163         power_modes_sleep(10, 0);
164     }
165
166     ZigBee_sleep();
167 }
168
169 void ZigBee_wake_up() {
170     LPC_GPIO2->DATA &= ~0x040;
171 }
172
173 // Explicit poll for connection status
174 void ZigBee_request_connection_status(void) {
175     char* buffer;
176     int size;
177     unsigned short command = ('A' << 8) + 'I';
178
179     // Try to get a buffer for filling
180     while(!ZigBee_buffer_get_buffer(&buffer));
181
182     // Construct message and set the frames ID etc, for resend if neccessary
183     _frames[_frame_id++] = buffer;
184
185     size = ZigBee_constructor_construct_at(command, buffer);
186     _frames_size[_frame_id - 1] = size;
187     _frame_id %= 4;
188
189     // Loop until we get an ok for sending
190     while(!ZigBee_sender_send_frame(buffer, size));
191 }
192
193 int ZigBee_get_state(void) {
194     return _state;
195 }
```

B-2 ZigBee_buffer

Listing B.3: ZigBee_buffer.h

```

1  /*
2  * ZigBee_buffer.h
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7
8  #ifndef ZIGBEE_BUFFER_H_
9  #define ZIGBEE_BUFFER_H_
10
11 void ZigBee_buffer_init(void);
12 // Tries to give a buffer back, all buffers are 128 bytes length
13 // return true if succeeded, returns false if no buffer could be found
14 // buffer parameter should point to a char pointer, the char pointer is set
15 // to the buffer, if succeeded, else is ignored
16 int ZigBee_buffer_get_buffer(char** buffer);
17 // Release the buffer, buffer parameter should point to the buffer
18 void ZigBee_buffer_release_buffer(char* buffer);
19
20 enum ZIGBEE_BUFFER_STATES {
21     ZIGBEE_BUFFER_EMPTY,
22     ZIGBEE_BUFFER_IN_USE
23 };
24
25 #endif /* ZIGBEE_BUFFER_H_ */

```

Listing B.4: ZigBee_buffer.c

```

1  /*
2  * ZigBee_buffer.c
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7
8  #include "ZigBee_buffer.h"
9
10 // Buffers and their states
11 static char _buffers[4][128];
12 int _buffer_states[4];
13
14 // Initialize
15 void ZigBee_buffer_init() {
16     int i;
17
18     for (i = 0; i < 4; i++)
19         _buffer_states[i] = ZIGBEE_BUFFER_EMPTY;
20 }
21
22 // Request a buffer
23 int ZigBee_buffer_get_buffer(char** buffer) {
24     int i;
25

```

```

26 // Check all buffers for availability
27 for (i = 0; i < 4; i++) {
28
29 // If available set buffer, set state and return true
30 if (_buffer_states[i] == ZIGBEE_BUFFER_EMPTY) {
31     *buffer = _buffers[i];
32     _buffer_states[i] = ZIGBEE_BUFFER_IN_USE;
33
34     return 1;
35 }
36 }
37
38 // No free buffer was found, return false
39 return 0;
40 }
41
42 // Set buffer state to empty
43 void ZigBee_buffer_release_buffer(char* buffer) {
44     _buffer_states[((buffer - _buffers[0]) >> 7)] = ZIGBEE_BUFFER_EMPTY;
45 }

```

B-3 ZigBee_constructor

Listing B.5: ZigBee_constructor.h

```

1 /*
2  * ZigBee_constructor.h
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7
8 #ifndef ZIGBEE_CONSTRUCTOR_H_
9 #define ZIGBEE_CONSTRUCTOR_H_
10
11 // Constructs a message to the coordinator
12 int ZigBee_constructor_construct_message(char* source, int size, char*
    destination, char frame_id);
13 int ZigBee_constructor_construct_at(unsigned short command, char*
    destination);
14 #endif /* ZIGBEE_CONSTRUCTOR_H_ */

```

Listing B.6: ZigBee_constructor

```

1 /*
2  * ZigBee_constructor.c
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7
8 #include "ZigBee_constructor.h"
9 #ifdef DEBUG
10 #include <stdio.h>

```

```

11 #endif
12
13 static int _checksum;
14 static int _offset;
15 static char _transmit_frame_header[14] = {
16     0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00 // 64-bit address
17     , 0xFF, 0xFE // 16-bit address
18     , 0x00 // Broadcast radius
19     , 0x00}; // Options
20
21 // Add data to the frame data
22 void ZigBee_constructor_add_data(char* source, int size, char* dest) {
23     int i;
24
25     for (i = 0; i < size; i++) {
26         _checksum += (dest[_offset++] = source[i]);
27     }
28 }
29
30 // Set size and checksum
31 void ZigBee_constructor_finalize(char* buffer) {
32     int size = _offset - 2;
33     buffer[0] = (char)(size>>8);
34     buffer[1] = (char)(size);
35
36     buffer[_offset++] = 0xFF - (0xFF & _checksum);
37 }
38
39 // Construct message
40 int ZigBee_constructor_construct_message(char* source, int size, char*
    destination, char frame_id) {
41     _offset = 2;
42     _checksum = 0;
43
44     _checksum += (destination[_offset++] = 0x10);
45     _checksum += (destination[_offset++] = frame_id);
46
47     ZigBee_constructor_add_data(_transmit_frame_header, 12, destination);
48
49     ZigBee_constructor_add_data(source, size, destination);
50
51     ZigBee_constructor_finalize(destination);
52
53     return _offset;
54 }
55
56 // Construct AT command request
57 int ZigBee_constructor_construct_at(unsigned short command, char* destination)
    {
58     _offset = 2;
59     _checksum = 0;
60
61     _checksum += (destination[_offset++] = 0x08);
62     _checksum += (destination[_offset++] = 1);
63     _checksum += (destination[_offset++] = (command>>8));
64     _checksum += (destination[_offset++] = (char)command);
65 }

```

```
66 ZigBee_constructor_finalize(destination);
67
68 return _offset;
69 }
```

B-4 ZigBee_receiver

Listing B.7: ZigBee_receiver.h

```
1 /*
2  * ZigBee_receiver.h
3  *
4  * Created on: 9 mei 2012
5  * Author: Jan Angevare
6  */
7
8 #ifndef ZIGBEE_RECEIVER_H_
9 #define ZIGBEE_RECEIVER_H_
10
11 #include "UART.h"
12 #include "ZigBee_buffer.h"
13
14 void ZigBee_receiver_init(void);
15 int ZigBee_receiver_get_state(void);
16 int ZigBee_receiver_get_message(char** message, int* size);
17
18 // get called by ZigBee receiver whenever a
19 // new message was received
20 void ZigBee_receiver_new_message_handle(void);
21
22 enum ZIGBEE_RECEIVER_STATES {
23     ZIGBEE_RECEIVER_IDLE = 0,
24     ZIGBEE_RECEIVER_RECEIVING = 1,
25     ZIGBEE_RECEIVER_MESSAGE_READY = 2,
26 };
27
28 #endif /* ZIGBEE_RECEIVER_H_ */
```

Listing B.8: ZigBee_receiver.c

```
1 /*
2  * ZigBee_receiver.c
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7
8 #include "ZigBee_receiver.h"
9
10 // Buffer for completely received frames
11 static char* volatile _b_buffer;
12 volatile static int _b_size;
13
14 // Buffer for receiving of frames
```

```
15 static char* _buffer;
16 static int _size;
17 static int _offset;
18 static int _state;
19
20 // State for escape characters
21 static char _escape;
22 static unsigned int _checksum;
23
24 // Initialize ZigBee_receiver
25 void ZigBee_receiver_init() {
26     _b_buffer = 0;
27     _b_size = 0;
28
29     _buffer = 0;
30     _size = 0;
31     _offset = 0;
32     _checksum = 0;
33     _state = ZIGBEE_RECEIVER_IDLE;
34 }
35
36 // Return the ZigBee_receiver_state
37 int ZigBee_receiver_get_state() {
38     return _state;
39 }
40
41 // Try to get the frame, this function returns true if a frame was ready
42 // else it returns false
43 int ZigBee_receiver_get_message(char** message, int* size) {
44     if (_state & ZIGBEE_RECEIVER_MESSAGE_READY) {
45         *message = _b_buffer;
46         *size = _b_size;
47
48         // reset frame buffer and state
49         _b_buffer = 0;
50         _b_size = 0;
51         _state = _state & 1;
52         return 1;
53     } else return 0;
54 }
55
56 // The received data handle
57 void UART_RBR_handle() {
58     char data;
59
60     // Read the data
61     data = LPC_UART->RBR;
62
63     // If we don't have a buffer to file , get one
64     if (!_buffer) {
65         if (data == 0x7E) {
66             while(!ZigBee_buffer_get_buffer(&_buffer))
67                 _state |= ZIGBEE_RECEIVER_RECEIVING;
68         } else {
69             return;
70         }
71     }
```

```

72
73  switch (data) {
74  // Started new frame
75  case 0x7E:
76      _size = 0;
77      _offset = 0;
78      _escape = 0;
79      _checksum = 0;
80      break;
81
82  // escape next character
83  case 0x7D:
84      _escape = 0x20;
85      break;
86
87  // Nothing special, just add the byte
88  default:
89      {
90          char t = _buffer[_offset++] = data ^ _escape;
91          _checksum += (_offset>2)? t: 0;
92          _escape = 0;
93      }
94  }
95
96  // We now have the size delimiter, so read it
97  if (_offset == 2) {
98      _size = (_buffer[0]<<8) + _buffer[1];
99
100  // If we have received the complete message, put it in the hold buffer
101  } else if (_offset == _size + 3) {
102      char* b = _buffer;
103      _b_buffer = _buffer;
104      _b_size = _offset;
105      _state = ZIGBEE_RECEIVER_MESSAGE_READY;
106      _buffer = 0;
107      _offset = 0;
108      if ((_checksum & 0xFF) == 0xFF) ZigBee_receiver_new_message_handle();
109      else ZigBee_buffer_release_buffer(b);
110      _checksum = 0;
111  }
112 }

```

B-5 ZigBee_sender

Listing B.9: ZigBee_sender.h

```

1  /*
2  * ZigBee_sender.h
3  *
4  * Created on: 9 mei 2012
5  * Author: Jan Angevare
6  */
7
8  #ifndef ZIGBEE_SENDER_H_
9  #define ZIGBEE_SENDER_H_

```

```

10
11 #include "UART.h"
12 #include "ZigBee_buffer.h"
13
14 // Initialize ZigBee_sender
15 void ZigBee_sender_init(void);
16 // Request send data, returns false if ZigBee_sender could not comply
17 // returns true if ZigBee_sender is going to transmit
18 // transmits asynchronously
19 // No need to add 0x7E to buffer, this function automatically starts with 7E
20 // Characters which need to be escaped are automatically escaped
21 int ZigBee_sender_send_frame(char* data, int size);
22 int ZigBee_sender_abort(void);
23
24 int ZigBee_sender_get_state(void);
25 // Define this function in your file
26 // This function is called when the transmit is completed
27 // the data parameter equals the data parameter from the send_frame function
28 // This function is called asynchronously
29 void ZigBee_sender_send_complete_handle(char* data);
30
31 enum ZIGBEE_SENDER_STATES {
32     ZIGBEE_SENDER_IDLE = 0,
33     ZIGBEE_SENDER_SENDING = 1
34 };
35
36 #endif /* ZIGBEE_SENDER_H_ */

```

Listing B.10: ZigBee_sender.c

```

1 /*
2  * ZigBee_sender.c
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7 #include "ZigBee_sender.h"
8
9 static char* _buffer;
10 static int _size;
11 static int _offset;
12 static int _escape;
13
14 volatile static int _state;
15
16 // Initialize state to zero
17 void ZigBee_sender_init() {
18     _buffer = 0;
19     _size = 0;
20     _offset = 0;
21     _escape = 0;
22     _state = ZIGBEE_SENDER_IDLE;
23 }
24
25 // Request send frame
26 int ZigBee_sender_send_frame(char* data, int size) {
27     // If a transmit request is in progress

```



```
28 // return false
29 if (!_buffer) return 0;
30 _state = ZIGBEE_SENDER_SENDING;
31
32 // Set variables
33 _buffer = data;
34 _size = size;
35 _offset = 0;
36 _escape = 0;
37
38 // Write new frame
39 UART_write(0x7E);
40
41 // return true
42 return 1;
43 }
44
45 int ZigBee_sender_abort() {
46     char* buffer = _buffer;
47
48     _buffer = 0;
49     _size = 0;
50     _offset = 0;
51     _escape = 0;
52     _state = ZIGBEE_SENDER_IDLE;
53
54     ZigBee_buffer_release_buffer(buffer);
55
56     return 0;
57 }
58
59 int ZigBee_sender_get_state() {
60     return _state;
61 }
62 // Event handle for Transmit Hold Register Empty interrupt
63 void UART_THRE_handle() {
64     // Nothing to do here
65     if (!_buffer) return;
66
67
68     // If escaped and escape 0x7D sent
69     if (_escape) {
70         LPC_UART->THR = _buffer[_offset++] ^ 0x20;
71         _escape = 0;
72
73
74     // If not escaped sent next char
75     } else {
76         char data = _buffer[_offset];
77         switch (data) {
78
79             // If next char needs to be escaped
80             case 0x7E:
81             case 0x7D:
82             case 0x11:
83             case 0x13:
84                 LPC_UART->THR = 0x7D;
```

```

85     _escape = 1;
86     break;
87
88     // Normal, just sent
89     default:
90         LPC_UART->THR = data;
91         _offset++;
92     }
93 }
94
95 // Sent all data
96 if (_offset == _size) {
97
98     // Call send_complete handler
99     char* buffer = _buffer;
100    _buffer = 0;
101    _size = 0;
102    _offset = 0;
103    _state = ZIGBEE_SENDER_IDLE;
104    ZigBee_sender_send_complete_handle(buffer);
105
106 }
107 }

```

B-6 ZigBee_translator

Listing B.11: ZigBee_translator.h

```

1  /*
2  * ZigBee_translator.h
3  *
4  * Created on: 9 mei 2012
5  * Author: Jan Angevare
6  */
7
8  #ifndef ZIGBEE_TRANSLATOR_H_
9  #define ZIGBEE_TRANSLATOR_H_
10
11 // Translates the message and calls the specified handle function
12 int ZigBee_translator_translate(char* message, int size);
13
14 // These functions get called by the ZigBee translator whenever it
15 // is translating the handles message
16 void ZigBee_translator_send_success_handle(int data_frame);
17 void ZigBee_translator_send_failure_handle(int data_frame);
18 void ZigBee_translator_receive_data_handle(char* data, int size);
19 void ZigBee_translator_associated(void);
20 void ZigBee_translator_disassociated(void);
21
22 enum ZIGBEE_FRAME_ID {
23     // Sent from ZigBee in specific conditions:
24     ZIGBEE_MODEM_STATUS = 0x8A,
25
26     // Allows for module parameter registers to be
27     // queried or set

```

```

28  ZIGBEE_AT_COMMAND = 0x08 ,
29
30  // Same as AT_COMMAND but parameters not applied
31  // until an AT_COMMAND or an APPLY_CHANGES
32  ZIGBEE_AT_COMMAND_QUEUE = 0x09 ,
33
34  // Sent from ZigBee, in response from AT_COMMAND
35  ZIGBEE_AT_COMMAND_RESPONSE = 0x88 ,
36  ZIGBEE_REMOTE_COMMAND_REQUEST = 0x17 ,
37
38  // Let ZigBee send data
39  ZIGBEE_TRANSMIT_REQUEST = 0x10 ,
40
41  // Same as transmit but expl. adr.
42  ZIGBEE_EXPLICIT_ADDRESSING = 0x11 ,
43
44  // When a TX request is completed, the module
45  // sends a TX message
46  ZIGBEE_TRANSMIT_STATUS = 0x8B ,
47
48  // When the ZigBee receive an packet it sends
49  // it to the UART with this message
50  ZIGBEE_RECEIVE_PACKET = 0x90 ,
51
52  // Received expl. adr. package
53  ZIGBEE_EXPLICIT_RX = 0x91 ,
54
55
56  ZIGBEE_SENSOR_READ = 0x94 ,
57  ZIGBEE_NODE_IDENTIFICATION = 0x95
58 };
59
60 #endif /* ZIGBEE_TRANSLATOR_H_ */

```

Listing B.12: ZigBee_translator.c

```

1  /*
2  * ZigBee_translator.c
3  *
4  * Created on: 9 mei 2012
5  * Author: bap
6  */
7
8  #include "ZigBee_translator.h"
9
10 int ZigBee_translator_translate(char* message, int size) {
11     switch (message[2]) {
12
13         // Only in debug mode use printf to print modem status
14         case ZIGBEE_MODEM_STATUS:
15             switch (message[3]) {
16
17                 case 2: // Associated
18                     ZigBee_translator_associated();
19                     break;
20
21                 case 3: // Disassociated

```

```

22     ZigBee_translator_disassociated();
23     break;
24
25     case 0: // Hardware reset
26     case 1: // Watchdog timer reset
27     case 4: // Synchronization lost
28     case 5: // Coordinator Realignment
29     case 6: // Coordinator Started
30     default:
31         break;
32     }
33     break;
34
35
36 case ZIGBEE_AT_COMMAND_RESPONSE:
37     // If ZigBee Associated Indication Response
38     if (message[4] == 'A' && message[5] == 'I') {
39
40         // If no error
41         if (message[6] == 0) {
42
43             // If associated
44             if (message[7] == 0) {
45                 ZigBee_translator_associated();
46             // If not associated
47             } else {
48                 ZigBee_translator_disassociated();
49             }
50         }
51     }
52     break;
53
54
55 case ZIGBEE_TRANSMIT_STATUS:
56     switch (message[7]) {
57     case 0x00: //Success
58         ZigBee_translator_send_success_handle((int)message[3]);
59         break;
60
61     case 0x22: //Not joined to network
62         ZigBee_translator_disassociated();
63         // continue
64     case 0x02: //CCA Failure
65     case 0x15: //Invalid destination endpoint
66     case 0x21: //Network ACK failure
67     case 0x23: //Self-addressed
68     case 0x24: //Address Not Found
69     case 0x25: //Route Not found
70     default:
71         ZigBee_translator_send_failure_handle((int)message[3]);
72         break;
73     }
74     break;
75
76 case ZIGBEE_RECEIVE_PACKET:
77     ZigBee_translator_receive_data_handle(message + 14, size - 15);
78     return 0;

```

```
79     //break;
80
81     default:
82         break;
83     }
84     return 1;
85 }
```

B-7 UART

Listing B.13: UART.h

```
1  /*
2  * UART.h
3  *
4  * Created on: 3 mei 2012
5  * Author: bap
6  */
7  #include "LPC11xx.h"
8
9  #ifndef UART_H_INCLUDED
10 #define UART_H_INCLUDED
11
12 // Initialize the UART, after calling this
13 // function interrupts on the UART are turned on
14 void UART_init(void);
15 // Write a char to the UART
16 int  UART_write(char);
17
18 // Get the UART state
19 int  UART_get_state(void);
20
21 // these functions get called by the UART
22 // after a char has been received and send
23 // respectively
24 void UART_RBR_handle(void);
25 void UART_THRE_handle(void);
26
27 enum UART_STATE {
28     UART_receive_data = 0x01,
29     UART_overrun_error = 0x02,
30     UART_parity_error = 0x04,
31     UART_framing_error = 0x08,
32     UART_break_interrupt = 0x10,
33     UART_transmit_hold_register_empty = 0x20,
34     UART_transmitter_empty = 0x40,
35     UART_receive_error = 0x80
36 };
37
38 enum RX_TRIGGER_LEVEL {
39     BYTE_1 = 0x00,
40     BYTES_4 = 0x40,
41     BYTES_8 = 0x80,
42     BYTES_14 = 0xC0
43 };
```

```

44
45 //const enum RX_TRIGGER_LEVEL TriggerLevel = BYTE_1;
46
47
48
49 #endif /* UART_H_ */

```

Listing B.14: UART.c

```

1  /*
2  * UART.c
3  *
4  * Created on: 3 mei 2012
5  * Author: bap
6  */
7  #include "UART.h"
8
9  //extern const enum RX_TRIGGER_LEVEL TriggerLevel;
10 const enum RX_TRIGGER_LEVEL TriggerLevel = BYTE_1;
11
12 void UART_init() {
13     // Initialize UART Con
14     LPC_IOCON->PIO1_6 = 0xC1; // Set RXD
15     LPC_IOCON->PIO1_7 = 0xC1; // Set TXD
16     LPC_IOCON->PIO0_7 = 0xC1; // Set CTS
17     LPC_IOCON->PIO1_5 = 0xC1; // Set RTS
18
19     LPC_SYSCON->SYSAHBCLKCTRL |= 0x1000; //Turn on clock
20     LPC_SYSCON->UARTCLKDIV = 1; //Set clock divider to 1
21     LPC_UART->MCR = 0xC0; // Enable auto RTS and CTS
22     LPC_UART->LCR |= 0x80; // Gain access to DLL and DLM
23
24     //LPC_UART->DLL = 71; // Set UART clock divider to 71 (baud = 9600)
25     LPC_UART->DLL = 4; // Set UART clock divider to 3 (baud = 115200)
26
27     LPC_UART->DLM = 0; // High byte
28     LPC_UART->LCR = 0x03; // Stop access to DL and set LCR in the meantime
29
30     //LPC_UART->FDR = 0xA1; // Set prescaler to a good number (baud = 9600)
31     LPC_UART->FDR = 0x85; // Set prescaler to a good number (baud = 115200)
32
33     LPC_UART->IER = 0x03; // Data Receive Interrupts enabled
34
35     NVIC->ISER[0] |= (1<<21); //Enable UART interrupts
36
37     // Clear FIFO Buffers and set interrupt trigger level
38     LPC_UART->FCR = 0x07 | TriggerLevel;
39 }
40
41 // Write a char to the UART
42 int UART_write(char c) {
43     // Wait until we can write a char to the transmit hold register
44     while (!(LPC_UART->LSR & UART_transmit_hold_register_empty));
45
46     // write it
47     LPC_UART->THR = c;
48     return 0;

```

```

49 }
50
51 int UART_get_state() {
52     return LPC_UART->LSR;
53 }
54
55 void UART_IRQHandler (void) {
56     int state = LPC_UART->IIR;
57     state = LPC_UART->LSR;
58
59     if (state & UART_receive_data)
60         UART_RBR_handle();
61
62     if (state & UART_transmit_hold_register_empty)
63         UART_THRE_handle();
64 }

```

2-8 Power_modes

Listing 2.15: Power_modes.h

```

1 /*
2  * power_modes.h
3  *
4  * Created on: 22 mei 2012
5  * Author: bap
6  */
7
8 #ifndef POWER_MODES_H_
9 #define POWER_MODES_H_
10
11 void power_modes_init(void);
12 // sleep for milliseconds, if interruptable this function will
13 // return on every interrupt, if not interruptable this function
14 // will only return when the timer expires.
15 // if the timer has expired this function return true
16 // if the timer has not expired this function returns false
17 // whenever milliseconds equals zero the timer is not reset
18 // but retains his value from last call
19 int power_modes_sleep(int milliseconds, char interruptable);
20 void power_modes_deep_sleep(int timerValue);
21
22 #endif /* POWER_MODES_H_ */

```

Listing 2.16: Power_modes.c

```

1 /*
2  * power_modes.c
3  *
4  * Created on: 22 mei 2012
5  * Author: Ingmar Jager
6  */
7 #include "power_modes.h"
8 #include <LPC11xx.h>

```

```

9
10 void sampling_timer_init(void);
11 void wakeup_timer_init(int timerValue);
12
13
14 volatile int wakeup_clkctrl;
15 volatile int wakeup_nvic_0;
16 volatile int wakeup_nvic_1;
17
18 void power_modes_init() {
19     // Enable the watchdog oscillator
20     LPC_SYSCON->PDRUNCFG &= ~0x40;
21
22     //Set watchdog oscillator frequency to 0.5MHz/64
23     LPC_SYSCON->WDTOSCCTRL = 0x3F;
24
25
26     // Setting timer for sleep
27     LPC_SYSCON->SYSAHBCLKCTRL |= (1<<10); // Enable timer
28     LPC_TMR32B1->PR = 12000; // Prescaler
29     LPC_TMR32B1->MCR = 0x07; // Interrupt on MR0 and Stop on MR0, also TCR[0]
30         reset
31     LPC_SYSCON->SYSAHBCLKCTRL &= ~(1<<10); // Disable timer
32     NVIC->ISER[0] |= (1<<19); // enable timer21b2 interrupt
33 }
34
35
36 int power_modes_sleep(int milliseconds, char interruptable) {
37     // PCON DPDEN zero
38     LPC_PMU->PCON &= ~0x02;
39
40     // SCR SLEEPDEEP = 0;
41     SCB->SCR &= ~0x04;
42
43     // Check if we need to reset the timer
44     if (milliseconds) {
45         // Enable timer
46         LPC_SYSCON->SYSAHBCLKCTRL |= (1<<10);
47
48         // Reset timer
49         LPC_TMR32B1->TCR = 0x02;
50
51         // Set timeout value
52         LPC_TMR32B1->MR0 = milliseconds;
53
54         // Timer control = Timer Counter + Prescale Counter enabled
55         LPC_TMR32B1->TCR = 0x01;
56
57     } else {
58         if (!(LPC_TMR32B1->TCR & 1)) return 0;
59     }
60
61     // Sleep
62     do {
63         __WFI();
64     } while ((LPC_TMR32B1->TCR & 1) && !interruptable);

```



```

65
66
67     return (LPC_TMR32B1->TCR & 1);
68 }
69
70 void TIMER32_1_IRQHandler() {
71     LPC_TMR32B1->IR = 1;
72     NVIC_ClearPendingIRQ(TIMER_32_1_IRQn);
73
74     // Disable timer
75     LPC_SYSCON->SYSAHBCLKCTRL &= ~(1<<10);
76 }
77
78 void power_modes_deep_sleep(int timerValue) {
79     // WAKEUP CONFIG
80
81     // Configure PDAWAKECFG to restore PDRUNCFG on wake up
82     LPC_SYSCON->PDAWAKECFG = LPC_SYSCON->PDRUNCFG;
83
84     // Store current value of SYSAHBCLKCTRL for restoration later
85     wakeup_clkctrl = LPC_SYSCON->SYSAHBCLKCTRL;
86
87     // Store current value of interrupt registers
88     wakeup_nvic_0 = NVIC->ICER[0];
89     wakeup_nvic_1 = NVIC->ICER[1];
90
91     // Disable all interrupts
92     NVIC->ICER[0] = 0xFFFFFFFF;
93     NVIC->ICER[1] = 0xFFFFFFFF;
94
95
96     // WAKEUP TIMER
97     //init timer: select timer32MAT3 mode
98     LPC_IOCON->R_PIO0_11 = 0xC3;
99     LPC_GPIO0->DIR |= (1 << 11);
100
101     // Enable clock to the timer
102     LPC_SYSCON->SYSAHBCLKCTRL |= 0x200;
103
104     // Configure timer
105
106     // No interrupts
107     LPC_TMR32B0->IR = 0x00;
108
109     // Disable and reset timer
110     LPC_TMR32B0->TCR = 0x2;
111
112     // No prescaler
113     LPC_TMR32B0->PR = 0;
114
115     // Let the timer stop automatically when it matches
116     LPC_TMR32B0->MCR = 0x800;
117
118     // Set match register to timerValue
119     LPC_TMR32B0->MR3 = (int)(500000.0 / 64.0) * timerValue;
120
121     // No PWM functionality used

```

```
122     LPC_TMR32B0->PVMC = 0x00;
123
124     // Set external match thing to make the pin high at first and then drive the
125     // pin low upon match 3
126     LPC_TMR32B0->EMR = 0x408;
127
128     // WORKAROUND FOR RESET CURRENT CONSUMPTION
129
130     // Make reset pin GPIO
131     LPC_IOCON->RESET_PIO0_0 |= 0x01;
132
133     // Make reset pin an output
134     LPC_GPIO0->DIR |= 0x001;
135
136     // Drive reset pin high
137     LPC_GPIO0->DATA |= 0x001;
138
139
140     // START LOGIC
141
142     // Falling edge
143     LPC_SYSCON->STARTAPRPO &= ~(1 << 11);
144
145     // Clear pending bit
146     LPC_SYSCON->STARTRSRPOCLR = 1 << 11;
147
148     // Enable Start Logic
149     LPC_SYSCON->STARTERPO |= 1 << 11;
150
151     // Enable wakeup interrupt
152     NVIC_EnableIRQ(WAKEUP11_IRQn);
153
154
155     // GO TO SLEEP NOW
156
157     // Shut down clocks to almost everything
158     LPC_SYSCON->SYSAHBCLKCTRL = 0x215;
159
160     // Configure deep sleep with WDT oscillator
161     LPC_SYSCON->PDSLEEPCFG = 0x18BF;
162
163     // Set SLEEPDEEP
164     SCB->SCR |= 0x04;
165
166     // Switch main clock to low-speed WDO
167     LPC_SYSCON->MAINCLKSEL = 0x02;
168     LPC_SYSCON->MAINCLKUEN = 0;
169     LPC_SYSCON->MAINCLKUEN = 1;
170
171     // Start the timer
172     LPC_TMR32B0->TCR = 0x1;
173
174     // Preload clock selection for quick switch back to XTAL on wakeup
175     LPC_SYSCON->MAINCLKUEN = 0;
176     LPC_SYSCON->MAINCLKSEL = 0x01;
177
```

```

178 // Enter deep sleep mode
179 __WFI();
180
181 // Restore main clock to IRC 12 MHz
182 LPC_SYSCON->MAINCLKUEN = 1;
183
184 // Clear match bit on timer
185 //LPC_TMR16B0->EMR = 0;
186
187 // Clear pending bit on start logic
188 LPC_SYSCON->STARTRSRPOCLR = 1 << 11;
189
190 // Restore clocks to chip modules
191 LPC_SYSCON->SYSAHBCLKCTRL = wakeup_clkctrl;
192
193 // Restore interrupts
194 NVIC_DisableIRQ(WAKEUP11_IRQn);
195 NVIC_ClearPendingIRQ(WAKEUP11_IRQn);
196 NVIC->ISER[0] = wakeup_nvic_0;
197 NVIC->ISER[1] = wakeup_nvic_1;
198 }
199
200 void WAKEUP_IRQHandler(void) {
201 // Clear pending bit on start logic
202
203 // Disable start logic
204 LPC_SYSCON->STARTERPO &= ~(1 << 11);
205 LPC_SYSCON->STARTRSRPOCLR = 1 << 11;
206
207 // Disable start logic interrupt
208 NVIC_DisableIRQ(WAKEUP11_IRQn);
209 NVIC_ClearPendingIRQ(WAKEUP11_IRQn);
210 }

```

2-9 MIST

Listing 2.17: mist.h

```

1 // Project: WICS
2 // Author: Jeroen van Straten
3 // Date: 20120503
4 // Purpose: High level access to MIST chip.
5
6 #ifndef mist_guard
7 #define mist_guard
8
9 #include <stdint.h>
10
11
12 // Struct containing the measurement result
13 __packed struct measurement {
14     uint16_t temperature;
15     uint16_t humidity;
16     uint16_t ambience;
17     uint8_t error;

```

```

18 };
19
20 #define ERRFLAG_BAT_MASK      0x07
21 #define ERRFLAG_MIST_ERROR    0x08
22 #define ERRFLAG_NOT_HARVESTING 0x10
23 #define ERRFLAG_INVALID_LIGHT 0x20
24 #define ERRFLAG_INVALID_HUMIDITY 0x40
25 #define ERRFLAG_INVALID_TEMPERATURE 0x80
26
27
28 // Initialize I/O and driver.
29 extern void initializeSensor(void);
30
31 // Takes a sample and writes this to data. Blocking!
32 extern uint8_t getMeasurement(struct measurement *data);
33
34
35
36 #endif

```

Listing 2.18: mist.c

```

1 #include "mist.h"
2
3 #include "LPC11xx.h"
4 #include <stdint.h>
5
6 #include "mist_lowlevel.h"
7 #include "mist_definitions.h"
8 #include "power_modes.h"
9 // #include "main.h"
10
11
12
13
14
15 uint8_t mist_enable(struct measurement *data);
16 void mist_read_temperature(struct measurement *data);
17 void mist_read_humidity(struct measurement *data);
18 void mist_read_light(struct measurement *data);
19 void mist_disable(struct measurement *data);
20 void adc_read_battery_and_pvcell(struct measurement *data);
21
22
23 // Sends a command, returns the reply code. The number of data words sent are
    // defined by
24 // data_count (which must thus be 0, 1 or 2).
25 uint16_t mist_send_command(uint16_t command, uint16_t data1, uint16_t data2,
    uint8_t data_count);
26
27 // Receives a response code
28 uint16_t mist_receive_response(void);
29
30
31 volatile uint16_t debug;
32
33 void initializeSensor() {

```

```

34     // Initialize I/O ports
35     mist_ll_init();
36 }
37
38 uint8_t analog_trim_value;
39
40 uint8_t getMeasurement(struct measurement *data) {
41
42     // Reset measurement data
43     data->temperature = 0;
44     data->humidity = 0;
45     data->ambience = 0;
46     data->error = ERRFLAG_INVALID_TEMPERATURE | ERRFLAG_INVALID_HUMIDITY |
47                 ERRFLAG_INVALID_LIGHT;
48
49     // Read MIST stuff
50     if (mist_enable(data)) {
51         mist_read_temperature(data);
52         mist_read_humidity(data);
53         mist_read_light(data);
54     }
55     mist_disable(data);
56
57     // Read battery stuff
58     adc_read_battery_and_pvcell(data);
59
60     return 1;
61 }
62
63
64 void adc_read_battery_and_pvcell(struct measurement *data) {
65     // TODO
66 }
67
68
69 uint8_t mist_enable(struct measurement *data) {
70
71     volatile uint32_t i;
72     uint8_t analog_trim_value;
73
74     // enable and init clock and SPI
75     mist_clock_enable();
76     mist_spi_enable();
77
78     // reset
79     mist_reset_assert();
80     for (i = 200; i; i--); // just a short delay
81     mist_reset_release();
82
83     // wait for SINT (enable pulldown just for the first test to test
84     // for disconnected sensor)
85     LPC_IOCON->R_PI01_0 |= 0x08; // enable SINT pulldown
86     while (!mist_get_sint()); // TODO: timeout
87     LPC_IOCON->R_PI01_0 &= ~0x08; // disable the SINT pulldown
88
89     // receive status word from the unit and save the factory calibration

```

```

90 // value for the analog LDO
91 analog_trim_value = (mist_receive_response() >> 8) & 0x0E;
92
93 // enable analog LDO
94 if (mist_send_command(POWER_LEVEL | analog_trim_value |
95     POWER_LEVEL_LDO_ON, 0, 0, 0) != ACK) {
96     data->error |= ERRFLAG_MIST_ERROR;
97     return 0;
98 }
99 return 1;
100 }
101
102
103 void mist_read_temperature(struct measurement *data) {
104
105     // power up the temperature sensor
106     if (mist_send_command(SENSOR_POWER | SENSOR_TEMPERATURE, 0, 0, 0) != ACK) {
107         data->error |= ERRFLAG_MIST_ERROR;
108         return;
109     }
110
111     // setup sensor
112     if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_TEMPERATURE,
113         TEMP_OPMODE, TEMP_OPMODE_OM_CALI | TEMP_OPMODE_RES_12B, 2)) != ACK) {
114         data->error |= ERRFLAG_MIST_ERROR;
115         return;
116     }
117
118     // start sensor read
119     if (mist_send_command(SENSOR_START | SENSOR_TEMPERATURE, 0, 0, 0) != ACK) {
120         data->error |= ERRFLAG_MIST_ERROR;
121         return;
122     }
123
124     // wait for sensor to be done
125     while (!(mist_send_command(STATUS, 0, 0, 0) & STATUS_TEMP_IRQ))
126         power_modes_sleep(10, 0);
127
128     // fetch result
129     data->temperature = mist_send_command(SENSOR_READ_OUTPUT |
130         SENSOR_TEMPERATURE, 0, 0, 0);
131     data->error &= ~ERRFLAG_INVALID_TEMPERATURE;
132
133     // turn off sensor again
134     mist_send_command(SENSOR_SHUTDOWN | SENSOR_TEMPERATURE, 0, 0, 0);
135 }
136
137 void mist_read_humidity(struct measurement *data) {
138     __fp16 half;
139
140     // power up the humidity sensor
141     if (mist_send_command(SENSOR_POWER | SENSOR_HUMIDITY, 0, 0, 0) != ACK) {
142         data->error |= ERRFLAG_MIST_ERROR;

```

```

143     return;
144 }
145
146 // sensor opmode
147 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY,
148     HUMID_OPMODE, HUMID_OPMODE_VAL, 2)) != ACK) {
149     data->error |= ERRFLAG_MIST_ERROR;
150     return;
151 }
152
153 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY,
154     HUMID_INPUT_SEL, HUMID_INPUT_SEL_1, 2)) != ACK) {
155     data->error |= ERRFLAG_MIST_ERROR;
156     return;
157 }
158
159 // sensor calibration
160 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY,
161     HUMID_CAL_A0, mist_send_command(NVMEM_READ | RH_1_A0, 0, 0, 0), 2)) !=
162     ACK) {
163     data->error |= ERRFLAG_MIST_ERROR;
164     return;
165 }
166
167 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY,
168     HUMID_CAL_A1, mist_send_command(NVMEM_READ | RH_1_A1, 0, 0, 0), 2)) !=
169     ACK) {
170     data->error |= ERRFLAG_MIST_ERROR;
171     return;
172 }
173
174 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY,
175     HUMID_CAL_B0, mist_send_command(NVMEM_READ | RH_1_B0, 0, 0, 0), 2)) !=
176     ACK) {
177     data->error |= ERRFLAG_MIST_ERROR;
178     return;
179 }
180
181 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY,
182     HUMID_CAL_B1, mist_send_command(NVMEM_READ | RH_1_B1, 0, 0, 0), 2)) !=
183     ACK) {
184     data->error |= ERRFLAG_MIST_ERROR;
185     return;
186 }
187
188 half = (__fp16)((float)data->temperature * 0.015625f);
189 if ((debug = mist_send_command(SENSOR_SETUP | SENSOR_HUMIDITY, HUMID_TEMP,
190     *(uint16_t*) &half, 2)) != ACK) {
191     data->error |= ERRFLAG_MIST_ERROR;
192     return;
193 }
194
195 // start sensor read
196 if (mist_send_command(SENSOR_START | SENSOR_HUMIDITY, 0, 0, 0) != ACK) {
197     data->error |= ERRFLAG_MIST_ERROR;
198     return;
199 }

```

```

189
190 // wait for sensor to be done
191 while (!(mist_send_command(STATUS, 0, 0, 0) & STATUS_HUMID_IRQ))
192     power_modes_sleep(10, 0);
193
194 // fetch result
195 data->humidity = mist_send_command(SENSOR_READ_OUTPUT | SENSOR_HUMIDITY,
196     0, 0, 0);
197 data->error &= ~ERRFLAG_INVALID_HUMIDITY;
198
199 // turn off sensor again
200 mist_send_command(SENSOR_SHUTDOWN | SENSOR_HUMIDITY, 0, 0, 0);
201 }
202
203 void mist_read_light(struct measurement *data) {
204     // TODO: not yet implemented
205 }
206
207 void mist_disable(struct measurement *data) {
208     // disable analog LDO
209     mist_send_command(POWER_LEVEL | analog_trim_value | POWER_LEVEL_LDO_OFF,
210         0, 0, 0);
211
212     // disable peripherals to reduce power consumption
213     mist_reset_assert();
214     mist_clock_disable();
215     mist_spi_disable();
216 }
217
218
219
220
221
222
223 uint16_t mist_send_command(uint16_t command, uint16_t data1, uint16_t data2,
224     uint8_t data_count) {
225     volatile uint32_t delay;
226
227     // Make sure no reply was pending still
228     if (mist_get_sint()) {
229         mist_receive_response();
230     }
231
232     // Something is wrong if the command is still pending, return ERR.
233     if (mist_get_sint()) {
234         return ERR;
235     }
236
237     // Select MIST chip
238     mist_spi_select();
239
240     // Give the chip a little time to get ready
241     for (delay = 20; delay; delay--);

```



```

242 // Send command
243 mist_spi_transfer(command);
244 if (data_count > 0) mist_spi_transfer(data1);
245 if (data_count > 1) mist_spi_transfer(data2);
246
247 // Deselect the MIST chip again
248 mist_spi_deselect();
249
250 // Wait for the MIST chip to get its data ready
251 while (!mist_get_sint());
252
253 // Wait for reply and return it.
254 return mist_receive_response();
255 }
256
257
258 uint16_t mist_receive_response() {
259     volatile uint32_t delay;
260     uint16_t response;
261
262     // Select MIST chip
263     mist_spi_select();
264
265     // Give the chip a little time to get ready
266     for (delay = 20; delay; delay--);
267
268     // Retrieve the response
269     response = mist_spi_transfer(0x0000);
270
271     // Deselect the MIST chip again
272     mist_spi_deselect();
273
274     return response;
275 }

```

Listing 2.19: mist_definitions.h

```

1 // Project: WICS
2 // Author:  Jeroen van Straten
3 // Date:    20120503
4 // Purpose: Defines a bunch of constants such as register numbers
5 //           and command IDs used by the MIST chip.
6
7 #ifndef mist_defs_guard
8 #define mist_defs_guard
9
10
11 // Return codes
12 #define ERR                0xA1A1
13 #define ILLEGAL            0x5555
14 #define ACK                0xAC00
15
16 // Command codes
17 #define RESET              0x0000
18 #define POWER_LEVEL        0x0100
19 #define STATUS              0x0200
20 #define IC_LOCK            0xC300

```

```
21 #define IC_UNLOCK          0xC400
22 #define SENSOR_POWER      0x0500
23 #define SENSOR_SHUTDOWN  0x0600
24 #define SENSOR_START      0x0700
25 #define SENSOR_SETUP      0xC800
26 #define SENSOR_READ_OUTPUT 0x0900
27 #define SENSOR_STATUS     0x0A00
28 #define NVMEM_UNLOCK_BANK 0xD000
29 #define NVMEM_LOCK_BANK   0xD200
30 #define NVMEM_SET_PASSWORD 0xD300
31 #define NVMEM_READ        0x1500
32 #define NVMEM_WRITE       0x5400
33
34 // Power level parameters
35 #define POWER_LEVEL_LDO_ON 0x0000
36 #define POWER_LEVEL_LDO_OFF 0x0001
37
38 // Sensor identifiers
39 #define SENSOR_TEMPERATURE 0x0001
40 #define SENSOR_HUMIDITY    0x0002
41 #define SENSOR_LIGHT       0x0003
42 #define SENSOR_ADC         0x0004
43
44 // Status masks
45 #define STATUS_TEMP_IRQ    0x1000
46 #define STATUS_HUMID_IRQ  0x2000
47 #define STATUS_LIGHT_IRQ   0x4000
48 #define STATUS_ADC_IRQ    0x8000
49
50 // Temperature sensor registers
51 #define TEMP_OPMODE        0x0020
52 #define TEMP_CAL_A        0x0021
53 #define TEMP_CAL_B        0x0022
54 #define TEMP_CAL_ALPHA    0x0023
55
56 // Temperature sensor opmode register values
57 #define TEMP_OPMODE_OM_RAW 0x0000
58 #define TEMP_OPMODE_OM_CALI 0x0001
59
60 #define TEMP_OPMODE_RES_5B 0x0000
61 #define TEMP_OPMODE_RES_6B 0x0002
62 #define TEMP_OPMODE_RES_7B 0x0004
63 #define TEMP_OPMODE_RES_8B 0x0006
64 #define TEMP_OPMODE_RES_9B 0x0008
65 #define TEMP_OPMODE_RES_10B 0x000A
66 #define TEMP_OPMODE_RES_11B 0x000C
67 #define TEMP_OPMODE_RES_12B 0x000E
68
69 #define TEMP_OPMODE_VCAL   0x0040
70
71 // Humidity sensor registers
72 #define HUMID_OPMODE       0x0020
73 #define HUMID_CAL_A0      0x0021
74 #define HUMID_CAL_A1      0x0022
75 #define HUMID_CAL_B0      0x0023
76 #define HUMID_CAL_B1      0x0024
77 #define HUMID_TEMP        0x0025
```

```
78 #define HUMID_INPUT_SEL      0x0026
79
80 // Humidity sensor opmode register values – only 1 value is supported
81 #define HUMID_OPMODE_VAL     0xEC07
82
83 // Humidity sensor input selection register values
84 #define HUMID_INPUT_SEL_1    0x0001
85 #define HUMID_INPUT_SEL_2    0x0002
86 #define HUMID_INPUT_SEL_3    0x0004
87 #define HUMID_INPUT_SEL_4    0x0008
88
89 // NVRAM calibration addresses
90 #define T_A                   0x0040
91 #define T_B                   0x0041
92 #define T_ALPHA               0x0042
93 #define RH_1_A0               0x0044
94 #define RH_1_A1               0x0045
95 #define RH_1_B0               0x0046
96 #define RH_1_B1               0x0047
97 #define RH_2_A0               0x0048
98 #define RH_2_A1               0x0049
99 #define RH_2_B0               0x004A
100 #define RH_2_B1               0x004B
101 #define RH_3_A0               0x004C
102 #define RH_3_A1               0x004D
103 #define RH_3_B0               0x004E
104 #define RH_3_B1               0x004F
105 #define RH_4_A0               0x0050
106 #define RH_4_A1               0x0051
107 #define RH_4_B0               0x0052
108 #define RH_4_B1               0x0053
109 #define AL_1_R_N              0x0054
110 #define AL_1_R_M              0x0055
111 #define AL_1_R_MIR           0x0056
112 #define AL_1_RB_N            0x0057
113 #define AL_1_RB_M            0x0058
114 #define AL_1_RB_MIR          0x0059
115 #define AL_2_R_N              0x005A
116 #define AL_2_R_M              0x005B
117 #define AL_2_R_MIR           0x005C
118 #define AL_2_RB_N            0x005D
119 #define AL_2_RB_M            0x005E
120 #define AL_2_RB_MIR          0x005F
121 #define AL_3_R_N              0x0060
122 #define AL_3_R_M              0x0061
123 #define AL_3_R_MIR           0x0062
124 #define AL_3_RB_N            0x0063
125 #define AL_3_RB_M            0x0064
126 #define AL_3_RB_MIR          0x0065
127 #define AL_4_R_N              0x0066
128 #define AL_4_R_M              0x0067
129 #define AL_4_R_MIR           0x0068
130 #define AL_4_RB_N            0x0069
131 #define AL_4_RB_M            0x006A
132 #define AL_4_RB_MIR          0x006B
133 #define AL_5_R_N              0x006C
134 #define AL_5_R_M              0x006D
```

```
135 #define AL_5_R_MIR          0x006E
136 #define AL_5_RB_N          0x006F
137 #define AL_5_RB_M          0x0070
138 #define AL_5_RB_MIR        0x0071
139 #define AL_6_R_N          0x0072
140 #define AL_6_R_M          0x0073
141 #define AL_6_R_MIR        0x0074
142 #define AL_6_RB_N          0x0075
143 #define AL_6_RB_M          0x0076
144 #define AL_6_RB_MIR        0x0077
145
146
147 #endif
```

Listing 2.20: mist_lowlevel.h

```
1 // Project: WICS
2 // Author:  Jeroen van Straten
3 // Date:    20120503
4 // Purpose: Low level access to MIST chip.
5
6 #ifndef mist_lowlevel_guard
7 #define mist_lowlevel_guard
8
9 #include <stdint.h>
10
11
12 // Initializes MIST chip I/O ports
13 extern void mist_ll_init(void);
14
15
16 /* SPI control */
17
18 // Enables and initializes the SPI peripheral.
19 extern void mist_spi_enable(void);
20
21 // Powers down the SPI peripheral.
22 extern void mist_spi_disable(void);
23
24 // Transfers a data word over the SPI bus.
25 extern uint16_t mist_spi_transfer(uint16_t data);
26
27 // Asserts /CSN (pulls it low).
28 extern void mist_spi_select(void);
29
30 // Releases /CSN (pulls it high).
31 extern void mist_spi_deselect(void);
32
33
34 /* Clock control */
35
36 // Enables and initializes the 1 MHz clock.
37 extern void mist_clock_enable(void);
38
39 // Powers down the 1 MHz clock.
40 extern void mist_clock_disable(void);
41
```

```

42
43 /* Reset control */
44
45 // Asserts /RESET (pulls it low).
46 extern void mist_reset_assert(void);
47
48 // Releases /RESET (pulls it high).
49 extern void mist_reset_release(void);
50
51
52 /* SINT */
53
54 // Returns the state of the SINT pin (nonzero if high, 0 if low)
55 extern uint8_t mist_get_sint(void);
56
57
58 /* DOUT */
59
60 // Returns the state of the DOUT pin (nonzero if high/stable, 0 if low)
61 extern uint8_t mist_is_reg_stable(void);
62
63
64 #endif

```

Listing 2.21: mist_lowlevel.c

```

1 #include "mist_lowlevel.h"
2
3 #include "LPC11xx.h"
4 #include <stdint.h>
5
6
7 void mist_ll_init() {
8     // CLK pin (pin 0.11)
9     LPC_IOCON->R_PI00_11 = 0xC1; // set GPIO, no pullup
10    LPC_GPIO0->DIR |= 0x800; // set pin as output
11
12    // /CS pin (pin 0.2)
13    LPC_IOCON->PI00_2 = 0xC0; // set GPIO, no pullup
14    LPC_GPIO0->DIR |= 0x004; // set output
15    LPC_GPIO0->DATA |= 0x004; // set default high
16
17    // MISO pin (pin 0.8)
18    LPC_IOCON->PI00_8 = 0xC1; // set MISO, no pullup
19
20    // MOSI pin (pin 0.9)
21    LPC_IOCON->PI00_9 = 0xC1; // set MOSI, no pullup
22    LPC_GPIO0->DIR |= 0x200; // set output
23
24    // SCLK pin (pin 2.11)
25    LPC_IOCON->SCK_LOC = 0x01; // set SCK on pin 2.11
26    LPC_IOCON->PI02_11 = 0xC1; // set SCLK, no pullup
27    LPC_GPIO2->DIR |= 0x800; // set output
28
29    // /RESET pin (pin 1.1)
30    LPC_IOCON->R_PI01_1 = 0xC1; // set GPIO, no pullup
31    LPC_GPIO1->DIR |= 0x002; // set output

```

```

32
33 // SINT pin (pin 1.0)
34 LPC_IOCON->R_PIO1_0 = 0xC1; // set GPIO, no pullup
35
36 // DOUT pin (pin 1.2)
37 LPC_IOCON->R_PIO1_2 = 0xC1; // set GPIO, no pullup
38 }
39
40
41 void mist_spi_enable() {
42 // enable SPI clock (set to 1 MHz input clock)
43 LPC_SYSCON->SYSAHBCLKCTRL |= 0x800;
44 LPC_SYSCON->SSPOCLKDIV = 12;
45
46 // release SPI reset
47 LPC_SYSCON->PRESETCTRL |= 0x01;
48
49 // setup SPI
50 LPC_SSP0->CR0 = 0x000F; // 16 bit, SPI mode 0, no further clock division
51 LPC_SSP0->CR1 = 0x0002; // enable SPI
52 LPC_SSP0->CPSR = 0x0010; // set prescaler to lowest acceptable value
53
54 }
55
56 void mist_spi_disable() {
57 // assert SPI reset
58 LPC_SYSCON->PRESETCTRL |= 0x01;
59
60 // disable SPI clock
61 LPC_SYSCON->SYSAHBCLKCTRL &= ~0x800;
62 }
63
64 uint16_t mist_spi_transfer(uint16_t data) {
65 // ensure the SPI is turned on (otherwise this will block forever)
66 if (!(LPC_SSP0->CR1 & 0x0002)) {
67     return 0;
68 }
69
70 // start the transfer
71 LPC_SSP0->DR = data;
72
73 // wait for the SPI to finish the transfer (wait for busy and RX empty
74 // to be released)
75 while ((LPC_SSP0->SR & 0x10) && !(LPC_SSP0->SR & 0x04));
76
77 // return received data
78 return LPC_SSP0->DR;
79 }
80
81 extern void mist_spi_select() {
82 // Set /CS pin low
83 LPC_GPIO0->DATA &= ~0x004;
84 }
85
86 extern void mist_spi_deselect() {
87 // Set /CS pin high
88 LPC_GPIO0->DATA |= 0x004;

```

```
89 }
90
91
92 void mist_clock_enable() {
93     // enable sensor clock timer
94     LPC_SYSCON->SYSAHBCLKCTRL |= 0x200;
95
96     // reset timer before setup
97     LPC_TMR32B0->TCR = 0x02;
98
99     // setup sensor clock timer
100    LPC_TMR32B0->PR = 0; // no prescaler
101    LPC_TMR32B0->MR0 = 11; // count to 12 for 1 MHz
102    LPC_TMR32B0->MR3 = 6; // 50% duty cycle
103    LPC_TMR32B0->MCR = 0x02; // reset on MR0
104    LPC_TMR32B0->PWMC = 0x08; // enable PWM for MR3
105
106    // sensor clock pin
107    LPC_IOCON->R_PIO0_11 = 0xC3; // select timer output
108
109    // start sensor clock timer
110    LPC_TMR32B0->TCR = 0x01;
111 }
112
113 void mist_clock_disable() {
114     // disable sensor clock timer
115     LPC_SYSCON->SYSAHBCLKCTRL &= ~0x200;
116
117     // set pin to GPIO mode to ensure the output is defined
118     LPC_IOCON->R_PIO0_11 = 0xC0;
119 }
120
121
122 void mist_reset_assert() {
123     // assert reset (pull it low)
124     LPC_GPIO1->DATA &= ~0x002;
125 }
126
127 void mist_reset_release() {
128     // release reset (pull it high)
129     LPC_GPIO1->DATA |= 0x002;
130 }
131
132
133 uint8_t mist_get_sint() {
134     return LPC_GPIO1->DATA & 0x001;
135 }
136
137
138 uint8_t mist_is_reg_stable() {
139     return LPC_GPIO1->DATA & 0x004;
140 }
```

2-10 Main loop

Listing 2.22: main.c

```

1  /*
2  =====
3  Name       : main.c
4  Author     : Ingmar Jager & Jan Angevare
5  Version    : 1.0.0
6  Copyright  : $(copyright)
7  Description : main definition
8  =====
9  */
10
11 #include "LPC11xx.h"
12 #include "ZigBee.h"
13 #include "mist.h"
14 #include "power_modes.h"
15
16 __packed struct WICS_data {
17     char SequenceID;
18     char MeasurementInterval;
19     __packed struct measurement Measurements[10];
20 };
21
22 int associate(void);
23 void wait_for_associated(void);
24
25 void SystemInit(void) {
26     int i;
27
28     // Set running mode for PDRUNCFG and SYSAHBCLKCTRL
29     LPC_SYSCON->PDRUNCFG = 0xED88;
30     LPC_SYSCON->SYSAHBCLKCTRL = 0x1305F;
31
32     // Switch to XTAL
33     for (i = 0; i < 200; i++) __NOP();
34
35     LPC_SYSCON->SYSPLLCLKSEL = 0x01;
36     LPC_SYSCON->SYSPLLCLKUEN = 0x00;
37     LPC_SYSCON->SYSPLLCLKUEN = 0x01;
38
39     LPC_SYSCON->MAINCLKSEL = 0x01;
40     LPC_SYSCON->MAINCLKUEN = 0x00;
41     LPC_SYSCON->MAINCLKUEN = 0x01;
42
43     // Disable IRC
44     LPC_SYSCON->PDRUNCFG |= 0x003;
45
46     // Set complete pin config here
47     LPC_IOCON->RESET_PIO0_0 = 0x0C0;
48     LPC_IOCON->PIO0_1      = 0x0D0;
49     LPC_IOCON->PIO0_2      = 0x0C1;
50     LPC_IOCON->PIO0_3      = 0x0C0;
51     LPC_IOCON->PIO0_4      = 0x1C0;
52
53

```



```

54  LPC_IOPCON->PIO0_5      = 0x1C0;
55  LPC_IOPCON->PIO0_6      = 0x0C0;
56  LPC_IOPCON->PIO0_7      = 0x0C1;
57  LPC_IOPCON->PIO0_8      = 0x0C1;
58  LPC_IOPCON->PIO0_9      = 0x0C1;
59  LPC_IOPCON->SWCLK_PIO0_10 = 0x0C0;
60  LPC_IOPCON->R_PIO0_11   = 0x0C3;
61
62  LPC_IOPCON->R_PIO1_0     = 0x0C1;
63  LPC_IOPCON->R_PIO1_1     = 0x0C1;
64  LPC_IOPCON->R_PIO1_2     = 0x0C1;
65  LPC_IOPCON->SWDIO_PIO1_3 = 0x0C0;
66  LPC_IOPCON->PIO1_4      = 0x041;
67  LPC_IOPCON->PIO1_5      = 0x0C1;
68  LPC_IOPCON->PIO1_6      = 0x0C1;
69  LPC_IOPCON->PIO1_7      = 0x0C1;
70  LPC_IOPCON->PIO1_8      = 0x0C0;
71  LPC_IOPCON->PIO1_9      = 0x0C0;
72  LPC_IOPCON->PIO1_10     = 0x0C0;
73  LPC_IOPCON->PIO1_11     = 0x041;
74
75  LPC_IOPCON->PIO2_0      = 0x0C0;
76  LPC_IOPCON->PIO2_1      = 0x0C0;
77  LPC_IOPCON->PIO2_2      = 0x0C0;
78  LPC_IOPCON->PIO2_3      = 0x0C0;
79  LPC_IOPCON->PIO2_4      = 0x0C0;
80  LPC_IOPCON->PIO2_5      = 0x0C0;
81  LPC_IOPCON->PIO2_6      = 0x0C0;
82  LPC_IOPCON->PIO2_7      = 0x0C0;
83  LPC_IOPCON->PIO2_8      = 0x0C0;
84  LPC_IOPCON->PIO2_9      = 0x0C0;
85  LPC_IOPCON->PIO2_10     = 0x0C0;
86  LPC_IOPCON->PIO2_11     = 0x0C1;
87
88  LPC_IOPCON->PIO3_0      = 0x0C0;
89  LPC_IOPCON->PIO3_1      = 0x0C0;
90  LPC_IOPCON->PIO3_2      = 0x0C0;
91  LPC_IOPCON->PIO3_3      = 0x0C0;
92  LPC_IOPCON->PIO3_4      = 0x0C0;
93  LPC_IOPCON->PIO3_5      = 0x0C0;
94
95  LPC_GPI00->DIR          = 0xA3C;
96  LPC_GPI00->DATA        = 0x004;
97
98  LPC_GPI01->DIR          = 0x7A2;
99  LPC_GPI01->DATA        = 0x0A0;
100
101  LPC_GPI02->DIR          = 0xFFF;
102  LPC_GPI02->DATA        = 0x000;
103
104  LPC_GPI03->DIR          = 0x03F;
105  LPC_GPI03->DATA        = 0x000;
106 }
107
108 int associate() {
109     if (ZigBee_get_state()) {
110         ZigBee_request_connection_status();

```

```

111     power_modes_sleep(10000, 1);
112
113     while (ZigBee_get_state())
114         if (!power_modes_sleep(0, 1))
115             break;
116
117 }
118
119 return !ZigBee_get_state();
120 }
121
122 void wait_for_associated() {
123     if (ZigBee_get_state()) {
124         ZigBee_wake_up();
125
126         while (!associate()) {
127             // Association is taking way too long, the coordinator is probably off.
128             // Enter deep sleep for five minutes to save power while waiting for it
129             // to turn on.
130             ZigBee_set_sleep();
131             //power_modes_deep_sleep(300);
132             ZigBee_wake_up();
133         }
134     }
135 }
136
137 int main(void) {
138     // Buffer handle
139     char* data;
140     int size;
141
142     // Header data + sample count
143     struct WICS_data measure_data;
144     int i = 0;
145     int sample_amount = 1;
146
147     // Initialize
148     LPC_GPIO3->DATA |= 0x008;
149     LPC_GPIO2->DATA &= ~0x040;
150     ZigBee_init();
151     initializeSensor();
152     power_modes_init();
153
154     /* vOOR MEETING*/
155     //LPC_GPIO2->DATA |= 0x040;
156     //while(1) power_modes_deep_sleep(300);
157     /**/
158
159     measure_data.MeasurementInterval = 1;
160     measure_data.SequenceID = 0;
161
162     power_modes_sleep(10, 0);
163
164     i = 0;
165
166     // Enter main-loop
167     while (1) {

```

```

168
169 // Enter out of order Loop (measurments are taken while ZigBee sends)
170 while (1) {
171     // Get measurement, this takes a while
172     getMeasurement(&measure_data.Measurements[i]);
173
174     // Ensure that we are still associated
175     wait_for_associated();
176
177     // Wait till time has transpired, this takes even longer
178     ZigBee_set_sleep();
179     power_modes_deep_sleep(measure_data.MeasurementInterval);
180
181     // Check for new messages, if so reconfigure everything
182     // Send the data we already got and reset count
183     ZigBee_wake_up();
184     if (ZigBee_check_for_new_message(&data, &size)) {
185
186         // Send everything we've got
187         //ZigBee_send((char*)&measure_data, 2 + 7 * (i + 1));
188
189         ZigBee_send(data, size);
190
191         // Reset count
192         i = 0;
193         measure_data.SequenceID++;
194
195         // Reconfiguring
196         //measure_data.MeasurementInterval = data[0];
197         //sample_amount = (((data[1]>0)? data[1]: 1)<11)?data[1]:10;
198
199         // Release buffer
200         ZigBee_done_reading_new_message();
201
202         // Only if no message was received will we update the count
203         // and on enough samples send, otherwise the count will be
204         // reset and no measurements will be in the buffer
205     } else {
206         i = (i + 1) % sample_amount;
207         if (i == 0) {
208             ZigBee_send((char*)&measure_data, 2 + 7 * sample_amount);
209             measure_data.SequenceID++;
210             } else {
211                 power_modes_sleep(3, 0);
212                 ZigBee_set_sleep();
213             }
214     }
215 }
216
217 //Enter in order Loop (first measurement then send)
218 while (1) {
219     // Get measurement, this takes a while
220     getMeasurement(&measure_data.Measurements[i]);
221
222     // Check for new messages, if so reconfigure everything
223     // Send the data we already got and reset count
224     ZigBee_wake_up();

```

```
225     if (ZigBee_check_for_new_message(&data, &size)) {
226
227         // Send everything we've got
228         //ZigBee_send((char*)&measure_data, 2 + 7 * (i + 1));
229         ZigBee_send(data, size);
230
231         // Reset count
232         i = 0;
233         measure_data.SequenceID++;
234
235         // Reconfiguring
236         measure_data.MeasurementInterval = data[0];
237         sample_amount = (((data[1]>0)? data[1]: 1)<11)?data[1]:10;
238
239         // Release buffer
240         ZigBee_done_reading_new_message();
241
242         // Only if no message was received will we update the count
243         // and on enough samples send, otherwise the count will be
244         // reset and no measurements will be in the buffer
245     } else {
246         i = (i + 1) % sample_amount;
247         measure_data.SequenceID++;
248         if (i == 0) ZigBee_send((char*)&measure_data, 2 + 7 * sample_amount);
249     }
250 }
251
252 }
253
254 // Without this stuff won't compile
255 //return 0 ;
256 }
```