3e tunneling processes in a superconducting single-electron tunneling transistor

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A current due to a tunneling event that involves three times the charge of an electron was observed in the current-voltage characteristics of a superconducting single-electron tunneling transistor. In this tunnel event, a Cooper pair tunnels through one tunnel barrier simultaneously with a quasiparticle that tunnels through a second tunnel barrier which is about 0.5 μm distant from the first tunnel barrier. This current was observed in a bias regime where current flow due to sequential quasiparticle tunneling is forbidden due to the Coulomb blockade. [S0163-1829/98/01347-2]

A superconducting single-electron tunneling (SET) transistor consists of a small superconducting island that is coupled to three leads, a gate and two output leads.1 The two output leads are connected to the island by tunnel junctions and the gate is capacitively coupled to the island. The quantum nature of this device is manifested in the periodic modulation of the current that flows through the output leads as the charge on the gate is varied. One modulation period corresponds to adding one electron charge on the gate. By monitoring the current, one can make very sensitive measurements of the charge at the gate. The charge sensitivity of a SET transistor in the normal state, which makes the superconducting SET transistor the most sensitive device now available for measuring charge.2,3

The characteristics of a superconducting SET transistor depend on the relative magnitudes of three energies: the charging energy \( E_C \), the Josephson energy \( E_J \), and the superconducting gap \( \Delta \).\(^4\) The charging energy is the energy associated with charging the island with a single electron charge, \( E_C = e^2/(2C_S) \). Here \( C_S \) is the total capacitance of the island. The Josephson energy is related to the junction critical current \( I_c \), \( E_J = \hbar I_c/2e \), and the superconducting gap can be seen as the addition energy that is required for a superconducting island to have an odd number of electrons rather than an even number of electrons.\(^5\) In devices with large junctions, the Josephson energy is much larger than the charging energy, \( E_J \gg E_C \), and a supercurrent is observed. As the junctions are made smaller, \( E_J \) decreases while \( E_C \) increases. When \( E_J \approx E_C \), the supercurrent can be modulated by applying a voltage to the gate, while for \( E_J \ll E_C \) the supercurrent is suppressed. If \( \Delta > E_C > E_J \), parity effects are observed.\(^6\) It is then possible to determine if the number of electrons on the island is an odd or even number. In the present experiment \( \Delta \approx E_C \gg E_J \) and no supercurrent was observed.

The SET transistor studied consisted of two Al/AlO\(_x\)/Al tunnel junctions that were fabricated by shadow evaporation. The two junction capacitances were \( C_1 = 1.78 \times 10^{-16} \) F and \( C_2 = 2.10 \times 10^{-16} \) F, the gate capacitance was \( C_g = 1.07 \times 10^{-18} \) F, the total resistance of the device was \( R_1 + R_2 = 1.7 \times 10^6 \) Ω, the superconducting gap was \( \Delta = 203 \) μeV, and the charging energy was \( E_C = 206 \) μeV. Under normal operating conditions, the current that flows through a superconducting SET transistor is primarily due to the sequential tunneling of normal quasiparticles. However, at low bias voltages, the tunneling of individual quasiparticles is suppressed by a combination of the Coulomb blockade and the absence of states in the superconducting gap. At these low bias voltages, other transport mechanisms can be observed such as cotunneling,\(^7\) the Josephson-quasiparticle cycle,\(^8,9\) Andreev reflection,\(^10\) the resonant tunneling of Cooper pairs,\(^11\) and singularity matching.\(^12\) Here we report the experimental observation of a current that flows due to the simultaneous tunneling of a Cooper pair and a quasiparticle. The Cooper pair and the quasiparticle simultaneously tunnel through two different tunnel barriers that are spaced about 0.5 μm from each other.

The thresholds for the various tunnel events that occur in a SET transistor can be determined by examining the electrostatic energy of the circuit. To calculate the change in electrostatic energy when an electron tunnels, one can treat the circuit as a network of capacitors. It is convenient to also treat the voltage sources as capacitors with very large capacitances. At the end of the calculation the limit of very large capacitance for the voltage sources is taken. Figure 1 shows the equivalent capacitor network for an asymmetrically biased SET transistor. The electrostatic energy of this network of capacitors is the sum of the electrostatic energies of the capacitors,

\[
E = \frac{1}{2} C_1 V_b^2 + \frac{1}{2} C_2 (V - V_b)^2 + \frac{1}{2} C_g (V - V_g)^2 + \frac{1}{2} C_b V_g^2 \\
+ \frac{1}{2} C_b V_g^2.
\]

FIG. 1. The equivalent capacitor network used to calculate the electrostatic energy of a SET transistor.
Taking the derivatives of the electrostatic energy with respect to the three voltages \((V, V_b, V_g)\) yields a set of three coupled equations which can be written in the form

\[
edE/\partial V_i = \sum C_{ij} q_j.
\]

Here \(q_j\) are the charges on the islands and \(C_{ij}\) is the capacitance matrix. The electrostatic energy of the circuit can then be written as

\[
E = \frac{1}{2} \sum C_{ij} q_i q_j.
\]

This form was used to calculate the charge in electrostatic energy when charge tunneled. Figure 2 illustrates the tunnel events that were considered. Each arrow indicates that a charge of \(e\) has passed through that tunnel junction. In the limit \(C_b \gg C_1, C_2, C_g\), the changes in the electrostatic energies are

\[
edE = \frac{e}{C_S} \left[ \frac{1}{2} - ne - q_0 C_2 V_b - C_g V_g \right],
\]

\[
edE = \frac{e}{C_S} \left[ \frac{1}{2} + ne + q_0 - (C_1 + C_g) V_b + C_g V_g \right],
\]

\[
edE = \frac{2e}{C_S} [e - ne - q_0 - C_2 V_b - C_g V_g],
\]

\[
edE = \frac{2e}{C_S} [e + ne + q_0 - (C_1 + C_g) V_b + C_g V_g],
\]

\[
edE = \frac{e}{C_S} \left[ \frac{1}{2} - ne - q_0 - (C_1 + 2C_2 + C_g) V_b - C_g V_g \right],
\]

\[
edE = \frac{e}{C_S} \left[ \frac{1}{2} + ne + q_0 - (2C_1 + C_2 + 2C_g) V_b + C_g V_g \right].
\]

Equations (2a)–(2f) correspond to the tunnel event illustrated in Figs. 2(a)–2(f). The changes in electrostatic energy can be used to construct a stability diagram for the superconducting SET transistor as shown in Fig. 3. Each line in Fig. 3 represents the threshold for a certain tunnel process. The position of the threshold is dependent on the number of electrons on the island \(n\). This results in a periodic stability diagram with a periodicity \(e\). The lines which are determined by the tunneling of charge only through junction 1 [Figs. 2(a) and 2(c)] have a slope of \(-C_g/C_2\). The lines which are determined by the tunneling of charge only through junction 2 [Figs. 2(b) and 2(d)] have a slope of \(-C_g/(C_1 + C_g)\). The threshold determined by the tunneling of \(3e\) of charge as shown in Fig. 2(e) has a slope of \(-C_g/(C_1 + 2C_2 + C_g)\) and the slope of the threshold determined by the tunnel process shown in Fig. 2(f) is \(C_g/(2C_1 + C_2 + 2C_g)\).

Figure 4(a) shows the measured current through the superconducting SET transistor as a function of the bias voltage and the gate voltage. The logarithm of the current was taken so that the high bias data and low bias data could be presented in the same figure. Figure 4(b) shows the derivative of the same data. The current is periodic in gate voltage with a periodicity of \(e/C_g\). The inverted white triangles at the top of Fig. 4(a) form the threshold for sequential quasiparticle tunneling through the SET transistor. In this process, a single quasiparticle tunnels onto the island through one junction and then another quasiparticle tunnels out through the other junction. These tunnel processes are shown in Fig. 2(a) (\(\delta E = -2\Delta\)) and Fig. 2(b) (\(\delta E = -2\Delta\)). The minimum bias voltage for the threshold for sequential quasiparticle tunneling is \(4\Delta/e\) and the maximum is \(4\Delta/e + e/C_S\). Here \(C_S\) is the total capacitance, \(C_S = C_1 + C_2 + C_g\). The change in electrostatic energy when a quasiparticle tunnels must be \(\delta E = -2\Delta\) because there are no quasiparticle states within the superconducting gap.

Also clearly visible in Fig. 4(a) are intersecting ridges of current that are due to the Josephson-quasiparticle (JQP) cycle. These are the white X’s centered at about 0.8 mV. This transport mechanism can occur when the bias voltages are such that a Cooper pair can be transported through one of the junctions without changing the total energy of the system. There are then two degenerate charge states which are...
coupled by the Josephson energy $E_J$. This results in a mixing of the charge states and the probability of the Cooper pair being on either side of the junction oscillates with a frequency $E_J/h = I_c/(2e)$. These oscillations produce no net current, however the oscillations can be interrupted by the tunneling of a quasiparticle through the other junction. The result of this interruption is that a Cooper pair is transported through one of the junctions while a quasiparticle is transported through the other junction. The charge of the island decreases by $e$ via the tunnel event shown in Fig. 2(b) with $\delta E = -2\Delta$. Then the island returns to its initial charge state via the tunnel event in Fig. 2(f) with $\delta E = -2\Delta$. The minimum bias voltage for this threshold is $4\Delta/(3e)$ and the maximum bias voltage for this threshold for this process is $4\Delta/(3e) + e/(3C_S)$. A similar simultaneous $3e$ tunneling threshold should also occur for SET transistors in the normal state [Fig. 2(e) and Fig. 2(f), $\delta E = 0$]. However in that case three particles would have to tunnel simultaneously so the rate would be much lower.

The tunneling of $3e$ of charge also forms part of a sequence of tunnel events that is responsible for the current observed in the diamond-shaped regions that extend from a bias voltage of about 0.4 mV to 1.2 mV. In this region, first $3e$ of charge tunnels as in Fig. 2(e) or Fig. 2(f) with $\delta E = -2\Delta$. Then the charge of the island returns to its initial state by the tunneling of a quasiparticle as in Fig. 2(b) or Fig. 2(a) with $\delta E = -2\Delta$.

At bias voltages between 0.2 mV and 0.4 mV a small current that is $2e$ periodic is observed. This current arises from the sequential tunneling of a quasiparticle and the tunneling of $3e$ of charge as described above. If the initial state of the island is odd, then a quasiparticle can tunnel on or off the island in the tunnel processes illustrated in Fig. 2(a) or Fig. 2(b) with $\delta E = 0$. In this tunnel process, the quasiparticle that tunnels pairs with the odd quasiparticle on the island. The island then returns to its initial charge state via a Cooper pair-quasiparticle cotunneling event [Fig. 2(e) or Fig. 2(f), $\delta E = -2\Delta$]. A similar process cannot occur if the initial state of the island is even since the quasiparticle that tunnels from the lead has no partner to condense with to form a Cooper pair. Consequently, this current is $2e$ periodic.
In summary, the thresholds for a number of distinct charge-transport mechanisms were observed in the current-voltage characteristics of a superconducting SET transistor. These cycles involve the sequential tunneling of quasiparticles, the sequential tunneling of Cooper pairs and quasiparticles (JQP cycles), cotunneling of quasiparticles, and the sequential cotunneling of Cooper pairs and quasiparticles with the tunneling of quasiparticles. Of particular interest are the currents that arise from cycles which include cotunneling of a Cooper pair and a quasiparticle. In this tunnel process, a charge of 3e tunnels and the Cooper pair and quasiparticle are transported simultaneously through two different tunnel barriers. Cotunneling of a Cooper pair and a quasiparticle also plays a role in a sequence of tunnel events that leads to a 2e periodic current at low bias voltages.

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